

# Driving BMDFM Drives Driven BMDFM

## CommChannel

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```
#!/bin/sh

# CommChannel

# In this example, we run two instances of BMDFM: a Driving BMDFM and a Driven
# BMDFM.
# The Driving BMDFM is fed with parallel cluster flows by a single BMDFMldr
# instance. The Driven BMDFM is fed with *multiple* parallel cluster flows by
# the Driving BMDFM instance achieving an even higher level of parallelization
# (see "Offload Extension" below):
#
# BMDFM Classic:
#
# BMDFMldr (Von-Neumann Arch.) Parallel Cluster Flow BMDFMsrv (Dataflow Arch.)
#
# Offload Extension (Driving BMDFM Drives Driven BMDFM):
#
# BMDFMldr (Von-Neumann Arch.) Parallel Cluster Flow BMDFMsrv (Dataflow Arch.)
#
# BMDFMsrv (Dataflow Arch.) Parallel Cluster Flow BMDFMsrv (Dataflow Arch.)
#
# *multiple*
#
# Parallel Cluster Flow
#
# Driving Flow Driven
#
# *****
# IMPLEMENTATION:
#
# 0. DRIVING SENDS SHELL COMMANDS TO PIPE BMDFmpipeDriving2Driven<i>
# "echo args | fastlisp -q exec.flx"
# "echo args | BMDFMldr -q exec.flz"
# 1. SHELL EXECUTES COMMANDS
# "echo args | fastlisp -q exec.flx >BMDFmpipeDriven2Driving<i>"
# "echo args | BMDFMldr -q exec.flz >BMDFmpipeDriven2Driving<i>"
# 2. DRIVING RECEIVES RESULTS FROM DRIVEN
#
# *****
#
# Driving fastlisp Driven fastlisp
#
# CommChannel 0
#
# BMDFmpipeDriving2Driven<0>
# BMDFmpipeDriven2Driving<0>
#
# Driving fastlisp (Single instance)
#
# fastlisp exec_master.flp
#
# Driven fastlisp (Single instance)
#
# echo args | fastlisp -q exec.flx >BMDFmpipeDriven2Driving<0>
#
# *****
#
# Driving BMDFMsrv (N_CPUPROC = <N>) Driven BMDFMsrv (N_IOREBP > <N>)
#
# CommChannel 0
#
# CPUPROC 0 BMDFmpipeDriving2Driven<0> Socket j
# BMDFmpipeDriven2Driving<0>
#
# CommChannel 1
#
# CPUPROC 1 BMDFmpipeDriving2Driven<1> Socket k
# BMDFmpipeDriven2Driving<1>
#
# CommChannel i
#
# CPUPROC i BMDFmpipeDriving2Driven<i> Socket l
# BMDFmpipeDriven2Driving<i>
#
# Socket m
```

Driving BMDFM Drives Driven BMDFM

```
#
# Driving BMDFMldr
# (Single instance)
#
# BMDFMldr exec_master.flp
#
#
# Driven BMDFMldr
# (Multiple dynamically-created instances)
#
# echo args | BMDFMldr -q exec.flz >BMDFmpipeDriven2Driving<0>
# echo args | BMDFMldr -q exec.flz >BMDFmpipeDriven2Driving<1>
# echo args | BMDFMldr -q exec.flz >BMDFmpipeDriven2Driving<i>
#
# *****
#
# ChannelNumber for BMDFmpipeDriving2Driven<i> and BMDFmpipeDriven2Driving<i>
# if [[ "$1" == "" ]]; then
#   echo "Usage: $0 <ChannelNumber>"
#   exit
# fi
# chan_num=$1
#
# Driving BMDFM instance is by default. Driven BMDFM instance is different.
# export BM_DFM_CONNECTION_FILE_path="./.BMDFMsrv"
# export BM_DFM_CONNECTION_NPIP_path="./.BMDFMsrv_npip"
#
# rm -rf BMDFmpipeDriving2Driven$chan_num
# mknod BMDFmpipeDriving2Driven$chan_num p
#
# rm -rf BMDFmpipeDriven2Driving$chan_num
# mknod BMDFmpipeDriven2Driving$chan_num p
#
# while true; do
#   read cmd_line <BMDFmpipeDriving2Driven$chan_num;
#   echo $chan_num: $cmd_line;
#   sh -c "$cmd_line" >BMDFmpipeDriven2Driving$chan_num;
# done
#
# <EOF>

fastlisp.cfg

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# fastlisp.cfg

#####
##
## Configuration Profile ##
##
## Global FastLisp Function Definitions ##
##
#####

# Format is: <(DEFUN ...)>{ <(DEFUN ...)>} # <EOF>

(defun format_list_4print
  (progn
    (alsetq list $1)
    (setq howmany_in_list (ival (index list 0)))
    (setq form_string "")
    (for i 1 1 howmany_in_list (progn
      (setq member (index list i))
      (if (> (indices member) 1)
        (setq form_string (cat form_string (format_list_4print member)))
        (if (= (type member) "S")
          (progn
            (setq form_string (cat form_string "\""))
            (setq form_string (cat form_string (str_unraw member)))
            (setq form_string (cat form_string "\""))
          )
        (setq form_string (cat form_string member))
      )
    )
    (setq form_string (cat form_string " "))
  )
)

(defun fastlisp
  (progn
    (alsetq ran (mapcar $1))
    (setq output (index ran 0))
    (if (index ran 2)
      (cat output (cat (str_fmt "[Syntax error %ld]: " (index ran 2)) (index ran 3)))
      (if (index ran 4)
        (cat output (cat (str_fmt "[Run-time error %ld]: " (index ran 4)) (index ran 5)))
        (progn
          (alsetq res (index ran 1))
          (if (> (indices res) 1)
            (cat output (format_list_4print res))
            (cat output (cat " " res))
          )
        )
      )
    )
  )
)

(defun bmdfm2bmdfm_offload_ (progn
#
# 0. DRIVING SENDS SHELL COMMANDS TO PIPE BMDFmpipeDriving2Driven<i>
# "echo args | fastlisp -q exec.flx"
# "echo args | BMDFMldr -q exec.flz"
# 1. SHELL EXECUTES COMMANDS
# "echo args | fastlisp -q exec.flx >BMDFmpipeDriven2Driving<i>"
# "echo args | BMDFMldr -q exec.flz >BMDFmpipeDriven2Driving<i>"
# 2. DRIVING RECEIVES RESULTS FROM DRIVEN
#
# (setq exec (cat " " $1)) # for "echo args | fastlisp -q exec.flx"
```



```

)

# main() begins here
(setq n (ival (accept ""))) # reads from stdin
(Fibonacci n)

Fibonacci_master.flp

~~~~~

# Fibonacci_master.flp
# Recursive Fibonacci calculation.
# FastLisp program example by Sancho Mining.

#/*
# 0. DRIVING SENDS SHELL COMMANDS TO PIPE BMDFMpipeDriving2Driven<i>
#   "echo args | fastlisp -q exec.flx"
#   "echo args | BMDFMldr -q exec.flz"
# 1. SHELL EXECUTES COMMANDS
#   "echo args | fastlisp -q exec.flx >BMDFMpipeDriven2Driving<i>"
#   "echo args | BMDFMldr -q exec.flz >BMDFMpipeDriven2Driving<i>"
# 2. DRIVING RECEIVES RESULTS FROM DRIVEN
#*/

void bmdfm2bmdfm_offload(const ULO *dat_ptr, struct fastlisp_data *ret_dat){
# CHR *exec=NULL,*args=NULL; /* "echo args | fastlisp -q exec.flx" */
#                               /* "echo args | BMDFMldr -q exec.flz" */
#
# CHR *temp=NULL,*templ=NULL;
# CHR *Driving2Driven_pipe_name=NULL,*Driven2Driving_pipe_name=NULL,
#     *result=NULL;
#
# int descr,Driving2Driven_pipe_descr,Driven2Driving_pipe_descr;
# ret_dat->disable_ptr=1;
# ret_sval(dat_ptr,&exec);
# ret_sval(dat_ptr+1,&args);
# if(noterror()){
#   cat(&exec,get_std_buff(&temp,am_I_in_the_fastlisp_module()?)
#     ".flx":".flz");
#   if(!==(descr=open(exec,O_RDONLY))){
#     get_std_buff(&temp,"*** Error while opening the ~");
#     cat(&temp,exec);
#     cat(&temp,get_std_buff(&templ," file!"));
#     rise_error_info(1,temp);
#   }
#   else{
#     close(descr);
#     locat(&exec,get_std_buff(&temp,am_I_in_the_fastlisp_module()?)
#       "fastlisp -q ":"BMDFMldr -q ");
#     get_std_buff(&Driving2Driven_pipe_name,"BMDFMpipeDriving2Driven");
#     cat(&Driving2Driven_pipe_name,eq_num(&temp,get_id_cpuproc()));
#     get_std_buff(&Driven2Driving_pipe_name,"BMDFMpipeDriven2Driving");
#     cat(&Driven2Driving_pipe_name,temp);
#     if(!==(Driving2Driven_pipe_descr=open(Driving2Driven_pipe_name,
#       O_RDWR))){
#       get_std_buff(&temp,"*** Error while opening the ~");
#       cat(&temp,Driving2Driven_pipe_name);
#       cat(&temp,get_std_buff(&templ," pipe!"));
#       rise_error_info(1,temp);
#     }
#     else
#     if(!==(Driven2Driving_pipe_descr=open(Driven2Driving_pipe_name,
#       O_RDWR))){
#       close(Driving2Driven_pipe_descr);
#       get_std_buff(&temp,"*** Error while opening the ~");
#       cat(&temp,Driven2Driving_pipe_name);
#       cat(&temp,get_std_buff(&templ," pipe!"));
#       rise_error_info(1,temp);
#     }
#     else{
#       get_std_buff(&temp,"echo ");
#       cat(&temp,args);
#       cat(&temp,get_std_buff(&templ," | "));
#       cat(&temp,exec);
#       cat(&temp,sch2str(&templ,'\n'));
#       write(Driving2Driven_pipe_descr,(void*)temp,len(temp));
#       space(&result,0);
#       while(!len(result))
#         while(l=read(Driven2Driving_pipe_descr,(void*)templ,1)){
#           if(*templ=='\n')
#             break;
#           cat(&result,templ);
#         }
#       close(Driving2Driven_pipe_descr);
#       close(Driven2Driving_pipe_descr);
#     }
#   }
#   ret_dat->single=1;
#   ret_dat->stype='S';
#   equ(&ret_dat->svalue,result);
#   free_string(&exec);
#   free_string(&args);
#   free_string(&Driving2Driven_pipe_name);
#   free_string(&Driven2Driving_pipe_name);
#   free_string(&result);
#   free_string(&temp);
#   free_string(&templ);
# }
# return;
# }

# INSTRUCTION_STRU INSTRUCTION_SET[]={
# {"BMDFM2BMDFM_OFFLOAD",2,'S',(UCH*)"SS",&bmdfm2bmdfm_offload}
# };

#(defun bmdfm2bmdfm_offload (progn # to be placed into .cfg
##
## 0. DRIVING SENDS SHELL COMMANDS TO PIPE BMDFMpipeDriving2Driven<i>
##   "echo args | fastlisp -q exec.flx"
##   "echo args | BMDFMldr -q exec.flz"
## 1. SHELL EXECUTES COMMANDS
##   "echo args | fastlisp -q exec.flx >BMDFMpipeDriven2Driving<i>"
##   "echo args | BMDFMldr -q exec.flz >BMDFMpipeDriven2Driving<i>"
## 2. DRIVING RECEIVES RESULTS FROM DRIVEN
##
# (setq exec (cat "" $1)) # for "echo args | fastlisp -q exec.flx"
# (setq args (cat "" $2)) # for "echo args | BMDFMldr -q exec.flz"
#
# (setq flp (cat " " (setq exec (cat "\" \"\" (cat exec "\""))

```

```

# (setq flp (cat flp " (setq args (cat "\" \"\"
# (setq flp (cat flp " (cat args "\""))
# ))))
#
# (setq flp (cat flp " (setq exec (if (am_i_in_the_fastlisp_module)
# (setq flp (cat flp " (cat exec "\".flx\"")
# (setq flp (cat flp " (cat exec "\".flz\"")
# (setq flp (cat flp " ))
# (setq flp (cat flp " (if (== (setq descr (file_open exec) -1)
# (setq flp (cat flp " (rise_error_info 1
# (setq flp (cat flp " (cat \"\"*** Error while opening the \"\"
# (setq flp (cat flp " (cat exec \"' file!\")
# (setq flp (cat flp " nil
# (setq flp (cat flp " )
# (setq flp (cat flp " (file_close descr)
# (setq flp (cat flp " (setq exec (if (am_i_in_the_fastlisp_module)
# (setq flp (cat flp " (cat \"fastlisp -q \" exec)
# (setq flp (cat flp " (cat \"BMDFMldr -q \" exec)
# (setq flp (cat flp " ))
# ))))
#
# (setq flp (cat flp " (setq Driving2Driven_pipe_name
# (setq flp (cat flp " (cat \"BMDFMpipeDriving2Driven\" (id_cpuproc))
# (setq flp (cat flp " (setq Driven2Driving_pipe_name
# (setq flp (cat flp " (cat \"BMDFMpipeDriven2Driving\" (id_cpuproc))
# ))))
#
# (setq flp (cat flp " (if (== (setq Driving2Driven_pipe_descr
# (setq flp (cat flp " (file_open Driving2Driven_pipe_name) -1)
# (setq flp (cat flp " (rise_error_info 1
# (setq flp (cat flp " (cat \"\"*** Error while opening the \"\"
# (setq flp (cat flp " (cat Driving2Driven_pipe_name \"' pipe!\")
# (setq flp (cat flp " nil
# (setq flp (cat flp " )
# ))))
#
# (setq flp (cat flp " (if (== (setq Driven2Driving_pipe_descr
# (setq flp (cat flp " (file_open Driven2Driving_pipe_name) -1)
# (setq flp (cat flp " (progn
# (setq flp (cat flp " (file_close Driving2Driven_pipe_descr)
# (setq flp (cat flp " (rise_error_info 1
# (setq flp (cat flp " (cat \"\"*** Error while opening the \"\"
# (setq flp (cat flp " (cat Driven2Driving_pipe_name \"' pipe!\")
# (setq flp (cat flp " )
# (setq flp (cat flp " nil
# (setq flp (cat flp " )
# ))))
#
# (setq flp (cat flp " (file_write Driving2Driven_pipe_descr
# (setq flp (cat flp " (cat \"echo \" (cat args
# (setq flp (cat flp " (cat \" | \" (cat exec \"\\n\\n\")))
# ))))
#
# (setq flp (cat flp " (setq result \"\")
# (setq flp (cat flp " (while (! (len result))
# (setq flp (cat flp " (while (== (len (setq str (file_read
# (setq flp (cat flp " Driven2Driving_pipe_descr 1)) 1)
# (setq flp (cat flp " (if (== str \"\\n\\n\")
# (setq flp (cat flp " (break)
# (setq flp (cat flp " (setq result (cat result str)
# (setq flp (cat flp " )
# (setq flp (cat flp " )
# (setq flp (cat flp " )
# ))))
#
# (setq flp (cat flp " (file_close Driving2Driven_pipe_descr)
# (setq flp (cat flp " (file_close Driven2Driving_pipe_descr)
# ))))
#
# (setq flp (cat flp " result
# ))))
#
# (setq flp (fastlisp flp))
# (if (== (at "[Run-time error " flp) 1)
# (rise_error_info (ival (right1 flp 16)) (right1 flp (+ (at "]": " flp) 2)))
# flp
# )
# ))))

(defun FibonacciCoordinator # to be placed into .flp
(progn
  (setq n (+ 0 $1))
  (setq spawn (+ 0 $2))
  (if (< n 2)
    n
    (if (> spawn 0)
      (+ (FibonacciCoordinator (-- n) (> spawn 1))
        (FibonacciCoordinator (- n 2) (> spawn 1)))
      (+ (ival (bmdfm2bmdfm_offload "Fibonacci" (str (-- n))))
        (ival (bmdfm2bmdfm_offload "Fibonacci" (str (- n 2)))))
    )
  )
)

(defun Fibonacci
(progn
  (setq n (+ 0 $1))
  (setq spawn (n_cpuproc))
  (FibonacciCoordinator n spawn)
)
)

# main() begins here
(setq n (+ 0 $1))
(Fibonacci n)

```

## cflp\_udf.c

~~~~~

/\* cflp\_udf.c - FastLisp User Defined Functions written in C  
Sancho Mining 07-09-2000 20:51:42.51pm \*/

```

#include <math.h>
#include <stdio.h>
#include <stdlib.h>
#ifdef NOT_UNIX
#include <unistd.h>
#endif
#include <string.h>
#include "cflp_udf.h"

#ifdef __cplusplus

```

```

extern "C" {
#endif

const CHR *VERSION_CFLPUDF__="Sancho M. CFLPUDF v.1.0.0.";

extern const ULO INSTRUCTIONS;

/* _____ */
/* Functions _____ SECTION 0 */
/* _____ */

#include <fcntl.h>

/* -----
0. DRIVING SENDS SHELL COMMANDS TO PIPE BMDFMPipeDriven2Driven<i>
    "echo args | fastlisp -q exec.flx"
    "echo args | BMDFmldr -q exec.flz"
1. SHELL EXECUTES COMMANDS
    "echo args | fastlisp -q exec.flx >BMDFMPipeDriven2Driven<i>"
    "echo args | BMDFmldr -q exec.flz >BMDFMPipeDriven2Driven<i>"
2. DRIVING RECEIVES RESULTS FROM DRIVEN
----- */

void bmdfm2bmdfm_offload(const ULO *dat_ptr, struct fastlisp_data *ret_dat){
    CHR *exec=NULL,*args=NULL; /* "echo args | fastlisp -q exec.flx" */
    /* "echo args | BMDFmldr -q exec.flz" */
    CHR *temp=NULL,*templ=NULL;
    CHR *Driving2Driven_pipe_name=NULL,*Driven2Driving_pipe_name=NULL,
    *result=NULL;
    int descr,Driving2Driven_pipe_descr,Driven2Driving_pipe_descr;
    ret_dat->disable_ptr=1;
    ret_sval(dat_ptr,&exec);
    ret_sval(dat_ptr+1,&args);
    if(noterror()){
        cat(&exec,get_std_buff(&temp,am_I_in_the_fastlisp_module())
            " .flx":".flz");
        if(-1==(descr=open(exec,O_RDONLY))){
            get_std_buff(&temp,"*** Error while opening the `");
            cat(&temp,exec);
            cat(&temp,get_std_buff(&templ," pipe!"));
            rise_error_info(1,temp);
        }
        else{
            close(descr);
            lcat(&exec,get_std_buff(&temp,am_I_in_the_fastlisp_module())
                "fastlisp -q ":"BMDFmldr -q ");
            get_std_buff(&Driving2Driven_pipe_name,"BMDFMPipeDriven2Driven");
            cat(&Driving2Driven_pipe_name,equ_num(&temp,get_id_cpuproc()));
            get_std_buff(&Driven2Driving_pipe_name,"BMDFMPipeDriven2Driving");
            cat(&Driven2Driving_pipe_name,temp);
            if(-1==(Driving2Driven_pipe_descr=open(Driving2Driven_pipe_name,
                O_RDWR))){
                get_std_buff(&temp,"*** Error while opening the `");
                cat(&temp,Driving2Driven_pipe_name);
                cat(&temp,get_std_buff(&templ," pipe!"));
                rise_error_info(1,temp);
            }
            else{
                if(-1==(Driven2Driving_pipe_descr=open(Driven2Driving_pipe_name,
                    O_RDWR))){
                    close(Driving2Driven_pipe_descr);
                    get_std_buff(&temp,"*** Error while opening the `");
                    cat(&temp,Driven2Driving_pipe_name);
                    cat(&temp,get_std_buff(&templ," pipe!"));
                    rise_error_info(1,temp);
                }
                else{
                    get_std_buff(&temp,"echo ");
                    cat(&temp,args);
                    cat(&temp,get_std_buff(&templ," | "));
                    cat(&temp,exec);
                    cat(&temp,sch2str(&templ,'\n'));
                    write(Driving2Driven_pipe_descr,(void*)temp,len(temp));
                    space(&result,0);
                    while(noterror()&&!len(result))
                        while(noterror()&&(1==read(Driven2Driving_pipe_descr,(void*)templ,
                            1))){
                            if(*templ=='\n')
                                break;
                            cat(&result,templ);
                        }
                    close(Driving2Driven_pipe_descr);
                    close(Driven2Driving_pipe_descr);
                }
            }
        }
        ret_dat->single=1;
        ret_dat->type='S';
        equ(&ret_dat->svalue,result);
        free_string(&exec);
        free_string(&args);
        free_string(&Driving2Driven_pipe_name);
        free_string(&Driven2Driving_pipe_name);
        free_string(&result);
        free_string(&temp);
        free_string(&templ);
    }
    return;
}

/* _____ */
/* FastLisp Callbacks _____ SECTION 1 */
/* _____ */

void startup_callback(void){
    /* This is just a stub. Place your own code here. */
    return;
}

void taskjob_end_callback(ULO id_taskjob){
    /* This is just a stub. Place your own code here. */
    return;
}

/* The BMDFmldr module is capable of invoking/evaluating VM language
expressions from C/C++ code (1-Capable;0-Unable).*/
UCH BMDFmldr_capable_call_VMcode_from_C=0;

void user_io_callback(SLO usr_id, CHR **usr_buff){
    /* This is just a stub. Place your own code here. */
    /* The following is a default behavior: */
    CHR *temp=NULL,*templ=NULL,*temp2=NULL;
    equ(&temp,*usr_buff);
    if(cmp(temp,get_std_buff(&templ,"PWD"))){

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Driving BMDFM Drives Driven BMDFM

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mk_fst_buff(&templ,4096);
if(getcwd((char*)templ,(size_t)len(templ)))
    get_std_buff(usr_buff,templ);
}
else
    if(cmp(head(&temp2,temp),get_std_buff(&templ,"GetEnv"))){
        tail(&templ,temp);
        get_std_buff(usr_buff,getenv(templ));
    }
    else{
        lcat(usr_buff,get_std_buff(&temp," usr_buff=\"");
        lcat(usr_buff,equ_num(&temp,usr_id));
        lcat(usr_buff,get_std_buff(&temp,"USER IO: usr_id="));
        cat(usr_buff,get_std_buff(&temp,"\"."));
    }
}
free_string(&temp);
free_string(&templ);
free_string(&temp2);
return;
}

/* _____ */
/* FastLisp Database Register _____ SECTION 2 */
/* _____ */

INSTRUCTION_STRU INSTRUCTION_SET[]={
    {"BMDFM2BMDFM_OFFLOAD",2,'S',(UCH*)"SS",&bmdfm2bmdfm_offload}
};

const ULO INSTRUCTIONS=sizeof(INSTRUCTION_SET)/sizeof(INSTRUCTION_STRU);

/* _____ */
/* Invocation of Function Main _____ SECTION 3 */
/* _____ */

extern int _Main_(int argc, char *argv[]);

int main(int argc, char *argv[]){
    return _Main_(argc,argv);
}

#ifdef _cplusplus
} // extern "C"
#endif

~~~~~

*****

Workflow:
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ttyl$ # get system processor info
ttyl$ cat /proc/cpuinfo

ttyl$ # list cflp_udf.c
ttyl$ cat cflp_udf.c

ttyl$ # list fastlisp.cfg
ttyl$ cat fastlisp.cfg

ttyl$ # list BMDFMSrv.cfg
ttyl$ cat BMDFMSrv.cfg

ttyl$ # list Fibonacci.flp
ttyl$ cat Fibonacci.flp

ttyl$ # list Fibonacci_master.flp
ttyl$ cat Fibonacci_master.flp

ttyl$ # prepare to build BMDFM
ttyl$ make clean

ttyl$ # build BMDFM
ttyl$ make

ttyl$ # set execution rights for CommChannel
ttyl$ chmod 755 CommChannel

ttyl$ # create CommChannel_starter script that invokes detached
CommChannel
ttyl$ ed
a
CommChannel $! &
.
w CommChannel_starter
17
q

ttyl$ # set execution rights for CommChannel_starter
ttyl$ chmod 755 CommChannel_starter

ttyl$ # start multiple instances of CommChannel
ttyl$ for i in 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18
19 20 21 22 23 24 25 26 27 28 29 30 31; do CommChannel_starter
$i; done

ttyl$ # execute Fibonacci.flp
ttyl$ echo 50 | fastlisp -c Fibonacci.flp

ttyl$ # execute Fibonacci_master.flp

```

```

tty1$ fastlisp -c Fibonacci_master.flp 50

tty2$ # prepare environment to start BMDfmsrv
tty2$ export BM_DFM_PROLOGFILE_path=PROCs0.log
tty2$ export BM_DFM_PROLOGFILE_KEEP_NxSIZE=1x1000000000

tty2$ # start BMDfmsrv
tty2$ BMDfmsrv -l BMDfmsrv0.log

tty1$ # execute Fibonacci.flp
tty1$ echo 50 | BMDfmlr -c Fibonacci.flp

tty2$ # list BMDfmsrv0.log
tty2$ cat BMDfmsrv0.log

tty2$ # list PROCs0.log
tty2$ cat PROCs0.log

tty2$ # prepare environment to start BMDfmsrv
tty2$ export BM_DFM_PROLOGFILE_path=PROCs1.log

tty2$ # start BMDfmsrv
tty2$ BMDfmsrv -l BMDfmsrv1.log

tty3$ # prepare environment to start BMDfmsrv
tty3$ export BM_DFM_CONNECTION_FILE_path=.BMDfmsrv
tty3$ export BM_DFM_CONNECTION_NPIP_path=.BMDfmsrv_npipe
tty3$ export BM_DFM_PROLOGFILE_path=PROCs2.log
tty3$ export BM_DFM_PROLOGFILE_KEEP_NxSIZE=1x1000000000

tty3$ # start BMDfmsrv
tty3$ BMDfmsrv -l BMDfmsrv2.log

tty1$ # execute Fibonacci_master.flp
tty1$ BMDfmlr Fibonacci_master.flp 50

tty2$ # list BMDfmsrv1.log
tty2$ cat BMDfmsrv1.log

tty2$ # list PROCs1.log
tty2$ cat PROCs1.log

tty3$ # list BMDfmsrv2.log
tty3$ cat BMDfmsrv2.log

tty3$ # list PROCs2.log
tty3$ cat PROCs2.log

tty1$ # stop multiple instances of CommChannel
tty1$ killall CommChannel; rm BMDfmpipe*

*****

tty1$ # get system processor info
tty1$ cat /proc/cpuinfo

processor       : 0
vendor_id      : GenuineIntel
cpu family     : 6
model          : 62
model name     : Intel(R) Xeon(R) CPU E7-4820 v2 @ 2.00GHz
stepping       : 7
microcode     : 1804
cpu MHz        : 1995.192
cache size     : 16384 KB
physical id    : 0
siblings       : 8
core id        : 0
cpu cores      : 8
apicid         : 0
initial apicid : 0
fpu            : yes
fpu_exception  : yes
cpuid level    : 13
wp             : yes
flags          : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
                 pat pse36 clflush dts mmx fxsr sse sse2 ss ht syscall nx rdtscp lm constant_ts
                 c arch_perfmon pebs bts xtopology tsc_reliable nonstop_tsc aperfmperf unfair_sp
                 inlock pni pclmulqdq sse3 cx16 sse4_1 sse4_2 popcnt aes xsave avx hypervisor_l
                 ahf_lm ida arat epb pln pts dts
bogomips       : 3990.38
clflush size   : 64
cache alignment : 64
address sizes   : 40 bits physical, 48 bits virtual
power management:

processor       : 1
vendor_id      : GenuineIntel
cpu family     : 6
model          : 62
model name     : Intel(R) Xeon(R) CPU E7-4820 v2 @ 2.00GHz
stepping       : 7
microcode     : 1804
cpu MHz        : 1995.192
cache size     : 16384 KB
physical id    : 0
siblings       : 8
core id        : 1
cpu cores      : 8
apicid         : 1
initial apicid : 1
fpu            : yes
fpu_exception  : yes
cpuid level    : 13
wp             : yes
flags          : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
                 pat pse36 clflush dts mmx fxsr sse sse2 ss ht syscall nx rdtscp lm constant_ts
                 c arch_perfmon pebs bts xtopology tsc_reliable nonstop_tsc aperfmperf unfair_sp

```

```

                 inlock pni pclmulqdq sse3 cx16 sse4_1 sse4_2 popcnt aes xsave avx hypervisor_l
                 ahf_lm ida arat epb pln pts dts
bogomips       : 3990.38
clflush size   : 64
cache alignment : 64
address sizes   : 40 bits physical, 48 bits virtual
power management:

processor       : 2
vendor_id      : GenuineIntel
cpu family     : 6
model          : 62
model name     : Intel(R) Xeon(R) CPU E7-4820 v2 @ 2.00GHz
stepping       : 7
microcode     : 1804
cpu MHz        : 1995.192
cache size     : 16384 KB
physical id    : 0
siblings       : 8
core id        : 2
cpu cores      : 8
apicid         : 2
initial apicid : 2
fpu            : yes
fpu_exception  : yes
cpuid level    : 13
wp             : yes
flags          : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
                 pat pse36 clflush dts mmx fxsr sse sse2 ss ht syscall nx rdtscp lm constant_ts
                 c arch_perfmon pebs bts xtopology tsc_reliable nonstop_tsc aperfmperf unfair_sp
                 inlock pni pclmulqdq sse3 cx16 sse4_1 sse4_2 popcnt aes xsave avx hypervisor_l
                 ahf_lm ida arat epb pln pts dts
bogomips       : 3990.38
clflush size   : 64
cache alignment : 64
address sizes   : 40 bits physical, 48 bits virtual
power management:

processor       : 3
vendor_id      : GenuineIntel
cpu family     : 6
model          : 62
model name     : Intel(R) Xeon(R) CPU E7-4820 v2 @ 2.00GHz
stepping       : 7
microcode     : 1804
cpu MHz        : 1995.192
cache size     : 16384 KB
physical id    : 0
siblings       : 8
core id        : 3
cpu cores      : 8
apicid         : 3
initial apicid : 3
fpu            : yes
fpu_exception  : yes
cpuid level    : 13
wp             : yes
flags          : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
                 pat pse36 clflush dts mmx fxsr sse sse2 ss ht syscall nx rdtscp lm constant_ts
                 c arch_perfmon pebs bts xtopology tsc_reliable nonstop_tsc aperfmperf unfair_sp
                 inlock pni pclmulqdq sse3 cx16 sse4_1 sse4_2 popcnt aes xsave avx hypervisor_l
                 ahf_lm ida arat epb pln pts dts
bogomips       : 3990.38
clflush size   : 64
cache alignment : 64
address sizes   : 40 bits physical, 48 bits virtual
power management:

processor       : 4
vendor_id      : GenuineIntel
cpu family     : 6
model          : 62
model name     : Intel(R) Xeon(R) CPU E7-4820 v2 @ 2.00GHz
stepping       : 7
microcode     : 1804
cpu MHz        : 1995.192
cache size     : 16384 KB
physical id    : 0
siblings       : 8
core id        : 4
cpu cores      : 8
apicid         : 4
initial apicid : 4
fpu            : yes
fpu_exception  : yes
cpuid level    : 13
wp             : yes
flags          : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
                 pat pse36 clflush dts mmx fxsr sse sse2 ss ht syscall nx rdtscp lm constant_ts
                 c arch_perfmon pebs bts xtopology tsc_reliable nonstop_tsc aperfmperf unfair_sp
                 inlock pni pclmulqdq sse3 cx16 sse4_1 sse4_2 popcnt aes xsave avx hypervisor_l
                 ahf_lm ida arat epb pln pts dts
bogomips       : 3990.38
clflush size   : 64
cache alignment : 64
address sizes   : 40 bits physical, 48 bits virtual
power management:

processor       : 5
vendor_id      : GenuineIntel
cpu family     : 6
model          : 62
model name     : Intel(R) Xeon(R) CPU E7-4820 v2 @ 2.00GHz
stepping       : 7
microcode     : 1804
cpu MHz        : 1995.192
cache size     : 16384 KB
physical id    : 0
siblings       : 8
core id        : 5
cpu cores      : 8
apicid         : 5
initial apicid : 5
fpu            : yes
fpu_exception  : yes
cpuid level    : 13
wp             : yes
flags          : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
                 pat pse36 clflush dts mmx fxsr sse sse2 ss ht syscall nx rdtscp lm constant_ts
                 c arch_perfmon pebs bts xtopology tsc_reliable nonstop_tsc aperfmperf unfair_sp
                 inlock pni pclmulqdq sse3 cx16 sse4_1 sse4_2 popcnt aes xsave avx hypervisor_l
                 ahf_lm ida arat epb pln pts dts

```



bogomips : 3990.38  
clflush size : 64  
cache\_alignment : 64  
address sizes : 40 bits physical, 48 bits virtual  
power management:

processor : 6  
vendor\_id : GenuineIntel  
cpu family : 6  
model : 62  
model name : Intel(R) Xeon(R) CPU E7-4820 v2 @ 2.00GHz  
stepping : 7  
microcode : 1804  
cpu MHz : 1995.192  
cache size : 16384 KB  
physical id : 0  
siblings : 8  
core id : 6  
cpu cores : 8  
apicid : 6  
initial apicid : 6  
fpu : yes  
fpu\_exception : yes  
cpuid level : 13  
wp : yes  
flags : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov  
pat pse36 clflush dts mmx fxsr sse sse2 ss ht syscall nx rdtscp lm constant\_ts  
c arch\_perfmon pebs bts xtopology tsc\_reliable nonstop\_tsc aperfmperf unfair\_sp  
inlock pni pclmulqdq ssse3 cx16 sse4\_1 sse4\_2 popcnt aes xsave avx hypervisor 1  
ahf\_lm ida arat epb pln pts dts  
bogomips : 3990.38  
clflush size : 64  
cache\_alignment : 64  
address sizes : 40 bits physical, 48 bits virtual  
power management:

processor : 7  
vendor\_id : GenuineIntel  
cpu family : 6  
model : 62  
model name : Intel(R) Xeon(R) CPU E7-4820 v2 @ 2.00GHz  
stepping : 7  
microcode : 1804  
cpu MHz : 1995.192  
cache size : 16384 KB  
physical id : 0  
siblings : 8  
core id : 7  
cpu cores : 8  
apicid : 7  
initial apicid : 7  
fpu : yes  
fpu\_exception : yes  
cpuid level : 13  
wp : yes  
flags : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov  
pat pse36 clflush dts mmx fxsr sse sse2 ss ht syscall nx rdtscp lm constant\_ts  
c arch\_perfmon pebs bts xtopology tsc\_reliable nonstop\_tsc aperfmperf unfair\_sp  
inlock pni pclmulqdq ssse3 cx16 sse4\_1 sse4\_2 popcnt aes xsave avx hypervisor 1  
ahf\_lm ida arat epb pln pts dts  
bogomips : 3990.38  
clflush size : 64  
cache\_alignment : 64  
address sizes : 40 bits physical, 48 bits virtual  
power management:

processor : 8  
vendor\_id : GenuineIntel  
cpu family : 6  
model : 62  
model name : Intel(R) Xeon(R) CPU E7-4820 v2 @ 2.00GHz  
stepping : 7  
microcode : 1804  
cpu MHz : 1995.192  
cache size : 16384 KB  
physical id : 1  
siblings : 8  
core id : 0  
cpu cores : 8  
apicid : 8  
initial apicid : 8  
fpu : yes  
fpu\_exception : yes  
cpuid level : 13  
wp : yes  
flags : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov  
pat pse36 clflush dts mmx fxsr sse sse2 ss ht syscall nx rdtscp lm constant\_ts  
c arch\_perfmon pebs bts xtopology tsc\_reliable nonstop\_tsc aperfmperf unfair\_sp  
inlock pni pclmulqdq ssse3 cx16 sse4\_1 sse4\_2 popcnt aes xsave avx hypervisor 1  
ahf\_lm ida arat epb pln pts dts  
bogomips : 3990.38  
clflush size : 64  
cache\_alignment : 64  
address sizes : 40 bits physical, 48 bits virtual  
power management:

processor : 9  
vendor\_id : GenuineIntel  
cpu family : 6  
model : 62  
model name : Intel(R) Xeon(R) CPU E7-4820 v2 @ 2.00GHz  
stepping : 7  
microcode : 1804  
cpu MHz : 1995.192  
cache size : 16384 KB  
physical id : 1  
siblings : 8  
core id : 1  
cpu cores : 8  
apicid : 9  
initial apicid : 9  
fpu : yes  
fpu\_exception : yes  
cpuid level : 13  
wp : yes  
flags : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov  
pat pse36 clflush dts mmx fxsr sse sse2 ss ht syscall nx rdtscp lm constant\_ts  
c arch\_perfmon pebs bts xtopology tsc\_reliable nonstop\_tsc aperfmperf unfair\_sp  
inlock pni pclmulqdq ssse3 cx16 sse4\_1 sse4\_2 popcnt aes xsave avx hypervisor 1  
ahf\_lm ida arat epb pln pts dts  
bogomips : 3990.38  
clflush size : 64

cache\_alignment : 64  
address sizes : 40 bits physical, 48 bits virtual  
power management:

processor : 10  
vendor\_id : GenuineIntel  
cpu family : 6  
model : 62  
model name : Intel(R) Xeon(R) CPU E7-4820 v2 @ 2.00GHz  
stepping : 7  
microcode : 1804  
cpu MHz : 1995.192  
cache size : 16384 KB  
physical id : 1  
siblings : 8  
core id : 2  
cpu cores : 8  
apicid : 10  
initial apicid : 10  
fpu : yes  
fpu\_exception : yes  
cpuid level : 13  
wp : yes  
flags : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov  
pat pse36 clflush dts mmx fxsr sse sse2 ss ht syscall nx rdtscp lm constant\_ts  
c arch\_perfmon pebs bts xtopology tsc\_reliable nonstop\_tsc aperfmperf unfair\_sp  
inlock pni pclmulqdq ssse3 cx16 sse4\_1 sse4\_2 popcnt aes xsave avx hypervisor 1  
ahf\_lm ida arat epb pln pts dts  
bogomips : 3990.38  
clflush size : 64  
cache\_alignment : 64  
address sizes : 40 bits physical, 48 bits virtual  
power management:

processor : 11  
vendor\_id : GenuineIntel  
cpu family : 6  
model : 62  
model name : Intel(R) Xeon(R) CPU E7-4820 v2 @ 2.00GHz  
stepping : 7  
microcode : 1804  
cpu MHz : 1995.192  
cache size : 16384 KB  
physical id : 1  
siblings : 8  
core id : 3  
cpu cores : 8  
apicid : 11  
initial apicid : 11  
fpu : yes  
fpu\_exception : yes  
cpuid level : 13  
wp : yes  
flags : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov  
pat pse36 clflush dts mmx fxsr sse sse2 ss ht syscall nx rdtscp lm constant\_ts  
c arch\_perfmon pebs bts xtopology tsc\_reliable nonstop\_tsc aperfmperf unfair\_sp  
inlock pni pclmulqdq ssse3 cx16 sse4\_1 sse4\_2 popcnt aes xsave avx hypervisor 1  
ahf\_lm ida arat epb pln pts dts  
bogomips : 3990.38  
clflush size : 64  
cache\_alignment : 64  
address sizes : 40 bits physical, 48 bits virtual  
power management:

processor : 12  
vendor\_id : GenuineIntel  
cpu family : 6  
model : 62  
model name : Intel(R) Xeon(R) CPU E7-4820 v2 @ 2.00GHz  
stepping : 7  
microcode : 1804  
cpu MHz : 1995.192  
cache size : 16384 KB  
physical id : 1  
siblings : 8  
core id : 4  
cpu cores : 8  
apicid : 12  
initial apicid : 12  
fpu : yes  
fpu\_exception : yes  
cpuid level : 13  
wp : yes  
flags : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov  
pat pse36 clflush dts mmx fxsr sse sse2 ss ht syscall nx rdtscp lm constant\_ts  
c arch\_perfmon pebs bts xtopology tsc\_reliable nonstop\_tsc aperfmperf unfair\_sp  
inlock pni pclmulqdq ssse3 cx16 sse4\_1 sse4\_2 popcnt aes xsave avx hypervisor 1  
ahf\_lm ida arat epb pln pts dts  
bogomips : 3990.38  
clflush size : 64  
cache\_alignment : 64  
address sizes : 40 bits physical, 48 bits virtual  
power management:

processor : 13  
vendor\_id : GenuineIntel  
cpu family : 6  
model : 62  
model name : Intel(R) Xeon(R) CPU E7-4820 v2 @ 2.00GHz  
stepping : 7  
microcode : 1804  
cpu MHz : 1995.192  
cache size : 16384 KB  
physical id : 1  
siblings : 8  
core id : 5  
cpu cores : 8  
apicid : 13  
initial apicid : 13  
fpu : yes  
fpu\_exception : yes  
cpuid level : 13  
wp : yes  
flags : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov  
pat pse36 clflush dts mmx fxsr sse sse2 ss ht syscall nx rdtscp lm constant\_ts  
c arch\_perfmon pebs bts xtopology tsc\_reliable nonstop\_tsc aperfmperf unfair\_sp  
inlock pni pclmulqdq ssse3 cx16 sse4\_1 sse4\_2 popcnt aes xsave avx hypervisor 1  
ahf\_lm ida arat epb pln pts dts  
bogomips : 3990.38  
clflush size : 64  
cache\_alignment : 64  
address sizes : 40 bits physical, 48 bits virtual

power management:

```
processor      : 14
vendor_id     : GenuineIntel
cpu family    : 6
model         : 62
model name    : Intel(R) Xeon(R) CPU E7-4820 v2 @ 2.00GHz
stepping      : 7
microcode     : 1804
cpu MHz       : 1995.192
cache size    : 16384 KB
physical id   : 1
siblings      : 8
core id       : 6
cpu cores     : 8
apicid        : 14
initial apicid : 14
fpu           : yes
fpu_exception : yes
cpuid level   : 13
wp            : yes
flags         : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
               pat pse36 clflush dts mmx fxsr sse sse2 ss ht syscall nx rdtscp lm constant ts
               c arch_perfmon pebs bts xtopology tsc reliable nonstop_tsc aperfmperf unfair_sp
               inlock pni pclmulqdq ssse3 cx16 sse4_1 sse4_2 popcnt aes xsave avx hypervisor 1
               ahf_lm ida arat epb pln pts dts
bogomips      : 3990.38
clflush size  : 64
cache alignment : 64
address sizes  : 40 bits physical, 48 bits virtual
power management:
```

```
processor      : 15
vendor_id     : GenuineIntel
cpu family    : 6
model         : 62
model name    : Intel(R) Xeon(R) CPU E7-4820 v2 @ 2.00GHz
stepping      : 7
microcode     : 1804
cpu MHz       : 1995.192
cache size    : 16384 KB
physical id   : 1
siblings      : 8
core id       : 7
cpu cores     : 8
apicid        : 15
initial apicid : 15
fpu           : yes
fpu_exception : yes
cpuid level   : 13
wp            : yes
flags         : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
               pat pse36 clflush dts mmx fxsr sse sse2 ss ht syscall nx rdtscp lm constant ts
               c arch_perfmon pebs bts xtopology tsc reliable nonstop_tsc aperfmperf unfair_sp
               inlock pni pclmulqdq ssse3 cx16 sse4_1 sse4_2 popcnt aes xsave avx hypervisor 1
               ahf_lm ida arat epb pln pts dts
bogomips      : 3990.38
clflush size  : 64
cache alignment : 64
address sizes  : 40 bits physical, 48 bits virtual
power management:
```

```
processor      : 16
vendor_id     : GenuineIntel
cpu family    : 6
model         : 62
model name    : Intel(R) Xeon(R) CPU E7-4820 v2 @ 2.00GHz
stepping      : 7
microcode     : 1804
cpu MHz       : 1995.192
cache size    : 16384 KB
physical id   : 2
siblings      : 8
core id       : 0
cpu cores     : 8
apicid        : 16
initial apicid : 16
fpu           : yes
fpu_exception : yes
cpuid level   : 13
wp            : yes
flags         : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
               pat pse36 clflush dts mmx fxsr sse sse2 ss ht syscall nx rdtscp lm constant ts
               c arch_perfmon pebs bts xtopology tsc reliable nonstop_tsc aperfmperf unfair_sp
               inlock pni pclmulqdq ssse3 cx16 sse4_1 sse4_2 popcnt aes xsave avx hypervisor 1
               ahf_lm ida arat epb pln pts dts
bogomips      : 3990.38
clflush size  : 64
cache alignment : 64
address sizes  : 40 bits physical, 48 bits virtual
power management:
```

```
processor      : 17
vendor_id     : GenuineIntel
cpu family    : 6
model         : 62
model name    : Intel(R) Xeon(R) CPU E7-4820 v2 @ 2.00GHz
stepping      : 7
microcode     : 1804
cpu MHz       : 1995.192
cache size    : 16384 KB
physical id   : 2
siblings      : 8
core id       : 1
cpu cores     : 8
apicid        : 17
initial apicid : 17
fpu           : yes
fpu_exception : yes
cpuid level   : 13
wp            : yes
flags         : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
               pat pse36 clflush dts mmx fxsr sse sse2 ss ht syscall nx rdtscp lm constant ts
               c arch_perfmon pebs bts xtopology tsc reliable nonstop_tsc aperfmperf unfair_sp
               inlock pni pclmulqdq ssse3 cx16 sse4_1 sse4_2 popcnt aes xsave avx hypervisor 1
               ahf_lm ida arat epb pln pts dts
bogomips      : 3990.38
clflush size  : 64
cache alignment : 64
address sizes  : 40 bits physical, 48 bits virtual
power management:
```

```
processor      : 18
vendor_id     : GenuineIntel
cpu family    : 6
model         : 62
model name    : Intel(R) Xeon(R) CPU E7-4820 v2 @ 2.00GHz
stepping      : 7
microcode     : 1804
cpu MHz       : 1995.192
cache size    : 16384 KB
physical id   : 2
siblings      : 8
core id       : 2
cpu cores     : 8
apicid        : 18
initial apicid : 18
fpu           : yes
fpu_exception : yes
cpuid level   : 13
wp            : yes
flags         : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
               pat pse36 clflush dts mmx fxsr sse sse2 ss ht syscall nx rdtscp lm constant ts
               c arch_perfmon pebs bts xtopology tsc reliable nonstop_tsc aperfmperf unfair_sp
               inlock pni pclmulqdq ssse3 cx16 sse4_1 sse4_2 popcnt aes xsave avx hypervisor 1
               ahf_lm ida arat epb pln pts dts
bogomips      : 3990.38
clflush size  : 64
cache alignment : 64
address sizes  : 40 bits physical, 48 bits virtual
power management:
```

```
processor      : 19
vendor_id     : GenuineIntel
cpu family    : 6
model         : 62
model name    : Intel(R) Xeon(R) CPU E7-4820 v2 @ 2.00GHz
stepping      : 7
microcode     : 1804
cpu MHz       : 1995.192
cache size    : 16384 KB
physical id   : 2
siblings      : 8
core id       : 3
cpu cores     : 8
apicid        : 19
initial apicid : 19
fpu           : yes
fpu_exception : yes
cpuid level   : 13
wp            : yes
flags         : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
               pat pse36 clflush dts mmx fxsr sse sse2 ss ht syscall nx rdtscp lm constant ts
               c arch_perfmon pebs bts xtopology tsc reliable nonstop_tsc aperfmperf unfair_sp
               inlock pni pclmulqdq ssse3 cx16 sse4_1 sse4_2 popcnt aes xsave avx hypervisor 1
               ahf_lm ida arat epb pln pts dts
bogomips      : 3990.38
clflush size  : 64
cache alignment : 64
address sizes  : 40 bits physical, 48 bits virtual
power management:
```

```
processor      : 20
vendor_id     : GenuineIntel
cpu family    : 6
model         : 62
model name    : Intel(R) Xeon(R) CPU E7-4820 v2 @ 2.00GHz
stepping      : 7
microcode     : 1804
cpu MHz       : 1995.192
cache size    : 16384 KB
physical id   : 2
siblings      : 8
core id       : 4
cpu cores     : 8
apicid        : 20
initial apicid : 20
fpu           : yes
fpu_exception : yes
cpuid level   : 13
wp            : yes
flags         : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
               pat pse36 clflush dts mmx fxsr sse sse2 ss ht syscall nx rdtscp lm constant ts
               c arch_perfmon pebs bts xtopology tsc reliable nonstop_tsc aperfmperf unfair_sp
               inlock pni pclmulqdq ssse3 cx16 sse4_1 sse4_2 popcnt aes xsave avx hypervisor 1
               ahf_lm ida arat epb pln pts dts
bogomips      : 3990.38
clflush size  : 64
cache alignment : 64
address sizes  : 40 bits physical, 48 bits virtual
power management:
```

```
processor      : 21
vendor_id     : GenuineIntel
cpu family    : 6
model         : 62
model name    : Intel(R) Xeon(R) CPU E7-4820 v2 @ 2.00GHz
stepping      : 7
microcode     : 1804
cpu MHz       : 1995.192
cache size    : 16384 KB
physical id   : 2
siblings      : 8
core id       : 5
cpu cores     : 8
apicid        : 21
initial apicid : 21
fpu           : yes
fpu_exception : yes
cpuid level   : 13
wp            : yes
flags         : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
               pat pse36 clflush dts mmx fxsr sse sse2 ss ht syscall nx rdtscp lm constant ts
               c arch_perfmon pebs bts xtopology tsc reliable nonstop_tsc aperfmperf unfair_sp
               inlock pni pclmulqdq ssse3 cx16 sse4_1 sse4_2 popcnt aes xsave avx hypervisor 1
               ahf_lm ida arat epb pln pts dts
bogomips      : 3990.38
clflush size  : 64
cache alignment : 64
address sizes  : 40 bits physical, 48 bits virtual
power management:
```

```
processor      : 22
vendor_id     : GenuineIntel
```

```

cpu family      : 6
model          : 62
model name     : Intel(R) Xeon(R) CPU E7-4820 v2 @ 2.00GHz
stepping       : 7
microcode     : 1804
cpu MHz       : 1995.192
cache size    : 16384 KB
physical id   : 2
siblings     : 8
core id      : 6
cpu cores    : 8
apicid       : 22
initial apicid : 22
fpu          : yes
fpu exception : yes
cpuid level  : 13
wp           : yes
flags        : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
               pat pse36 clflush dts mmx fxsr sse sse2 ss ht syscall nx rdtscp lm constant_ts
               c arch_perfmon pebs bts xtopology tsc reliable nonstop_tsc aperfmperf unfair_sp
inlock pni pclmulqdq sse3 cx16 sse4_1 sse4_2 popcnt aes xsave avx hypervisor 1
ahf_lm ida arat epb pln pts dts
bogomips     : 3990.38
clflush size : 64
cache_alignm : 64
address sizes : 40 bits physical, 48 bits virtual
power management:

processor      : 23
vendor_id     : GenuineIntel
cpu family    : 6
model        : 62
model name    : Intel(R) Xeon(R) CPU E7-4820 v2 @ 2.00GHz
stepping     : 7
microcode    : 1804
cpu MHz      : 1995.192
cache size   : 16384 KB
physical id  : 2
siblings    : 8
core id     : 7
cpu cores   : 8
apicid      : 23
initial apicid : 23
fpu         : yes
fpu exception : yes
cpuid level : 13
wp          : yes
flags       : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
               pat pse36 clflush dts mmx fxsr sse sse2 ss ht syscall nx rdtscp lm constant_ts
               c arch_perfmon pebs bts xtopology tsc reliable nonstop_tsc aperfmperf unfair_sp
inlock pni pclmulqdq sse3 cx16 sse4_1 sse4_2 popcnt aes xsave avx hypervisor 1
ahf_lm ida arat epb pln pts dts
bogomips    : 3990.38
clflush size : 64
cache_alignm : 64
address sizes : 40 bits physical, 48 bits virtual
power management:

processor      : 24
vendor_id     : GenuineIntel
cpu family    : 6
model        : 62
model name    : Intel(R) Xeon(R) CPU E7-4820 v2 @ 2.00GHz
stepping     : 7
microcode    : 1804
cpu MHz      : 1995.192
cache size   : 16384 KB
physical id  : 3
siblings    : 8
core id     : 0
cpu cores   : 8
apicid      : 24
initial apicid : 24
fpu         : yes
fpu exception : yes
cpuid level : 13
wp          : yes
flags       : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
               pat pse36 clflush dts mmx fxsr sse sse2 ss ht syscall nx rdtscp lm constant_ts
               c arch_perfmon pebs bts xtopology tsc reliable nonstop_tsc aperfmperf unfair_sp
inlock pni pclmulqdq sse3 cx16 sse4_1 sse4_2 popcnt aes xsave avx hypervisor 1
ahf_lm ida arat epb pln pts dts
bogomips    : 3990.38
clflush size : 64
cache_alignm : 64
address sizes : 40 bits physical, 48 bits virtual
power management:

processor      : 25
vendor_id     : GenuineIntel
cpu family    : 6
model        : 62
model name    : Intel(R) Xeon(R) CPU E7-4820 v2 @ 2.00GHz
stepping     : 7
microcode    : 1804
cpu MHz      : 1995.192
cache size   : 16384 KB
physical id  : 3
siblings    : 8
core id     : 1
cpu cores   : 8
apicid      : 25
initial apicid : 25
fpu         : yes
fpu exception : yes
cpuid level : 13
wp          : yes
flags       : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
               pat pse36 clflush dts mmx fxsr sse sse2 ss ht syscall nx rdtscp lm constant_ts
               c arch_perfmon pebs bts xtopology tsc reliable nonstop_tsc aperfmperf unfair_sp
inlock pni pclmulqdq sse3 cx16 sse4_1 sse4_2 popcnt aes xsave avx hypervisor 1
ahf_lm ida arat epb pln pts dts
bogomips    : 3990.38
clflush size : 64
cache_alignm : 64
address sizes : 40 bits physical, 48 bits virtual
power management:

processor      : 26
vendor_id     : GenuineIntel
cpu family    : 6
model        : 62

```

```

model name     : Intel(R) Xeon(R) CPU E7-4820 v2 @ 2.00GHz
stepping       : 7
microcode     : 1804
cpu MHz       : 1995.192
cache size    : 16384 KB
physical id   : 3
siblings     : 8
core id      : 2
cpu cores    : 8
apicid       : 26
initial apicid : 26
fpu          : yes
fpu exception : yes
cpuid level  : 13
wp           : yes
flags        : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
               pat pse36 clflush dts mmx fxsr sse sse2 ss ht syscall nx rdtscp lm constant_ts
               c arch_perfmon pebs bts xtopology tsc reliable nonstop_tsc aperfmperf unfair_sp
inlock pni pclmulqdq sse3 cx16 sse4_1 sse4_2 popcnt aes xsave avx hypervisor 1
ahf_lm ida arat epb pln pts dts
bogomips     : 3990.38
clflush size : 64
cache_alignm : 64
address sizes : 40 bits physical, 48 bits virtual
power management:

processor      : 27
vendor_id     : GenuineIntel
cpu family    : 6
model        : 62
model name    : Intel(R) Xeon(R) CPU E7-4820 v2 @ 2.00GHz
stepping     : 7
microcode    : 1804
cpu MHz      : 1995.192
cache size   : 16384 KB
physical id  : 3
siblings    : 8
core id     : 3
cpu cores   : 8
apicid      : 27
initial apicid : 27
fpu         : yes
fpu exception : yes
cpuid level : 13
wp          : yes
flags       : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
               pat pse36 clflush dts mmx fxsr sse sse2 ss ht syscall nx rdtscp lm constant_ts
               c arch_perfmon pebs bts xtopology tsc reliable nonstop_tsc aperfmperf unfair_sp
inlock pni pclmulqdq sse3 cx16 sse4_1 sse4_2 popcnt aes xsave avx hypervisor 1
ahf_lm ida arat epb pln pts dts
bogomips    : 3990.38
clflush size : 64
cache_alignm : 64
address sizes : 40 bits physical, 48 bits virtual
power management:

processor      : 28
vendor_id     : GenuineIntel
cpu family    : 6
model        : 62
model name    : Intel(R) Xeon(R) CPU E7-4820 v2 @ 2.00GHz
stepping     : 7
microcode    : 1804
cpu MHz      : 1995.192
cache size   : 16384 KB
physical id  : 3
siblings    : 8
core id     : 4
cpu cores   : 8
apicid      : 28
initial apicid : 28
fpu         : yes
fpu exception : yes
cpuid level : 13
wp          : yes
flags       : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
               pat pse36 clflush dts mmx fxsr sse sse2 ss ht syscall nx rdtscp lm constant_ts
               c arch_perfmon pebs bts xtopology tsc reliable nonstop_tsc aperfmperf unfair_sp
inlock pni pclmulqdq sse3 cx16 sse4_1 sse4_2 popcnt aes xsave avx hypervisor 1
ahf_lm ida arat epb pln pts dts
bogomips    : 3990.38
clflush size : 64
cache_alignm : 64
address sizes : 40 bits physical, 48 bits virtual
power management:

processor      : 29
vendor_id     : GenuineIntel
cpu family    : 6
model        : 62
model name    : Intel(R) Xeon(R) CPU E7-4820 v2 @ 2.00GHz
stepping     : 7
microcode    : 1804
cpu MHz      : 1995.192
cache size   : 16384 KB
physical id  : 3
siblings    : 8
core id     : 5
cpu cores   : 8
apicid      : 29
initial apicid : 29
fpu         : yes
fpu exception : yes
cpuid level : 13
wp          : yes
flags       : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
               pat pse36 clflush dts mmx fxsr sse sse2 ss ht syscall nx rdtscp lm constant_ts
               c arch_perfmon pebs bts xtopology tsc reliable nonstop_tsc aperfmperf unfair_sp
inlock pni pclmulqdq sse3 cx16 sse4_1 sse4_2 popcnt aes xsave avx hypervisor 1
ahf_lm ida arat epb pln pts dts
bogomips    : 3990.38
clflush size : 64
cache_alignm : 64
address sizes : 40 bits physical, 48 bits virtual
power management:

processor      : 30
vendor_id     : GenuineIntel
cpu family    : 6
model        : 62
model name    : Intel(R) Xeon(R) CPU E7-4820 v2 @ 2.00GHz
stepping     : 7

```



```

microcode      : 1804
cpu MHz        : 1995.192
cache size     : 16384 KB
physical id    : 3
siblings       : 8
core id        : 6
cpu cores      : 8
apicid         : 30
initial apicid : 30
fpu            : yes
fpu exception  : yes
cpuid level    : 13
wp             : yes
flags          : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
                 pat pse36 clflush dts mmx fxsr sse sse2 ss ht syscall nx rdtscp lm constant_ts
                 c arch_perfmon pebs bts xtopology tsc reliable nonstop tsc aperfmperf unfair_sp
                 inlock pni pclmulqdq ssse3 cx16 sse4_1 sse4_2 popcnt aes xsave avx hypervisor 1
                 ahf_lm ida arat epb pln pts dts
bogomips       : 3990.38
clflush size   : 64
cache alignment : 64
address sizes  : 40 bits physical, 48 bits virtual
power management:

processor      : 31
vendor_id      : GenuineIntel
cpu family     : 6
model          : 62
model name     : Intel(R) Xeon(R) CPU E7-4820 v2 @ 2.00GHz
stepping       : 7
microcode      : 1804
cpu MHz        : 1995.192
cache size     : 16384 KB
physical id    : 3
siblings       : 8
core id        : 7
cpu cores      : 8
apicid         : 31
initial apicid : 31
fpu            : yes
fpu exception  : yes
cpuid level    : 13
wp             : yes
flags          : fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
                 pat pse36 clflush dts mmx fxsr sse sse2 ss ht syscall nx rdtscp lm constant_ts
                 c arch_perfmon pebs bts xtopology tsc reliable nonstop tsc aperfmperf unfair_sp
                 inlock pni pclmulqdq ssse3 cx16 sse4_1 sse4_2 popcnt aes xsave avx hypervisor 1
                 ahf_lm ida arat epb pln pts dts
bogomips       : 3990.38
clflush size   : 64
cache alignment : 64
address sizes  : 40 bits physical, 48 bits virtual
power management:

```

```

tty1$ # list cflp_udf.c
tty1$ cat cflp_udf.c

```

```

/* cflp_udf.c - FastLisp User Defined Functions written in C
   Sancho Mining 07-09-2000 20:51:42.51pm */

#include <math.h>
#include <stdio.h>
#include <stdlib.h>
#ifdef _NOT_UNIX_
#include <unistd.h>
#endif
#include <string.h>
#include "cflp_udf.h"

#ifdef _cplusplus
extern "C" {
#endif

const CHR *VERSION_CFLPUDEF__="Sancho M. CFLPUDEF v.1.0.0.";

extern const ULO INSTRUCTIONS;

/*
/* Functions SECTION 0 */
/* ----- */

#include <fcntl.h>

/*
-----
0. DRIVING SENDS SHELL COMMANDS TO PIPE BMDFMPipeDriving2Driven<i>
   "echo args | fastlisp -q exec.flx"
   "echo args | BMDFMDlr -q exec.flx"
1. SHELL EXECUTES COMMANDS
   "echo args | fastlisp -q exec.flx >BMDFMPipeDriven2Driving<i>"
   "echo args | BMDFMDlr -q exec.flx >BMDFMPipeDriven2Driving<i>"
2. DRIVING RECEIVES RESULTS FROM DRIVEN
----- */

void bmdfm2bmdfm_offload(const ULO *dat_ptr, struct fastlisp_data *ret_dat){
    CHR *exec=NULL,*args=NULL; /* "echo args | fastlisp -q exec.flx" */
                                /* "echo args | BMDFMDlr -q exec.flx" */
    CHR *temp=NULL,*templ=NULL;
    CHR *Driving2Driven_pipe_name=NULL,*Driven2Driving_pipe_name=NULL,
        *result=NULL;
    int descr,Driving2Driven_pipe_descr,Driven2Driving_pipe_descr;
    ret_dat->disable_ptr=1;
    ret_sval(dat_ptr,&exec);
    ret_sval(dat_ptr+1,&args);
    if(noterror()){
        cat(&exec,get_std_buff(&temp,am_I_in_the_fastlisp_module())
            " flx":".flx");
        if(-1==(descr=open(exec,O_RDONLY))){
            get_std_buff(&temp,"*** Error while opening the ~");
            cat(&temp,exec);
            cat(&temp,get_std_buff(&templ,"' file!"));
            rise_error_info(1,temp);
        }
    }
    else{
        close(descr);
        lcat(&exec,get_std_buff(&temp,am_I_in_the_fastlisp_module())
            "fastlisp -q ":"BMDFMDlr -q ");
        get_std_buff(&Driving2Driven_pipe_name,"BMDFMPipeDriving2Driven");
        cat(&Driving2Driven_pipe_name,eq_num(&temp,get_id_cpuproc()));
        get_std_buff(&Driven2Driving_pipe_name,"BMDFMPipeDriven2Driving");
        cat(&Driven2Driving_pipe_name,temp);
        if(-1==(Driving2Driven_pipe_descr=open(Driving2Driven_pipe_name,
            O_RDWR))){

```

Driving BMDFM Drives Driven BMDFM

```

        get_std_buff(&temp,"*** Error while opening the ~");
        cat(&temp,Driving2Driven_pipe_name);
        cat(&temp,get_std_buff(&templ,"' pipe!"));
        rise_error_info(1,temp);
    }
    else
        if(-1==(Driven2Driving_pipe_descr=open(Driven2Driving_pipe_name,
            O_RDWR))){
            close(Driving2Driven_pipe_descr);
            get_std_buff(&temp,"*** Error while opening the ~");
            cat(&temp,Driven2Driving_pipe_name);
            cat(&temp,get_std_buff(&templ,"' pipe!"));
            rise_error_info(1,temp);
        }
    }
    else{
        get_std_buff(&temp,"echo ");
        cat(&temp,args);
        cat(&temp,get_std_buff(&templ," | "));
        cat(&temp,exec);
        cat(&temp,sch2str(&templ,'\n'));
        write(Driving2Driven_pipe_descr,(void*)temp,len(temp));
        space(&result,0);
        while(noterror()&&!len(result))
            while(noterror()&&(l==read(Driven2Driving_pipe_descr,(void*)templ,
                1))){
                if(*templ=='\n')
                    break;
                cat(&result,templ);
            }
        close(Driving2Driven_pipe_descr);
        close(Driven2Driving_pipe_descr);
    }
}

ret_dat->single=1;
ret_dat->type='S';
equ(&ret_dat->svalue,result);
free_string(&exec);
free_string(&args);
free_string(&Driving2Driven_pipe_name);
free_string(&Driven2Driving_pipe_name);
free_string(&result);
free_string(&temp);
free_string(&templ);
}

return;
}

SLO_dffb FibonacciSeamless(SLO n){
    return noterror()&&n>1? dffb FibonacciSeamless(n-1)+
        _dffb FibonacciSeamless(n-2):n;
}

void dffb FibonacciSeamless(const ULO *dat_ptr,
    struct fastlisp_data *ret_dat){
    SLO n;
    ret_ival(dat_ptr,&n);
    if(noterror()){
        ret_dat->single=1;
        ret_dat->type='I';
        ret_dat->value.ival=_dffb FibonacciSeamless(n);
    }
    return;
}

/*
/* FastLisp Callbacks SECTION 1 */
/* ----- */

void startup_callback(void){
    /* This is just a stub. Place your own code here. */
    return;
}

void taskjob_end_callback(ULO id_taskjob){
    /* This is just a stub. Place your own code here. */
    return;
}

/* The BMDFMDlr module is capable of invoking/evaluating VM language
   expressions from C/C++ code (1-Capable;0-Unable).*/
UCH BMDFMDlr_capable_call_VMcode_from_C=0;

void user_io_callback(SLO usr_id, CHR **usr_buff){
    /* This is just a stub. Place your own code here. */
    /* The following is a default behavior: */
    CHR *temp=NULL,*templ=NULL,*temp2=NULL;
    equ(&temp,*usr_buff);
    if(cmp(temp,get_std_buff(&templ,"PWD"))){
        mk_fst_buff(&templ,4096);
        if(getcwd((char*)templ,(size_t)len(templ)))
            get_std_buff(usr_buff,templ);
    }
    else
        if(cmp(head(&temp2,temp),get_std_buff(&templ,"GetEnv"))){
            tail(&templ,temp);
            get_std_buff(usr_buff,getenv(templ));
        }
    }
    else{
        lcat(usr_buff,get_std_buff(&temp," usr_buff="));
        lcat(usr_buff,eq_num(&temp,usr_id));
        lcat(usr_buff,get_std_buff(&temp,"USER IO: usr_id="));
        cat(usr_buff,get_std_buff(&temp,"\".\");
    }
    free_string(&temp);
    free_string(&templ);
    free_string(&temp2);
    return;
}

/*
/* FastLisp Database Register SECTION 2 */
/* ----- */

INSTRUCTION_STRU INSTRUCTION_SET[]={
    {"FIBONACCISEAMLESS", 1,'I',(UCH*)"I", &dffb FibonacciSeamless},
    {"BMDFM2BMDFM_OFFLOAD",2,'S',(UCH*)"SS",&bmdfm2bmdfm_offload}
};

const ULO INSTRUCTIONS=sizeof(INSTRUCTION_SET)/sizeof(INSTRUCTION_STRU);

/*
/* Invocation of Function Main SECTION 3 */
/* ----- */

```





```

PROGN (SETQEI I 1 50) (SETQS TERM TYPES "xterm") (SETQEI LINES TERM0 24) (SI
TQEI COLUMNS TERM0 80) (SETQS CLRSCTR TERMS "\e[H\2J") (SETQS REVERSE TER
MS "\e[7m") (SETQS BLINK TERMS "\e[5m") (SETQS BOLD TERMS "\e[1m") (SETQS
NORMAL TERMS "\e[0m") (SETQS HIDECURSOR TERMS "\e[?251") (SETQS SHOWCURSOR
TERMS "\e[?211\2[?25h") (SETQS GOTOCURSOR TERMS "\e[&iid;&:di") (DEFUN FIBON
ACCICCOORDINATOR (PROGN (SETQEI N0I (+ 0 1)) (SETQEI SPANW0I (+ 0 2)) (IFQJ (<
I N0I 2) N0I (IFQJ (>EI SPANW0I 0)) (+J (FIBONACCICCOORDINATOR (->J N0I) (->J

```

```
98 4 , 02 00 00 00 00 0D 00 00 00 00 00 00 00 02 00 00 00 00 00 00 00 B0 /
```







```

(N# 3)
(FLP (SETQ@S MAIN:CLRSCR_TERM@S "\e[H\e[2J"))
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 05 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" S 00 00 00 00 00 00 00 00 00" "07 00 00 00 00 00 00 00 00"
"1B [ H 1B [ 2 J 00"
)
(Var_Ptrs 3)
)
(Fnc
(N# 4)
(FLP (SETQ@S MAIN:REVERSE_TERM@S "\e[7m"))
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 05 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" S 00 00 00 00 00 00 00 00 00" "04 00 00 00 00 00 00 00 00"
"1B [ 7 m 00 00 00 00"
)
(Var_Ptrs 4)
)
(Fnc
(N# 5)
(FLP (SETQ@S MAIN:BLINK_TERM@S "\e[5m"))
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 05 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" S 00 00 00 00 00 00 00 00 00" "04 00 00 00 00 00 00 00 00"
"1B [ 5 m 00 00 00 00"
)
(Var_Ptrs 5)
)
(Fnc
(N# 6)
(FLP (SETQ@S MAIN:BOLD_TERM@S "\e[1m"))
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 05 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" S 00 00 00 00 00 00 00 00 00" "04 00 00 00 00 00 00 00 00"
"1B [ 1 m 00 00 00 00"
)
(Var_Ptrs 6)
)
(Fnc
(N# 7)
(FLP (SETQ@S MAIN:NORMAL_TERM@S "\e[0m"))
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 05 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" S 00 00 00 00 00 00 00 00 00" "04 00 00 00 00 00 00 00 00"
"1B [ 0 m 00 00 00 00"
)
(Var_Ptrs 7)
)
(Fnc
(N# 8)
(FLP (SETQ@S MAIN:HIDECURSOR_TERM@S "\e[?251"))
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 05 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" S 00 00 00 00 00 00 00 00 00" "06 00 00 00 00 00 00 00 00"
"1B [ ? 2 5 1 00 00"
)
(Var_Ptrs 8)
)
(Fnc
(N# 9)
(FLP (SETQ@S MAIN:SHOWCURSOR_TERM@S "\e[?121\e[?25h"))
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 05 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" S 00 00 00 00 00 00 00 00 00" "0C 00 00 00 00 00 00 00 00"
"1B [ ? 1 2 1 1B [ " ? 2 5 h 00 00 00 00"
)
(Var_Ptrs 9)
)
(Fnc
(N# 10)
(FLP (SETQ@S MAIN:GOTOCURSOR_TERM@S "\e[%i%d;%dH"))
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 05 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" S 00 00 00 00 00 00 00 00 00" "0A 00 00 00 00 00 00 00 00"
"1B [ % i % d ; % " d H 00 00 00 00 00 00"
)
(Var_Ptrs 10)
)
)
)
(CTRL
(N# 1)
(OpGroup 2)
(COP 14)
(GOTO 51)
(REM "Pass over UDF `MAIN:FIBONACCICOORDINATOR' body")
)
(CTRL
(N# 2)
(OpGroup 2)
(COP 14)
(GOTO 27)
(REM "Pass over UDF `MAIN:FIBONACCICOORDINATOR:SHADOW' body")
)
(CTRL
(N# 3)
(OpGroup 1)
(COP 50)
(dfmput_marshaled_cluster
(Var_N#_Ref_Name [Array]
(0 11 "MAIN:FIBONACCICOORDINATOR:SHADOW:$1")
(1 13 "MAIN:FIBONACCICOORDINATOR:SHADOW:N@I")
(2 12 "MAIN:FIBONACCICOORDINATOR:SHADOW:$2")
(3 14 "MAIN:FIBONACCICOORDINATOR:SHADOW:SPAWN@I")
(4 19 "MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000004@I")
)
)
)
)

```

```

)
(Fnc
(N# 0)
(FLP
(SETQ@I
MAIN:FIBONACCICOORDINATOR:SHADOW:N@I
(+ 0 MAIN:FIBONACCICOORDINATOR:SHADOW:$1)
)
)
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" T BC 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"03 00 00 00 00 00 00 00 00" " I 00 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" " V 00 00 00 00 00 00 00 00 00"
"01 00 00 00 00 00 00 00 00"
)
(Var_Ptrs 1 0)
)
(Fnc
(N# 1)
(FLP
(SETQ@I
MAIN:FIBONACCICOORDINATOR:SHADOW:SPAWN@I
(+ 0 MAIN:FIBONACCICOORDINATOR:SHADOW:$2)
)
)
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" T BC 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"03 00 00 00 00 00 00 00 00" " I 00 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" " V 00 00 00 00 00 00 00 00 00"
"01 00 00 00 00 00 00 00 00"
)
(Var_Ptrs 3 2)
)
(Fnc
(N# 2)
(FLP
(SETQ@I
MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000004@I
(<@I MAIN:FIBONACCICOORDINATOR:SHADOW:N@I 2)
)
)
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"D4 x 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"03 00 00 00 00 00 00 00 00" " i 00 00 00 00 00 00 00 00 00"
"01 00 00 00 00 00 00 00 00" " I 00 00 00 00 00 00 00 00 00"
"02 00 00 00 00 00 00 00 00"
)
(Var_Ptrs 4 1)
)
)
)
(CTRL
(N# 4)
(OpGroup 1)
(COP 70)
(dfmput_xdata
(VarRef 19)
(VarName "MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000004@I")
(Inq_Dest Ld)
)
)
(CTRL
(N# 5) (OpGroup 1) (COP 81) (<accum_slo> (dfmget_idata)))
(CTRL
(N# 6)
(OpGroup 2)
(COP 17)
(IF_NOT <accum_slo> (GOTO 9))
(REM
"Pass over `MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000004@I' <if>
conditional branch"
)
)
(CTRL
(N# 7)
(OpGroup 1)
(COP 50)
(dfmput_marshaled_cluster
(Var_N#_Ref_Name [Array]
(0 13 "MAIN:FIBONACCICOORDINATOR:SHADOW:N@I")
(1 15 "MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000000@I")
)
)
(Fnc
(N# 0)
(FLP
(SETQ@I
MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000000@I
MAIN:FIBONACCICOORDINATOR:SHADOW:N@I
)
)
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" i 00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
)
(Var_Ptrs 1 0)
)
)
)
(CTRL
(N# 8)
(OpGroup 2)
(COP 14)
(GOTO 26)
(REM
"Pass over `MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000004@I' <else>
conditional branch"
)
)
(CTRL
(N# 9)
(OpGroup 1)
(COP 50)
)
)

```





```

(ALSETQ
  MAIN:FIBONACCICORDINATOR:SHADOW:TMP__000000002@I
  MAIN:FIBONACCICORDINATOR:TMP__000000000@I
)
)
(FLP_COMPILED
  "D5 01 00 00 00 00 00 00" "02 00 00 00 00 00 00 00"
  "00 00 00 00 00 00 00 00" "T 08 00 00 00 00 00 00 00"
  "00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
  " i 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
)
(Var_Ptrs 0 1)
)
)
(REM "UDF `MAIN:FIBONACCICORDINATOR' returned value")
)
(CTRL (N# 22) (OpGroup 2) (COP 13) (LEAVE_RECURSION))
(CTRL
  (N# 23)
  (OpGroup 1)
  (COP 50)
  (dfmput_marshaled_cluster
    (Vars_N# Ref_Name [Array]
      (0 16 "MAIN:FIBONACCICORDINATOR:SHADOW:TMP__000000001@I")
      (1 17 "MAIN:FIBONACCICORDINATOR:SHADOW:TMP__000000002@I")
      (2 15 "MAIN:FIBONACCICORDINATOR:SHADOW:TMP__000000000@I")
    )
  )
  (Fnc
    (N# 0)
    (FLP
      (SETQ@I
        MAIN:FIBONACCICORDINATOR:SHADOW:TMP__000000000@I
        (+@J
          MAIN:FIBONACCICORDINATOR:SHADOW:TMP__000000001@I
          MAIN:FIBONACCICORDINATOR:SHADOW:TMP__000000002@I
        )
      )
    )
  )
  (FLP_COMPILED
    "D5 01 00 00 00 00 00 00" "03 00 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
    "D4 BC 00 00 00 00 00 00" "02 00 00 00 00 00 00 00"
    "03 00 00 00 00 00 00 00" " i 00 00 00 00 00 00 00 00"
    "01 00 00 00 00 00 00 00" " i 00 00 00 00 00 00 00 00"
    "02 00 00 00 00 00 00 00"
  )
  (Var_Ptrs 2 0 1)
)
)
)
(CTRL
  (N# 24)
  (OpGroup 2)
  (COP 14)
  (GOTO 26)
  (REM
    "Pass over `MAIN:FIBONACCICORDINATOR:SHADOW:TMP__000000003@I' <else>
    conditional branch"
  )
)
)
(CTRL
  (N# 25)
  (OpGroup 1)
  (COP 50)
  (dfmput_marshaled_cluster
    (Vars_N# Ref_Name [Array]
      (0 13 "MAIN:FIBONACCICORDINATOR:SHADOW:N@I")
      (1 16 "MAIN:FIBONACCICORDINATOR:SHADOW:TMP__000000001@I")
      (2 17 "MAIN:FIBONACCICORDINATOR:SHADOW:TMP__000000002@I")
      (3 15 "MAIN:FIBONACCICORDINATOR:SHADOW:TMP__000000000@I")
    )
  )
  (Fnc
    (N# 0)
    (FLP
      (SETQ@I
        MAIN:FIBONACCICORDINATOR:SHADOW:TMP__000000001@I
        (FIBONACCISEAMLESS@J (--@J MAIN:FIBONACCICORDINATOR:SHADOW:N@I))
      )
    )
  )
  (FLP_COMPILED
    "D5 01 00 00 00 00 00 00" "02 00 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
    " t 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
    "D4 F4 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
    " i 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
  )
  (Var_Ptrs 1 0)
)
)
)
(Fnc
  (N# 1)
  (FLP
    (SETQ@I
      MAIN:FIBONACCICORDINATOR:SHADOW:TMP__000000002@I
      (FIBONACCISEAMLESS@J (--@J MAIN:FIBONACCICORDINATOR:SHADOW:N@I 2))
    )
  )
  (FLP_COMPILED
    "D5 01 00 00 00 00 00 00" "02 00 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
    " t 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
    "D4 C4 00 00 00 00 00 00" "02 00 00 00 00 00 00 00"
    "03 00 00 00 00 00 00 00" " i 00 00 00 00 00 00 00 00"
    "01 00 00 00 00 00 00 00" " i 00 00 00 00 00 00 00 00"
    "02 00 00 00 00 00 00 00"
  )
  (Var_Ptrs 2 0)
)
)
)
(Fnc
  (N# 2)
  (FLP
    (SETQ@I
      MAIN:FIBONACCICORDINATOR:SHADOW:TMP__000000000@I
      (+@J
        MAIN:FIBONACCICORDINATOR:SHADOW:TMP__000000001@I
        MAIN:FIBONACCICORDINATOR:SHADOW:TMP__000000002@I
      )
    )
  )
  (FLP_COMPILED

```

```

    "D5 01 00 00 00 00 00 00" "03 00 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
    "D4 BC 00 00 00 00 00 00" "02 00 00 00 00 00 00 00"
    "03 00 00 00 00 00 00 00" " i 00 00 00 00 00 00 00 00"
    "01 00 00 00 00 00 00 00" " i 00 00 00 00 00 00 00 00"
    "02 00 00 00 00 00 00 00"
  )
  (Var_Ptrs 3 1 2)
)
)
)
(CTRL
  (N# 26)
  (OpGroup 2)
  (COP 16)
  (RETURN)
  (REM "End of UDF `MAIN:FIBONACCICORDINATOR:SHADOW' body")
)
)
(CTRL
  (N# 27)
  (OpGroup 1)
  (COP 50)
  (dfmput_marshaled_cluster
    (Vars_N# Ref_Name [Array]
      (0 8 "MAIN:FIBONACCICORDINATOR:$1")
      (1 10 "MAIN:FIBONACCICORDINATOR:N@I")
      (2 9 "MAIN:FIBONACCICORDINATOR:$2")
      (3 20 "MAIN:FIBONACCICORDINATOR:SPAWN@I")
      (4 25 "MAIN:FIBONACCICORDINATOR:TMP__000000004@I")
    )
  )
  (Fnc
    (N# 0)
    (FLP
      (SETQ@I
        MAIN:FIBONACCICORDINATOR:N@I
        (+ 0 MAIN:FIBONACCICORDINATOR:$1)
      )
    )
  )
  (FLP_COMPILED
    "D5 01 00 00 00 00 00 00" "02 00 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
    " T BC 00 00 00 00 00 00" "02 00 00 00 00 00 00 00"
    "03 00 00 00 00 00 00 00" " i 00 00 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00" " v 00 00 00 00 00 00 00 00"
    "01 00 00 00 00 00 00 00"
  )
  (Var_Ptrs 1 0)
)
)
)
(Fnc
  (N# 1)
  (FLP
    (SETQ@I
      MAIN:FIBONACCICORDINATOR:SPAWN@I
      (+ 0 MAIN:FIBONACCICORDINATOR:$2)
    )
  )
  (FLP_COMPILED
    "D5 01 00 00 00 00 00 00" "02 00 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
    " T BC 00 00 00 00 00 00" "02 00 00 00 00 00 00 00"
    "03 00 00 00 00 00 00 00" " i 00 00 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00" " v 00 00 00 00 00 00 00 00"
    "01 00 00 00 00 00 00 00"
  )
  (Var_Ptrs 3 2)
)
)
)
(Fnc
  (N# 2)
  (FLP
    (SETQ@I
      MAIN:FIBONACCICORDINATOR:TMP__000000004@I
      (<@I MAIN:FIBONACCICORDINATOR:N@I 2)
    )
  )
  (FLP_COMPILED
    "D5 01 00 00 00 00 00 00" "02 00 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
    "D4 x 00 00 00 00 00 00" "02 00 00 00 00 00 00 00"
    "03 00 00 00 00 00 00 00" " i 00 00 00 00 00 00 00 00"
    "01 00 00 00 00 00 00 00" " i 00 00 00 00 00 00 00 00"
    "02 00 00 00 00 00 00 00"
  )
  (Var_Ptrs 4 1)
)
)
)
)
(CTRL
  (N# 28)
  (OpGroup 1)
  (COP 70)
  (dfmput_xdata
    (VarRef 25)
    (VarName "MAIN:FIBONACCICORDINATOR:TMP__000000004@I")
    (Inq_Dest Ld)
  )
)
)
)
(CTRL (N# 29) (OpGroup 1) (COP 81) (<accum_slo> (dfmget_idata)))
(CTRL
  (N# 30)
  (OpGroup 2)
  (COP 17)
  (IF NOT <accum_slo> (GOTO 33))
  (REM
    "Pass over `MAIN:FIBONACCICORDINATOR:TMP__000000004@I' <if> conditional
    branch"
  )
)
)
)
(CTRL
  (N# 31)
  (OpGroup 1)
  (COP 50)
  (dfmput_marshaled_cluster
    (Vars_N# Ref_Name [Array]
      (0 10 "MAIN:FIBONACCICORDINATOR:N@I")
      (1 21 "MAIN:FIBONACCICORDINATOR:TMP__000000000@I")
    )
  )
  (Fnc
    (N# 0)

```



```

    "01 00 00 00 00 00 00 00" " I 00 00 00 00 00 00 00 00"
    "01 00 00 00 00 00 00 00"
  )
  (Var_Ptrs 2 3)
)
)
(REM
  "UDF `MAIN:FIBONACCICOORDINATOR:SHADOW' invoke initialization (passing the
  arguments)"
)
)
(CTRL
  (N# 44)
  (OpGroup 2)
  (COP 15)
  (GOSUB 3)
  (REM "UDF `MAIN:FIBONACCICOORDINATOR:SHADOW' call")
)
)
(CTRL
  (N# 45)
  (OpGroup 1)
  (COP 50)
  (dfmput_marshaled_cluster
    (Vars N# Ref Name [Array]
      (0 23 "MAIN:FIBONACCICOORDINATOR:TMP_000000002@I")
      (1 15 "MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000000@I")
    )
  )
  (Fnc
    (N# 0)
    (FLP
      (ALSETQ
        MAIN:FIBONACCICOORDINATOR:TMP_000000002@I
        MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000000@I
      )
    )
    (FLP_COMPILED
      "D5 01 00 00 00 00 00 00" "02 00 00 00 00 00 00 00"
      "00 00 00 00 00 00 00 00" " T 08 00 00 00 00 00 00 00"
      "00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
      " i 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
    )
  )
  (Var_Ptrs 0 1)
)
)
)
(REM "UDF `MAIN:FIBONACCICOORDINATOR:SHADOW' returned value")
)
(CTRL (N# 46) (OpGroup 2) (COP 13) (LEAVE_RECURSION))
(CTRL
  (N# 47)
  (OpGroup 1)
  (COP 50)
  (dfmput_marshaled_cluster
    (Vars N# Ref Name [Array]
      (0 22 "MAIN:FIBONACCICOORDINATOR:TMP_000000001@I")
      (1 23 "MAIN:FIBONACCICOORDINATOR:TMP_000000002@I")
      (2 21 "MAIN:FIBONACCICOORDINATOR:TMP_000000000@I")
    )
  )
  (Fnc
    (N# 0)
    (FLP
      (SETQ@I
        MAIN:FIBONACCICOORDINATOR:TMP_000000000@I
        (+@J
          MAIN:FIBONACCICOORDINATOR:TMP_000000001@I
          MAIN:FIBONACCICOORDINATOR:TMP_000000002@I
        )
      )
    )
  )
  (FLP_COMPILED
    "D5 01 00 00 00 00 00 00" "03 00 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
    "D4 BC 00 00 00 00 00 00" "02 00 00 00 00 00 00 00"
    "03 00 00 00 00 00 00 00" " i 00 00 00 00 00 00 00 00"
    "01 00 00 00 00 00 00 00" " i 00 00 00 00 00 00 00 00"
    "02 00 00 00 00 00 00 00"
  )
  (Var_Ptrs 2 0 1)
)
)
)
)
(CTRL
  (N# 48)
  (OpGroup 2)
  (COP 14)
  (GOTO 50)
  (REM
    "Pass over `MAIN:FIBONACCICOORDINATOR:TMP_000000003@I' <else> conditional
    branch"
  )
)
)
(CTRL
  (N# 49)
  (OpGroup 1)
  (COP 50)
  (dfmput_marshaled_cluster
    (Vars N# Ref Name [Array]
      (0 10 "MAIN:FIBONACCICOORDINATOR:N@I")
      (1 22 "MAIN:FIBONACCICOORDINATOR:TMP_000000001@I")
      (2 23 "MAIN:FIBONACCICOORDINATOR:TMP_000000002@I")
      (3 21 "MAIN:FIBONACCICOORDINATOR:TMP_000000000@I")
    )
  )
  (Fnc
    (N# 0)
    (FLP
      (SETQ@I
        MAIN:FIBONACCICOORDINATOR:TMP_000000001@I
        (FIBONACCISEAMLESS@J (--@J MAIN:FIBONACCICOORDINATOR:N@I))
      )
    )
  )
  (FLP_COMPILED
    "D5 01 00 00 00 00 00 00" "02 00 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
    " t 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
    "D4 F4 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
    " i 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
  )
  (Var_Ptrs 1 0)
)
)
)
(Fnc
  (N# 1)

```

```

  (FLP
    (SETQ@I
      MAIN:FIBONACCICOORDINATOR:TMP_000000002@I
      (FIBONACCISEAMLESS@J (--@J MAIN:FIBONACCICOORDINATOR:N@I 2))
    )
  )
  (FLP_COMPILED
    "D5 01 00 00 00 00 00 00" "02 00 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
    " t 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
    "D4 C4 00 00 00 00 00 00" "02 00 00 00 00 00 00 00"
    "03 00 00 00 00 00 00 00" " i 00 00 00 00 00 00 00 00"
    "01 00 00 00 00 00 00 00" " i 00 00 00 00 00 00 00 00"
    "02 00 00 00 00 00 00 00"
  )
  (Var_Ptrs 2 0)
)
)
(Fnc
  (N# 2)
  (FLP
    (SETQ@I
      MAIN:FIBONACCICOORDINATOR:TMP_000000000@I
      (+@J
        MAIN:FIBONACCICOORDINATOR:TMP_000000001@I
        MAIN:FIBONACCICOORDINATOR:TMP_000000002@I
      )
    )
  )
  (FLP_COMPILED
    "D5 01 00 00 00 00 00 00" "03 00 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
    "D4 BC 00 00 00 00 00 00" "02 00 00 00 00 00 00 00"
    "03 00 00 00 00 00 00 00" " i 00 00 00 00 00 00 00 00"
    "01 00 00 00 00 00 00 00" " i 00 00 00 00 00 00 00 00"
    "02 00 00 00 00 00 00 00"
  )
  (Var_Ptrs 3 1 2)
)
)
)
(CTRL
  (N# 50)
  (OpGroup 2)
  (COP 16)
  (RETURN)
  (REM "End of UDF `MAIN:FIBONACCICOORDINATOR' body")
)
)
(CTRL
  (N# 51)
  (OpGroup 2)
  (COP 14)
  (GOTO 59)
  (REM "Pass over UDF `MAIN:FIBONACCI' body")
)
)
(CTRL
  (N# 52)
  (OpGroup 1)
  (COP 50)
  (dfmput_marshaled_cluster
    (Vars N# Ref Name [Array]
      (0 4 "MAIN:FIBONACCI:$1")
      (1 5 "MAIN:FIBONACCI:N@I")
      (2 6 "MAIN:FIBONACCI:SPAWN@I")
    )
  )
  (Fnc
    (N# 0)
    (FLP (SETQ@I MAIN:FIBONACCI:N@I (+ 0 MAIN:FIBONACCI:$1)))
    (FLP_COMPILED
      "D5 01 00 00 00 00 00 00" "02 00 00 00 00 00 00 00"
      "00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00"
      "00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
      " T BC 00 00 00 00 00 00" "02 00 00 00 00 00 00 00"
      "03 00 00 00 00 00 00 00" " i 00 00 00 00 00 00 00 00"
      "00 00 00 00 00 00 00 00" " V 00 00 00 00 00 00 00 00"
      "01 00 00 00 00 00 00 00"
    )
  )
  (Var_Ptrs 1 0)
)
)
)
(Fnc
  (N# 1)
  (FLP (SETQ@I MAIN:FIBONACCI:SPAWN@I (N_CPUPROC)))
  (FLP_COMPILED
    "D5 01 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
    " T D0 02 00 00 00 00 00"
  )
  (Var_Ptrs 2)
)
)
)
(CTRL
  (N# 53)
  (OpGroup 2)
  (COP 12)
  (ENTER_RECURSION)
  (Vars N# Ref Name [Array]
    (0 10 "MAIN:FIBONACCICOORDINATOR:N@I")
    (1 8 "MAIN:FIBONACCICOORDINATOR:$1")
    (2 20 "MAIN:FIBONACCICOORDINATOR:SPAWN@I")
    (3 9 "MAIN:FIBONACCICOORDINATOR:$2")
    (4 25 "MAIN:FIBONACCICOORDINATOR:TMP_000000004@I")
    (5 21 "MAIN:FIBONACCICOORDINATOR:TMP_000000000@I")
    (6 24 "MAIN:FIBONACCICOORDINATOR:TMP_000000003@I")
    (7 22 "MAIN:FIBONACCICOORDINATOR:TMP_000000001@I")
    (8 23 "MAIN:FIBONACCICOORDINATOR:TMP_000000002@I")
  )
)
)
)
(CTRL
  (N# 54)
  (OpGroup 1)
  (COP 50)
  (dfmput_marshaled_cluster
    (Vars N# Ref Name [Array]
      (0 8 "MAIN:FIBONACCICOORDINATOR:$1")
      (1 5 "MAIN:FIBONACCI:N@I")
      (2 9 "MAIN:FIBONACCICOORDINATOR:$2")
      (3 6 "MAIN:FIBONACCI:SPAWN@I")
    )
  )
  (Fnc

```

```

(N# 0)
(FLP (ALSETQ MAIN:FIBONACCICOORDINATOR:$1 MAIN:FIBONACCI:N#I))
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "T 08 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"i 00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
)
(Var_Ptrs 0 1)
)
(Fnc
(N# 1)
(FLP (ALSETQ MAIN:FIBONACCICOORDINATOR:$2 MAIN:FIBONACCI:SPAWN#I))
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "T 08 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"i 00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
)
(Var_Ptrs 2 3)
)
)
(REM
"UDF `MAIN:FIBONACCICOORDINATOR' invoke initialization (passing the
arguments)"
)
)
(CTRL
(N# 55)
(OpGroup 2)
(COP 15)
(GOSUB 2)
(REM "UDF `MAIN:FIBONACCICOORDINATOR' call")
)
)
(CTRL
(N# 56)
(OpGroup 1)
(COP 50)
(dfmpmt_marshaled_cluster
(Var_N#_Ref_Name_[Array]
(0 7 "MAIN:FIBONACCI:TMP__000000000I")
(1 21 "MAIN:FIBONACCICOORDINATOR:TMP__000000000I")
)
)
(Fnc
(N# 0)
(FLP
(ALSETQ
MAIN:FIBONACCI:TMP__000000000I
MAIN:FIBONACCICOORDINATOR:TMP__000000000I
)
)
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "T 08 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"i 00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
)
(Var_Ptrs 0 1)
)
)
(REM "UDF `MAIN:FIBONACCICOORDINATOR' returned value")
)
(CTRL (N# 57) (OpGroup 2) (COP 13) (LEAVE_RECURSION))
(CTRL
(N# 58)
(OpGroup 2)
(COP 16)
(RETURN)
(REM "End of UDF `MAIN:FIBONACCI' body")
)
)
(CTRL
(N# 59)
(OpGroup 1)
(COP 50)
(dfmpmt_marshaled_cluster
(Var_N#_Ref_Name_[Array] (0 35 "MAIN:TMP__000000001"))
(Fnc
(N# 0)
(FLP (SETQ@S MAIN:TMP__000000001 ""))
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 05 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"s 00 00 00 00 00 00 00 00 00" "00 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00"
)
(Var_Ptrs 0)
)
)
)
(CTRL
(N# 60)
(OpGroup 1)
(COP 70)
(dfmpmt_zdata (VarRef 35) (VarName "MAIN:TMP__000000001") (Inq_Dest Ls))
)
)
(CTRL
(N# 61)
(OpGroup 1)
(COP 70)
(dfmpmt_zdata (VarRef 35) (VarName "MAIN:TMP__000000001") (Inq_Dest Ld))
)
)
(CTRL
(N# 62)
(OpGroup 1)
(COP 83)
(<accum_ch> (dfmget_sdata))
(REM "I/O synchro")
)
)
(CTRL (N# 63) (OpGroup 3) (COP 20) (<accum_ch> (ACCEPT <accum_ch>)))
(CTRL
(N# 64)
(OpGroup 1)
(COP 73)
(dfmpmt_sdata <accum_ch> (VarRef 36) (VarName "MAIN:TMP__000000002S"))
)
)
(CTRL
(N# 65)
(OpGroup 1)
(COP 50)
(dfmpmt_marshaled_cluster
(Var_N#_Ref_Name_[Array] (0 36 "MAIN:TMP__000000002S") (1 29 "MAIN:N#I"))

```

```

(Fnc
(N# 0)
(FLP (SETQ@I MAIN:N#I (IVAL@S MAIN:TMP__000000002S)))
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"D4 AD 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"s 00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
)
(Var_Ptrs 1 0)
)
)
)
(CTRL
(N# 66)
(OpGroup 2)
(COP 12)
(ENTER_RECURSION)
(Var_N#_Ref_Name_[Array]
(0 5 "MAIN:FIBONACCI:N#I")
(1 4 "MAIN:FIBONACCI:$1")
(2 6 "MAIN:FIBONACCI:SPAWN#I")
(3 7 "MAIN:FIBONACCI:TMP__000000000I")
)
)
)
(CTRL
(N# 67)
(OpGroup 1)
(COP 50)
(dfmpmt_marshaled_cluster
(Var_N#_Ref_Name_[Array] (0 4 "MAIN:FIBONACCI:$1") (1 29 "MAIN:N#I"))
(Fnc
(N# 0)
(FLP (ALSETQ MAIN:FIBONACCI:$1 MAIN:N#I))
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "T 08 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"i 00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
)
(Var_Ptrs 0 1)
)
)
)
(REM "UDF `MAIN:FIBONACCI' invoke initialization (passing the arguments)")
)
)
(CTRL
(N# 68)
(OpGroup 2)
(COP 15)
(GOSUB 52)
(REM "UDF `MAIN:FIBONACCI' call")
)
)
(CTRL
(N# 69)
(OpGroup 1)
(COP 50)
(dfmpmt_marshaled_cluster
(Var_N#_Ref_Name_[Array]
(0 35 "MAIN:TMP__000000001")
(1 7 "MAIN:FIBONACCI:TMP__000000000I")
)
)
)
(Fnc
(N# 0)
(FLP (ALSETQ MAIN:TMP__000000001 MAIN:FIBONACCI:TMP__000000000I))
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "T 08 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"i 00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
)
(Var_Ptrs 0 1)
)
)
)
(REM "UDF `MAIN:FIBONACCI' returned value")
)
(CTRL (N# 70) (OpGroup 2) (COP 13) (LEAVE_RECURSION))
(CTRL
(N# 71)
(OpGroup 1)
(COP 50)
(dfmpmt_marshaled_cluster
(Var_N#_Ref_Name_[Array]
(0 35 "MAIN:TMP__000000001")
(1 35 "MAIN:TMP__000000001")
(2 34 "MAIN:TMP__000000000S")
)
)
)
(Fnc
(N# 0)
(FLP
(SETQ@S
MAIN:TMP__000000001
(OUTF (PRN_STRING_FMT) (CAT "" MAIN:TMP__000000001))
)
)
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 05 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" T 8 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"02 00 00 00 00 00 00 00 00" "T 80 02 00 00 00 00 00 00 00"
" T F4 01 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"04 00 00 00 00 00 00 00 00" " S 00 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "00 00 00 00 00 00 00 00 00"
" V 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
)
)
(Inq_Dest Ls)
(Var_Ptrs 1 0)
)
)
(Fnc
(N# 1)
(FLP (SETQ@S MAIN:TMP__000000000S ""))
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 05 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" S 00 00 00 00 00 00 00 00" "00 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00"
)
(Var_Ptrs 2)
)
)
)

```

```

)
(CTRL (N# 72) (OpGroup 4) (COP 200) (END) (REM "End of the control sequence"))
-----
*You may recompile BMDfMldr module with commented `#define _NOISY_MODEL_`
to disable print of the BM DFM control sequence.
*** Uploading and immediate running of the BM DFM control sequence by
the BM DFM kernel will start here just after the time report!
Time spent to check and prepare the task approx.:
  Used by process: 0.024996sec.
  Used by system: 0.001000sec.
  Total used time: 2.599600000000E-02sec.
Real absolute time: 2.528786659241E-02sec.
*** Resetting time counters (second event controlpoint)... ***
=====
The task is being carried out on SocketN# 0.
=====
12586269025
=====
Time spent to run the task (by PARENT loader and CHILD listener):
  Used by process: 0.011998sec.
  Used by system: 0.020997sec.
  Total used time: 3.299500000000E-02sec.
Real absolute time: 8.218098163605E+00sec.
Task has been detached (logged out) from the BM DFM Server.
The BM DFM Task Loader/Listener pair has done its job decently and gracefully.

tty2$ # list BMDfMsrV0.log
tty2$ cat BMDfMsrV0.log

*** Logfile is opened at systime Mon Jul 13 11:08:36 2015 ***
[StrgLib]: Init Fast String Library done.
[TermCap]: TERM=xterm. Init TERMCAP for the BM DFM console done.
[TermCap]: Current termcap settings:
[TermCap]: TERM TYPE='xterm'; LINES TERM='24'; COLUMNS TERM='80';
[TermCap]: CLRSCR_TERM='\e[H\e[2J'; REVERSE_TERM='\e[7m'; BLINK_TERM='\e[5m';
;
[TermCap]: BOLD_TERM='\e[1m'; NORMAL_TERM='\e[0m'; HIDECURSOR_TERM='\e[?25l';
;
[TermCap]: SHOWCURSOR_TERM='\e[?12l\e[?25h'; GOTOCURSOR_TERM='\e[%i%d;%dH'.
[TermCap]: Remote terminal device driver installed.
[FastLisp]: Init FastLisp RT Engine done.
[DFMsrV]: (VERSION BMDfM SYS_: "Sancho M. BMDfMSys V5.9.9..")
[DFMsrV]: Elaborated version for parallel high-performance technical computi
ng; build common state: <TESTED, STABLE, FINAL RELEASE>.
[DFMsrV]: This version number also belongs to all slave BM DFM processes tha
t run on the background behind the master Server console.
[DFMsrV]: (Compiled on: "Linux RedHatEL62VM 2.6.32-220.13.1.el6.x86_64 #1 S
MP Thu Mar 29 11:46:40 EDT 2012 x86_64".)
[DFMsrV]: (Compiled by: "gcc version 4.4.6 20110731 (Red Hat 4.4.6-3) (GCC)
as [ELF 64-bit LSB executable, x86_64, version 1 (SYSV), dynamically linked
(uses shared libs), for GNU/Linux 2.6.18, stripped] at systime Fri Mar 13 21
:15:06 CET 2015".)
[DFMsrV]: Logs are in `BMDfMsrV0.log', descriptor=4.
[Msg]: Reading the configuration profile `./BMDfMsrV.cfg'...
[Msg]: Reading done.
[SysMsg]: Setting SHMEM POOL SIZE... # Shared memory pool size
[MemPool]: SHMEM POOL SIZE=10000000000Bytes.
[SysMsg]: Setting SHMEM POOL MNTADDR... # ShMemPool mount address
[MemPool]: SHMEM POOL MNTADDR=999999999.
[SysMsg]: Setting SHMEM POOL PERMS... # ShMemPool permissions
[MemPool]: SHMEM POOL PERMS=432.
[DFMsrV]: SEMA4 PERMS=SHMEM POOL PERMS=432.
[SysMsg]: Setting SHMEM POOL BANKS... # Number of banks in pool
[MemPool]: SHMEM POOL BANKS=64.
[SysMsg]: Setting POSIX SEMA4 SYNC... # Use SVR4 or POSIX semaphores
[MemPool]: POSIX SEMA4_SYNC=RW.
[DFMkrnl]: POSIX SEMA4_SYNC=RW+COUNT.
[SysMsg]: Setting ARRAYBLOCK SIZE... # Array block size
[DFMsrV]: ARRAYBLOCK SIZE=32Entities.
[SysMsg]: Setting OQ_FUNC ARG COUNT... # OQ function argument count
[DFMsrV]: OQ_FUNC ARG COUNT=32Entities.
[SysMsg]: Setting Q_OQ... # Operation Queue (OQ) size
[DFMkrnl]: Q_OQ=20000Entities.
[SysMsg]: Setting Q_DB... # Data Buffer (DB) size
[DFMkrnl]: Q_DB=15000Entities.
[SysMsg]: Setting Q_IORBP... # I/O Ring Buffer Port (IORBP) size
[DFMkrnl]: Q_IORBP=100Entities.
[SysMsg]: Setting N_IORBP... # Number of the IORBPs
[DFMkrnl]: N_IORBP=500.
[SysMsg]: Setting N_TRACEPORT... # Number of the Trace Ports (TPs)
[DFMsrV]: N_TRACEPORT=5.
[SysMsg]: Setting N_CPUPROC... # Number of the CPU PROCs
[DFMkrnl]: N_CPUPROC=32.
[SysMsg]: Setting N_OQPROC... # Number of the OQ PROCs
[DFMkrnl]: N_OQPROC=32.
[SysMsg]: Setting N_IORBPPROC... # Number of the IORBP PROCs
[DFMkrnl]: N_IORBPPROC=32.
[SysMsg]: Setting CPUPROC MTHREAD... # Multithreaded CPU PROC
[DFMsrV]: CPUPROC MTHREAD=YES.
[SysMsg]: Setting OQPROC MTHREAD... # Multithreaded OQ PROC
[DFMsrV]: OQPROC MTHREAD=YES.
[SysMsg]: Setting IORBPPROC MTHREAD... # Multithreaded IORBP PROC
[DFMsrV]: IORBPPROC MTHREAD=YES.
[SysMsg]: Setting BMDfMLDR MTHREAD... # Multithreaded BMDfMldr
[DFMsrV]: BMDfMLDR MTHREAD=YES.
[SysMsg]: Setting T_STATISTIC... # Time to scan DFM for statistic
[DFMsrV]: T_STATISTIC=1Second.
[SysMsg]: Setting PROC HEARTBEATS... # Process heartbeats
[DFMsrV]: PROC HEARTBEATS=YES.
[SysMsg]: Setting DFSTLHAZARD DETECT... # Dataflow stall hazard detection
[DFMsrV]: DFSTLHAZARD DETECT=YES.
[SysMsg]: Setting ALLOW_DROP_NONPROD... # Allow dropping nonproductive instr
uctions
[DFMsrV]: ALLOW_DROP_NONPROD=NO.
[SysMsg]: Setting PROC CPU LOGS... # Registration logs for the PROCs
[DFMsrV]: PROC CPU LOGS=YES.
[SysMsg]: Setting HARD ARRAY SYNCHRO... # Hard array synchronization
[DFMkrnl]: HARD ARRAY SYNCHRO=NO.
[SysMsg]: Setting EXT_IN_OUT SYNCHRO... # I/O sync of external tasks
[DFMkrnl]: EXT_IN_OUT SYNCHRO=YES.
[SysMsg]: Setting OQ_DB_SEM_LIMIT... # Limit for OQ&DB semaphores
[DFMsrV]: OQ_DB_SEM_LIMIT=0.
[Msg]: Determining the system semaphore parameters...
[DFMsrV]: Number of SVR4 semaphores per group is 250.
[DFMsrV]: Maximal SVR4/POSIX semaphore value is 2147483647.
[SysMsg]: Creating the freeIPC EMERGENCY CASE file...
[DFMsrV]: File name: ./freeIPC.inf
[SysMsg]: Initializing the Shared Memory Pool...
[SysMsg]: Setting up common semaphores...
[DFMsrV]: Common semaphores init done.
[DFMkrnl]: DFM Global FastLisp function set (embedded MACROS):

```

```

[DFMkrnl]: (CFLP_UDF FIBONACCISEAMLESS BMDfM2BMDfM_OFFLOAD)
[DFMkrnl]: (DEFOP <Undefined>)
[SysMsg]: Setting up the Task Connection Zone (TCZ)...
[DFMsrV]: Task Connection Zone (TCZ) init done.
[DFMsrV]: FIRST GATEWAY Interface: Task Connection Zone [500Entities]:
[DFMsrV]: SocketN# | InUse | Context | FLSP Funcs | ErrCode
-----
[DFMsrV]: 0 | No | 0000000000 | 0000000000 | (000)
[DFMsrV]: 1 | No | 0000000001 | 0000000000 | (000)
[DFMsrV]: 2 | No | 0000000002 | 0000000000 | (000)
[DFMsrV]: 3 | No | 0000000003 | 0000000000 | (000)
[DFMsrV]: 4 | No | 0000000004 | 0000000000 | (000)
[DFMsrV]: 5 | No | 0000000005 | 0000000000 | (000)
[DFMsrV]: 6 | No | 0000000006 | 0000000000 | (000)
[DFMsrV]: 7 | No | 0000000007 | 0000000000 | (000)
[DFMsrV]: 8 | No | 0000000008 | 0000000000 | (000)
[DFMsrV]: 9 | No | 0000000009 | 0000000000 | (000)
[DFMsrV]: 10 | No | 0000000010 | 0000000000 | (000)
[DFMsrV]: 11 | No | 0000000011 | 0000000000 | (000)
[DFMsrV]: 12 | No | 0000000012 | 0000000000 | (000)
[DFMsrV]: 13 | No | 0000000013 | 0000000000 | (000)
[DFMsrV]: 14 | No | 0000000014 | 0000000000 | (000)
[DFMsrV]: 15 | No | 0000000015 | 0000000000 | (000)
[DFMsrV]: 16 | No | 0000000016 | 0000000000 | (000)
[DFMsrV]: 17 | No | 0000000017 | 0000000000 | (000)
[DFMsrV]: 18 | No | 0000000018 | 0000000000 | (000)
[DFMsrV]: 19 | No | 0000000019 | 0000000000 | (000)
[DFMsrV]: 20 | No | 0000000020 | 0000000000 | (000)
[DFMsrV]: 21 | No | 0000000021 | 0000000000 | (000)
[DFMsrV]: 22 | No | 0000000022 | 0000000000 | (000)
[DFMsrV]: 23 | No | 0000000023 | 0000000000 | (000)
[DFMsrV]: 24 | No | 0000000024 | 0000000000 | (000)
[DFMsrV]: 25 | No | 0000000025 | 0000000000 | (000)
[DFMsrV]: 26 | No | 0000000026 | 0000000000 | (000)
[DFMsrV]: 27 | No | 0000000027 | 0000000000 | (000)
[DFMsrV]: 28 | No | 0000000028 | 0000000000 | (000)
[DFMsrV]: 29 | No | 0000000029 | 0000000000 | (000)
[DFMsrV]: 30 | No | 0000000030 | 0000000000 | (000)
[DFMsrV]: 31 | No | 0000000031 | 0000000000 | (000)
[DFMsrV]: 32 | No | 0000000032 | 0000000000 | (000)
[DFMsrV]: 33 | No | 0000000033 | 0000000000 | (000)
[DFMsrV]: 34 | No | 0000000034 | 0000000000 | (000)
[DFMsrV]: 35 | No | 0000000035 | 0000000000 | (000)
[DFMsrV]: 36 | No | 0000000036 | 0000000000 | (000)
[DFMsrV]: 37 | No | 0000000037 | 0000000000 | (000)
[DFMsrV]: 38 | No | 0000000038 | 0000000000 | (000)
[DFMsrV]: 39 | No | 0000000039 | 0000000000 | (000)
[DFMsrV]: 40 | No | 0000000040 | 0000000000 | (000)
[DFMsrV]: 41 | No | 0000000041 | 0000000000 | (000)
[DFMsrV]: 42 | No | 0000000042 | 0000000000 | (000)
[DFMsrV]: 43 | No | 0000000043 | 0000000000 | (000)
[DFMsrV]: 44 | No | 0000000044 | 0000000000 | (000)
[DFMsrV]: 45 | No | 0000000045 | 0000000000 | (000)
[DFMsrV]: 46 | No | 0000000046 | 0000000000 | (000)
[DFMsrV]: 47 | No | 0000000047 | 0000000000 | (000)
[DFMsrV]: 48 | No | 0000000048 | 0000000000 | (000)
[DFMsrV]: 49 | No | 0000000049 | 0000000000 | (000)
[DFMsrV]: 50 | No | 0000000050 | 0000000000 | (000)
[DFMsrV]: 51 | No | 0000000051 | 0000000000 | (000)
[DFMsrV]: 52 | No | 0000000052 | 0000000000 | (000)
[DFMsrV]: 53 | No | 0000000053 | 0000000000 | (000)
[DFMsrV]: 54 | No | 0000000054 | 0000000000 | (000)
[DFMsrV]: 55 | No | 0000000055 | 0000000000 | (000)
[DFMsrV]: 56 | No | 0000000056 | 0000000000 | (000)
[DFMsrV]: 57 | No | 0000000057 | 0000000000 | (000)
[DFMsrV]: 58 | No | 0000000058 | 0000000000 | (000)
[DFMsrV]: 59 | No | 0000000059 | 0000000000 | (000)
[DFMsrV]: 60 | No | 0000000060 | 0000000000 | (000)
[DFMsrV]: 61 | No | 0000000061 | 0000000000 | (000)
[DFMsrV]: 62 | No | 0000000062 | 0000000000 | (000)
[DFMsrV]: 63 | No | 0000000063 | 0000000000 | (000)
[DFMsrV]: 64 | No | 0000000064 | 0000000000 | (000)
[DFMsrV]: 65 | No | 0000000065 | 0000000000 | (000)
[DFMsrV]: 66 | No | 0000000066 | 0000000000 | (000)
[DFMsrV]: 67 | No | 0000000067 | 0000000000 | (000)
[DFMsrV]: 68 | No | 0000000068 | 0000000000 | (000)
[DFMsrV]: 69 | No | 0000000069 | 0000000000 | (000)
[DFMsrV]: 70 | No | 0000000070 | 0000000000 | (000)
[DFMsrV]: 71 | No | 0000000071 | 0000000000 | (000)
[DFMsrV]: 72 | No | 0000000072 | 0000000000 | (000)
[DFMsrV]: 73 | No | 0000000073 | 0000000000 | (000)
[DFMsrV]: 74 | No | 0000000074 | 0000000000 | (000)
[DFMsrV]: 75 | No | 0000000075 | 0000000000 | (000)
[DFMsrV]: 76 | No | 0000000076 | 0000000000 | (000)
[DFMsrV]: 77 | No | 0000000077 | 0000000000 | (000)
[DFMsrV]: 78 | No | 0000000078 | 0000000000 | (000)
[DFMsrV]: 79 | No | 0000000079 | 0000000000 | (000)
[DFMsrV]: 80 | No | 0000000080 | 0000000000 | (000)
[DFMsrV]: 81 | No | 0000000081 | 0000000000 | (000)
[DFMsrV]: 82 | No | 0000000082 | 0000000000 | (000)
[DFMsrV]: 83 | No | 0000000083 | 0000000000 | (000)
[DFMsrV]: 84 | No | 0000000084 | 0000000000 | (000)
[DFMsrV]: 85 | No | 0000000085 | 0000000000 | (000)
[DFMsrV]: 86 | No | 0000000086 | 0000000000 | (000)
[DFMsrV]: 87 | No | 0000000087 | 0000000000 | (000)
[DFMsrV]: 88 | No | 0000000088 | 0000000000 | (000)
[DFMsrV]: 89 | No | 0000000089 | 0000000000 | (000)
[DFMsrV]: 90 | No | 0000000090 | 0000000000 | (000)
[DFMsrV]: 91 | No | 0000000091 | 0000000000 | (000)
[DFMsrV]: 92 | No | 0000000092 | 0000000000 | (000)
[DFMsrV]: 93 | No | 0000000093 | 0000000000 | (000)
[DFMsrV]: 94 | No | 0000000094 | 0000000000 | (000)
[DFMsrV]: 95 | No | 0000000095 | 0000000000 | (000)
[DFMsrV]: 96 | No | 0000000096 | 0000000000 | (000)
[DFMsrV]: 97 | No | 0000000097 | 0000000000 | (000)
[DFMsrV]: 98 | No | 0000000098 | 0000000000 | (000)
[DFMsrV]: 99 | No | 0000000099 | 0000000000 | (000)
[DFMsrV]: 100 | No | 0000000100 | 0000000000 | (000)
[DFMsrV]: 101 | No | 0000000101 | 0000000000 | (000)
[DFMsrV]: 102 | No | 0000000102 | 0000000000 | (000)
[DFMsrV]: 103 | No | 0000000103 | 0000000000 | (000)
[DFMsrV]: 104 | No | 0000000104 | 0000000000 | (000)
[DFMsrV]: 105 | No | 0000000105 | 0000000000 | (000)
[DFMsrV]: 106 | No | 0000000106 | 0000000000 | (000)
[DFMsrV]: 107 | No | 0000000107 | 0000000000 | (000)
[DFMsrV]: 108 | No | 0000000108 | 0000000000 | (000)
[DFMsrV]: 109 | No | 0000000109 | 0000000000 | (000)
[DFMsrV]: 110 | No | 0000000110 | 0000000000 | (000)
[DFMsrV]: 111 | No | 0000000111 | 0000000000 | (000)
[DFMsrV]: 112 | No | 0000000112 | 0000000000 | (000)
[DFMsrV]: 113 | No | 0000000113 | 0000000000 | (000)
[DFMsrV]: 114 | No | 0000000114 | 0000000000 | (000)

```





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Driving BMDFM Drives Driven BMDFM
= Page 23 of 79 =
<http://bmdfm.com>

### Driving BMDFM Drives Driven BMDFM

```

[DFMSrv]: 344 0000000000 Enabled ==> *FREE_SOCKET*
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[SysMsg]: Setting up the Trace Plugging Area (TPA)...
[DFMSrv]: Trace Plugging Area (TPA) init done.
[DFMSrv]: SECOND_GATEWAY Interface: Trace Plugging Area [5Entities]:
[DFMSrv]: PortN# InUse Tracer_PID
[DFMSrv]: -----
[DFMSrv]: 0 No ==> *FREE_PORT*
[DFMSrv]: 1 No ==> *FREE_PORT*
[DFMSrv]: 2 No ==> *FREE_PORT*
[DFMSrv]: 3 No ==> *FREE_PORT*
[DFMSrv]: 4 No ==> *FREE_PORT*

[DFMkrnl]: Tracing is disabled: trace processes are prohibited,
[DFMkrnl]: external tracer connections will be refused.
[OSInfo]: Type SysSize [Bytes]
[OSInfo]: -----
[OSInfo]: UCH, SCH 1, 1
[OSInfo]: USH, SSH 2, 2
[OSInfo]: ULO, SLO 8, 8
[OSInfo]: DFL 8
[OSInfo]: char 1
[OSInfo]: short 2
[OSInfo]: int 4
[OSInfo]: long 8
[OSInfo]: float 4
[OSInfo]: double 8
[OSInfo]: long double 8
[OSInfo]: void* 16
[OSInfo]: Machine type: Little-Endian.
[OSInfo]: System CLOCKS_PER_SEC=1000000.
[OSInfo]: sysconf(_SC_CLK_TCK)=100.
[OSInfo]: Current UNIX SVR4 IPC limits:
[OSInfo]: sem: semaphore constants are not available.
[OSInfo]: shm: shared memory constants are not available.
[OSInfo]: Current POSIX SEMA4 limits:
[OSInfo]: sem: semaphore constants are not available.
[SysMsg]: Opening PROC logs "PROCs0.log"...
[DFMSrv]: *** PROC logfile is opened at systime Mon Jul 13 11:08:36 2015 ***
[DFMSrv]: PROC logs are in "PROCs0.log".
[SysMsg]: Organizing an abstract DFM UNIT STRUCTURE in the SHMEM_POOL:
[SysMsg]: Initializing CPU PROC state array...
[SysMsg]: Organizing DFM IOBPs...
[SysMsg]: Collecting system semaphores for the OQ and DB...
[SysMsg]: SemOQ=20000/20000, SemDBAreas=4800000/4800000.
[SysMsg]: Organizing DFM OQ...
[SysMsg]: Organizing DFM DB...
[SysMsg]: Making pipe for msg exchange between PROCs...
[DFMSrv]: Upipe read ID=6. Upipe write ID=7.
[SysMsg]: Creating named pipe for external tasks...
[DFMSrv]: Named FIFO pipe name: /tmp/.BMDfmsrv_npipe
[DFMSrv]: Named FIFO pipe R/W ID=8.
[DFMSrv]: Named FIFO pipe creation done.
[MemPool]: The Shared Memory Pool init done.
[MemPool]: * STATUS OF THE SHARED MEMORY DRIVEN BY THE RE-ENTRANT CODE *
[MemPool]: Shared memory segment ID=111706174.
[MemPool]: SHMEM_POOL_SIZE: 1000000000Bytes (64 BANKS of 156249904 each).
[MemPool]: Shared memory segment has been attached at 0x00000003B9AC000.
[MemPool]: Shared memory segment permissions are: 0660=="rw-rw----".
[MemPool]: Using POSIX sema4 sync instead of SVR4 sema4 sync.
[MemPool]: Red-Black Tree (RBT) node size: 72Bytes.
[MemPool]: Number of reserved RBT-nodes: 13.
[MemPool]:<BANK#: Entities, FirstEntSpaceAfter, Free(Max), Fragmentation.>
[MemPool]: B#0: Ent=2676, FA=2675, Free=152729056 (152729056), Frag=0.00%.
[MemPool]: B#1: Ent=2676, FA=2675, Free=152732768 (152732768), Frag=0.00%.
[MemPool]: B#2: Ent=2676, FA=2675, Free=152729408 (152729408), Frag=0.00%.
[MemPool]: B#3: Ent=2676, FA=2675, Free=152732768 (152732768), Frag=0.00%.
[MemPool]: B#4: Ent=2676, FA=2675, Free=152729408 (152729408), Frag=0.00%.
[MemPool]: B#5: Ent=2676, FA=2675, Free=152216784 (152216784), Frag=0.00%.
[MemPool]: B#6: Ent=2676, FA=2675, Free=152728400 (152728400), Frag=0.00%.
[MemPool]: B#7: Ent=2676, FA=2675, Free=152731984 (152731984), Frag=0.00%.
[MemPool]: B#8: Ent=2676, FA=2675, Free=152728376 (152728376), Frag=0.00%.
[MemPool]: B#9: Ent=2676, FA=2675, Free=152731960 (152731960), Frag=0.00%.
[MemPool]: B#10: Ent=2676, FA=2675, Free=152728376 (152728376), Frag=0.00%.
[MemPool]: B#11: Ent=2676, FA=2675, Free=152731488 (152731488), Frag=0.00%.
[MemPool]: B#12: Ent=2676, FA=2675, Free=152724408 (152724408), Frag=0.00%.
[MemPool]: B#13: Ent=2676, FA=2675, Free=152731488 (152731488), Frag=0.00%.
[MemPool]: B#14: Ent=2676, FA=2675, Free=152728400 (152728400), Frag=0.00%.
[MemPool]: B#15: Ent=2676, FA=2675, Free=152731488 (152731488), Frag=0.00%.
[MemPool]: B#16: Ent=1381, FA=1380, Free=968 (968), Frag=0.00%.
[MemPool]: B#17: Ent=3971, FA=3970, Free=151218152 (151218152), Frag=0.00%.
[MemPool]: B#18: Ent=2676, FA=2675, Free=152727128 (152727128), Frag=0.00%.
[MemPool]: B#19: Ent=2676, FA=2675, Free=152730712 (152730712), Frag=0.00%.
[MemPool]: B#20: Ent=2676, FA=2675, Free=152727128 (152727128), Frag=0.00%.
[MemPool]: B#21: Ent=2676, FA=2675, Free=152730712 (152730712), Frag=0.00%.
[MemPool]: B#22: Ent=2676, FA=2675, Free=152727128 (152727128), Frag=0.00%.
[MemPool]: B#23: Ent=2676, FA=2675, Free=152730712 (152730712), Frag=0.00%.
[MemPool]: B#24: Ent=2676, FA=2675, Free=152727128 (152727128), Frag=0.00%.
[MemPool]: B#25: Ent=2676, FA=2675, Free=152730712 (152730712), Frag=0.00%.
[MemPool]: B#26: Ent=2676, FA=2675, Free=152727128 (152727128), Frag=0.00%.
[MemPool]: B#27: Ent=2676, FA=2675, Free=152730712 (152730712), Frag=0.00%.

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[MemPool]: B#28: Ent=2676, FA=2675, Free=152727128(152727128), Frag=0.00%.
[MemPool]: B#29: Ent=2676, FA=2675, Free=152730712(152730712), Frag=0.00%.
[MemPool]: B#30: Ent=2676, FA=2675, Free=152727128(152727128), Frag=0.00%.
[MemPool]: B#31: Ent=2676, FA=2675, Free=152730712(152730712), Frag=0.00%.
[MemPool]: B#32: Ent=2676, FA=2675, Free=152727128(152727128), Frag=0.00%.
[MemPool]: B#33: Ent=2675, FA=2674, Free=152731880(152731880), Frag=0.00%.
[MemPool]: B#34: Ent=2675, FA=2674, Free=152728296(152728296), Frag=0.00%.
[MemPool]: B#35: Ent=2675, FA=2674, Free=152731880(152731880), Frag=0.00%.
[MemPool]: B#36: Ent=2675, FA=2674, Free=152728296(152728296), Frag=0.00%.
[MemPool]: B#37: Ent=2675, FA=2674, Free=152731880(152731880), Frag=0.00%.
[MemPool]: B#38: Ent=2675, FA=2674, Free=152728296(152728296), Frag=0.00%.
[MemPool]: B#39: Ent=2675, FA=2674, Free=152731880(152731880), Frag=0.00%.
[MemPool]: B#40: Ent=2675, FA=2674, Free=152728296(152728296), Frag=0.00%.
[MemPool]: B#41: Ent=2675, FA=2674, Free=152731880(152731880), Frag=0.00%.
[MemPool]: B#42: Ent=2675, FA=2674, Free=152728296(152728296), Frag=0.00%.
[MemPool]: B#43: Ent=2675, FA=2674, Free=152731880(152731880), Frag=0.00%.
[MemPool]: B#44: Ent=2675, FA=2674, Free=152728296(152728296), Frag=0.00%.
[MemPool]: B#45: Ent=2675, FA=2674, Free=152731880(152731880), Frag=0.00%.
[MemPool]: B#46: Ent=2675, FA=2674, Free=152729280(152729280), Frag=0.00%.
[MemPool]: B#47: Ent=2675, FA=2674, Free=152732656(152732656), Frag=0.00%.
[MemPool]: B#48: Ent=2675, FA=2674, Free=152729096(152729096), Frag=0.00%.
[MemPool]: B#49: Ent=2675, FA=2674, Free=152732544(152732544), Frag=0.00%.
[MemPool]: B#50: Ent=2675, FA=2674, Free=152727784(152727784), Frag=0.00%.
[MemPool]: B#51: Ent=2675, FA=2674, Free=146732936(146732936), Frag=0.00%.
[MemPool]: B#52: Ent=2675, FA=2674, Free=144329576(144329576), Frag=0.00%.
[MemPool]: B#53: Ent=2675, FA=2674, Free=152712936(152712936), Frag=0.00%.
[MemPool]: B#54: Ent=2675, FA=2674, Free=151289576(151289576), Frag=0.00%.
[MemPool]: B#55: Ent=2675, FA=2674, Free=147612936(147612936), Frag=0.00%.
[MemPool]: B#56: Ent=2675, FA=2674, Free=152714216(152714216), Frag=0.00%.
[MemPool]: B#57: Ent=2675, FA=2674, Free=152582936(152582936), Frag=0.00%.
[MemPool]: B#58: Ent=2675, FA=2674, Free=152720552(152720552), Frag=0.00%.
[MemPool]: B#59: Ent=2675, FA=2674, Free=152732928(152732928), Frag=0.00%.
[MemPool]: B#60: Ent=2675, FA=2674, Free=152729448(152729448), Frag=0.00%.
[MemPool]: B#61: Ent=2675, FA=2674, Free=152732808(152732808), Frag=0.00%.
[MemPool]: B#62: Ent=2675, FA=2674, Free=152729448(152729448), Frag=0.00%.
[MemPool]: B#63: Ent=2675, FA=2674, Free=152732928(152732928), Frag=0.00%.
[MemPool]: Memory Pool TOTAL:
[MemPool]: Number of allocated entities: 171233.
[MemPool]: Number of all/(LazyGarbageCollected) RBT-nodes: 342530/(342530)
.
[MemPool]: Allocated size: 376511880Bytes.
[MemPool]: Free space/(LargestFreeBlock): 9598815208/(152732928)Bytes.
[MemPool]: Fragmentation of holes: 0.00%.
[MemPool]: Number of extra multicast references: 0.
[DFMKrnl]: The DFM Kernel init done.
[DFMKrnl]: Global parameters of the BM_DFM Kernel:
[DFMKrnl]: Operation Queue (OQ) size: Q_OQ=20000Entities.
[DFMKrnl]: Data Buffer (DB) size: Q_DB=150000Entities.
[DFMKrnl]: I/O Ring Buffer Port (IORBP) size: Q_IORBP=100Entities.
[DFMKrnl]: Number of the IORBPs: N_IORBP=500.
[DFMKrnl]: Number of the main processes (CPU PROCs): N_CPUPROC=32.
[DFMKrnl]: Number of the OQ PROCs: N_OQPROC=32.
[DFMKrnl]: Number of the IORBP PROCs: N_IORBPPROC=32.
[DFMKrnl]: Block size used in OQ search algorithm is 282.
[DFMKrnl]: Size of caches in speculative prediction unit is 5120000Bytes.
[DFMKrnl]: Associative hierarchy of speculative tagging max. 304232000Bytes
.
[DFMKrnl]: Display stall warnings: STALL_WARNINGS=NO.
[DFMKrnl]: Hard array synchronization: HARD_ARRAY_SYNCRO=NO.
[DFMKrnl]: I/O synchronization of external task: EXT_IN_OUT_SYNCRO=YES.
[DFMKrnl]: Compensate ShMem relaxed consistency: RELAXED_CNSTN_SM_MODEL=YES.
[DFMKrnl]: Use SVR4 or POSIX semaphores: POSIX_SEMA4_SYNC=RW+COUNT.
[DFMKrnl]: SVR4 sema4 is replaced with POSIX sema4 where possible.
[SysMsg]: Closing the freeIPC EMERGENCY CASE file...
[SysMsg]: Invoking startup_callback()...
[SysMsg]: Forking up and handshaking the PROCstat daemon...
pipe[PROCstat]: My PID=18778. SHMEM_POOL attached. Init done. (VERSION_BMDFM_SYS : "Sancho M. BMDFMSys V5.9.9.") (Compiled on: "Linux RedHatEL6S62VM 2.6.32-220.13.1.el6.x86_64 #1 SMP Thu Mar 29 11:46:40 EDT 2012 x86_64") (Compiled by: "gcc version 4.4.6 20110731 (Red Hat 4.4.6-3) (GCC) as [ELF 64-bit LSB executable, x86-64, version 1 (SYSV), dynamically linked (uses shared libs), for GNU/Linux 2.6.18, stripped] at systime Fri Mar 13 21:15:16 CET 2015".) (Started at systime Mon Jul 13 11:08:37 2015.)
[DFMGrv]: Fork for the PROCstat daemon done.
[SysMsg]: Forking up and handshaking the CPU PROC daemons...
pipe[CPUPROC#2]: My PID=18780, tID=18782. SHMEM_POOL attached. TermCap init done. FastLisp independent micro-kernel init done. Address cross-reference table is changed. All inits done. (VERSION_BMDFM_SYS : "Sancho M. BMDFMSys V5.9.9.") (Compiled on: "Linux RedHatEL6S62VM 2.6.32-220.13.1.el6.x86_64 #1 SMP P Thu Mar 29 11:46:40 EDT 2012 x86_64") (Compiled by: "gcc version 4.4.6 20110731 (Red Hat 4.4.6-3) (GCC) as [ELF 64-bit LSB executable, x86-64, version 1 (SYSV), dynamically linked (uses shared libs), for GNU/Linux 2.6.18, stripped] at systime Fri Mar 13 21:15:17 CET 2015".) (Started at systime Mon Ju 13 11:08:37 2015.)
pipe[CPUPROC#4]: My PID=18780, tID=18784. SHMEM_POOL attached. TermCap init done. FastLisp independent micro-kernel init done. Address cross-reference table is changed. All inits done. (VERSION_BMDFM_SYS : "Sancho M. BMDFMSys V5.9.9.") (Compiled on: "Linux RedHatEL6S62VM 2.6.32-220.13.1.el6.x86_64 #1 SMP P Thu Mar 29 11:46:40 EDT 2012 x86_64") (Compiled by: "gcc version 4.4.6 20110731 (Red Hat 4.4.6-3) (GCC) as [ELF 64-bit LSB executable, x86-64, version 1 (SYSV), dynamically linked (uses shared libs), for GNU/Linux 2.6.18, stripped] at systime Fri Mar 13 21:15:17 CET 2015".) (Started at systime Mon Ju 13 11:08:37 2015.)
pipe[CPUPROC#1]: My PID=18780, tID=18781. SHMEM_POOL attached. TermCap init done. FastLisp independent micro-kernel init done. Address cross-reference table is changed. All inits done. (VERSION_BMDFM_SYS : "Sancho M. BMDFMSys V5.9.9.") (Compiled on: "Linux RedHatEL6S62VM 2.6.32-220.13.1.el6.x86_64 #1 SMP P Thu Mar 29 11:46:40 EDT 2012 x86_64") (Compiled by: "gcc version 4.4.6 20110731 (Red Hat 4.4.6-3) (GCC) as [ELF 64-bit LSB executable, x86-64, version 1 (SYSV), dynamically linked (uses shared libs), for GNU/Linux 2.6.18, stripped] at systime Fri Mar 13 21:15:17 CET 2015".) (Started at systime Mon Ju 13 11:08:37 2015.)
pipe[CPUPROC#3]: My PID=18780, tID=18783. SHMEM_POOL attached. TermCap init done. FastLisp independent micro-kernel init done. Address cross-reference table is changed. All inits done. (VERSION_BMDFM_SYS : "Sancho M. BMDFMSys V5.9.9.") (Compiled on: "Linux RedHatEL6S62VM 2.6.32-220.13.1.el6.x86_64 #1 SMP P Thu Mar 29 11:46:40 EDT 2012 x86_64") (Compiled by: "gcc version 4.4.6 20110731 (Red Hat 4.4.6-3) (GCC) as [ELF 64-bit LSB executable, x86-64, version 1 (SYSV), dynamically linked (uses shared libs), for GNU/Linux 2.6.18, stripped] at systime Fri Mar 13 21:15:17 CET 2015".) (Started at systime Mon Ju 13 11:08:37 2015.)
pipe[CPUPROC#5]: My PID=18780, tID=18785. SHMEM_POOL attached. TermCap init done. FastLisp independent micro-kernel init done. Address cross-reference table is changed. All inits done. (VERSION_BMDFM_SYS : "Sancho M. BMDFMSys V5.9.9.") (Compiled on: "Linux RedHatEL6S62VM 2.6.32-220.13.1.el6.x86_64 #1 SMP P Thu Mar 29 11:46:40 EDT 2012 x86_64") (Compiled by: "gcc version 4.4.6 20110731 (Red Hat 4.4.6-3) (GCC) as [ELF 64-bit LSB executable, x86-64, version 1 (SYSV), dynamically linked (uses shared libs), for GNU/Linux 2.6.18, stripped] at systime Fri Mar 13 21:15:17 CET 2015".) (Started at systime Mon Ju 13 11:08:37 2015.)
pipe[CPUPROC#6]: My PID=18780, tID=18786. SHMEM_POOL attached. TermCap init done. FastLisp independent micro-kernel init done. Address cross-reference t
```

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able is changed. All inits done. (VERSION_BMDFM_SYS : "Sancho M. BMDFMSys V5.9.9.") (Compiled on: "Linux RedHatEL6S62VM 2.6.32-220.13.1.el6.x86_64 #1 SM P Thu Mar 29 11:46:40 EDT 2012 x86_64") (Compiled by: "gcc version 4.4.6 20110731 (Red Hat 4.4.6-3) (GCC) as [ELF 64-bit LSB executable, x86-64, versio n 1 (SYSV), dynamically linked (uses shared libs), for GNU/Linux 2.6.18, str ipped] at systime Fri Mar 13 21:15:17 CET 2015".) (Started at systime Mon Ju 13 11:08:37 2015.)
pipe[CPUPROC#7]: My PID=18780, tID=18787. SHMEM_POOL attached. TermCap init done. FastLisp independent micro-kernel init done. Address cross-reference t able is changed. All inits done. (VERSION_BMDFM_SYS : "Sancho M. BMDFMSys V5.9.9.") (Compiled on: "Linux RedHatEL6S62VM 2.6.32-220.13.1.el6.x86_64 #1 SM P Thu Mar 29 11:46:40 EDT 2012 x86_64") (Compiled by: "gcc version 4.4.6 20110731 (Red Hat 4.4.6-3) (GCC) as [ELF 64-bit LSB executable, x86-64, versio n 1 (SYSV), dynamically linked (uses shared libs), for GNU/Linux 2.6.18, str ipped] at systime Fri Mar 13 21:15:17 CET 2015".) (Started at systime Mon Ju 13 11:08:37 2015.)
pipe[CPUPROC#8]: My PID=18780, tID=18788. SHMEM_POOL attached. TermCap init done. FastLisp independent micro-kernel init done. Address cross-reference t able is changed. All inits done. (VERSION_BMDFM_SYS : "Sancho M. BMDFMSys V5.9.9.") (Compiled on: "Linux RedHatEL6S62VM 2.6.32-220.13.1.el6.x86_64 #1 S MP Thu Mar 29 11:46:40 EDT 2012 x86_64") (Compiled by: "gcc version 4.4.6 20110731 (Red Hat 4.4.6-3) (GCC) as [ELF 64-bit LSB executable, x86-64, versi on 1 (SYSV), dynamically linked (uses shared libs), for GNU/Linux 2.6.18, str ipped] at systime Fri Mar 13 21:15:17 CET 2015".) (Started at systime Mon Ju 13 11:08:37 2015.)
pipe[CPUPROC#23]: My PID=18780, tID=18803. SHMEM_POOL attached. TermCap init done. FastLisp independent micro-kernel init done. Address cross-reference table is changed. All inits done. (VERSION_BMDFM_SYS : "Sancho M. BMDFMSys V 5.9.9.") (Compiled on: "Linux RedHatEL6S62VM 2.6.32-220.13.1.el6.x86_64 #1 S MP Thu Mar 29 11:46:40 EDT 2012 x86_64") (Compiled by: "gcc version 4.4.6 2 0110731 (Red Hat 4.4.6-3) (GCC) as [ELF 64-bit LSB executable, x86-64, versi on 1 (SYSV), dynamically linked (uses shared libs), for GNU/Linux 2.6.18, st ripped] at systime Fri Mar 13 21:15:17 CET 2015".) (Started at systime Mon J ul 13 11:08:37 2015.)
pipe[CPUPROC#18]: My PID=18780, tID=18798. SHMEM_POOL attached. TermCap init done. FastLisp independent micro-kernel init done. Address cross-reference table is changed. All inits done. (VERSION_BMDFM_SYS : "Sancho M. BMDFMSys V 5.9.9.") (Compiled on: "Linux RedHatEL6S62VM 2.6.32-220.13.1.el6.x86_64 #1 S MP Thu Mar 29 11:46:40 EDT 2012 x86_64") (Compiled by: "gcc version 4.4.6 2 0110731 (Red Hat 4.4.6-3) (GCC) as [ELF 64-bit LSB executable, x86-64, versi on 1 (SYSV), dynamically linked (uses shared libs), for GNU/Linux 2.6.18, st ripped] at systime Fri Mar 13 21:15:17 CET 2015".) (Started at systime Mon J ul 13 11:08:37 2015.)
pipe[CPUPROC#17]: My PID=18780, tID=18797. SHMEM_POOL attached. TermCap init done. FastLisp independent micro-kernel init done. Address cross-reference table is changed. All inits done. (VERSION_BMDFM_SYS : "Sancho M. BMDFMSys V 5.9.9.") (Compiled on: "Linux RedHatEL6S62VM 2.6.32-220.13.1.el6.x86_64 #1 S MP Thu Mar 29 11:46:40 EDT 2012 x86_64") (Compiled by: "gcc version 4.4.6 2 0110731 (Red Hat 4.4.6-3) (GCC) as [ELF 64-bit LSB executable, x86-64, versi on 1 (SYSV), dynamically linked (uses shared libs), for GNU/Linux 2.6.18, st ripped] at systime Fri Mar 13 21:15:17 CET 2015".) (Started at systime Mon J ul 13 11:08:37 2015.)
pipe[CPUPROC#21]: My PID=18780, tID=18801. SHMEM_POOL attached. TermCap init done. FastLisp independent micro-kernel init done. Address cross-reference table is changed. All inits done. (VERSION_BMDFM_SYS : "Sancho M. BMDFMSys V 5.9.9.") (Compiled on: "Linux RedHatEL6S62VM 2.6.32-220.13.1.el6.x86_64 #1 S MP Thu Mar 29 11:46:40 EDT 2012 x86_64") (Compiled by: "gcc version 4.4.6 2 0110731 (Red Hat 4.4.6-3) (GCC) as [ELF 64-bit LSB executable, x86-64, versi on 1 (SYSV), dynamically linked (uses shared libs), for GNU/Linux 2.6.18, st ripped] at systime Fri Mar 13 21:15:17 CET 2015".) (Started at systime Mon J ul 13 11:08:37 2015.)
pipe[CPUPROC#14]: My PID=18780, tID=18794. SHMEM_POOL attached. TermCap init done. FastLisp independent micro-kernel init done. Address cross-reference table is changed. All inits done. (VERSION_BMDFM_SYS : "Sancho M. BMDFMSys V 5.9.9.") (Compiled on: "Linux RedHatEL6S62VM 2.6.32-220.13.1.el6.x86_64 #1 S MP Thu Mar 29 11:46:40 EDT 2012 x86_64") (Compiled by: "gcc version 4.4.6 2 0110731 (Red Hat 4.4.6-3) (GCC) as [ELF 64-bit LSB executable, x86-64, versi on 1 (SYSV), dynamically linked (uses shared libs), for GNU/Linux 2.6.18, st ripped] at systime Fri Mar 13 21:15:17 CET 2015".) (Started at systime Mon J ul 13 11:08:37 2015.)
pipe[CPUPROC#24]: My PID=18780, tID=18804. SHMEM_POOL attached. TermCap init done. FastLisp independent micro-kernel init done. Address cross-reference table is changed. All inits done. (VERSION_BMDFM_SYS : "Sancho M. BMDFMSys V 5.9.9.") (Compiled on: "Linux RedHatEL6S62VM 2.6.32-220.13.1.el6.x86_64 #1 S MP Thu Mar 29 11:46:40 EDT 2012 x86_64") (Compiled by: "gcc version 4.4.6 2 0110731 (Red Hat 4.4.6-3) (GCC) as [ELF 64-bit LSB executable, x86-64, versi on 1 (SYSV), dynamically linked (uses shared libs), for GNU/Linux 2.6.18, st ripped] at systime Fri Mar 13 21:15:17 CET 2015".) (Started at systime Mon J ul 13 11:08:37 2015.)
pipe[CPUPROC#12]: My PID=18780, tID=18792. SHMEM_POOL attached. TermCap init done. FastLisp independent micro-kernel init done. Address cross-reference table is changed. All inits done. (VERSION_BMDFM_SYS : "Sancho M. BMDFMSys V 5.9.9.") (Compiled on: "Linux RedHatEL6S62VM 2.6.32-220.13.1.el6.x86_64 #1 S MP Thu Mar 29 11:46:40 EDT 2012 x86_64") (Compiled by: "gcc version 4.4.6 2 0110731 (Red Hat 4.4.6-3) (GCC) as [ELF 64-bit LSB executable, x86-64, versi on 1 (SYSV), dynamically linked (uses shared libs), for GNU/Linux 2.6.18, st ripped] at systime Fri Mar 13 21:15:17 CET 2015".) (Started at systime Mon J ul 13 11:08:37 2015.)
pipe[CPUPROC#13]: My PID=18780, tID=18793. SHMEM_POOL attached. TermCap init done. FastLisp independent micro-kernel init done. Address cross-reference table is changed. All inits done. (VERSION_BMDFM_SYS : "Sancho M. BMDFMSys V 5.9.9.") (Compiled on: "Linux RedHatEL6S62VM 2.6.32-220.13.1.el6.x86_64 #1 S MP Thu Mar 29 11:46:40 EDT 2012 x86_64") (Compiled by: "gcc version 4.4.6 2 0110731 (Red Hat 4.4.6-3) (GCC) as [ELF 64-bit LSB executable, x86-64, versi on 1 (SYSV), dynamically linked (uses shared libs), for GNU/Linux 2.6.18, st ripped] at systime Fri Mar 13 21:15:17 CET 2015".) (Started at systime Mon J ul 13 11:08:37 2015.)
pipe[CPUPROC#20]: My PID=18780, tID=18800. SHMEM_POOL attached. TermCap init done. FastLisp independent micro-kernel init done. Address cross-reference table is changed. All inits done. (VERSION_BMDFM_SYS : "Sancho M. BMDFMSys V 5.9.9.") (Compiled on: "Linux RedHatEL6S62VM 2.6.32-220.13.1.el6.x86_64 #1 S MP Thu Mar 29 11:46:40 EDT 2012 x86_64") (Compiled by: "gcc version 4.4.6 2 0110731 (Red Hat 4.4.6-3) (GCC) as [ELF 64-bit LSB executable, x86-64, versi on 1 (SYSV), dynamically linked (uses shared libs), for GNU/Linux 2.6.18, st ripped] at systime Fri Mar 13 21:15:17 CET 2015".) (Started at systime Mon J ul 13 11:08:37 2015.)
pipe[CPUPROC#19]: My PID=18780, tID=18799. SHMEM_POOL attached. TermCap init done. FastLisp independent micro-kernel init done. Address cross-reference table is changed. All inits done. (VERSION_BMDFM_SYS : "Sancho M. BMDFMSys V 5.9.9.") (Compiled on: "Linux RedHatEL6S62VM 2.6.32-220.13.1.el6.x86_64 #1 S MP Thu Mar 29 11:46:40 EDT 2012 x86_64") (Compiled by: "gcc version 4.4.6 2 0110731 (Red Hat 4.4.6-3) (GCC) as [ELF 64-bit LSB executable, x86-64, versi on 1 (SYSV), dynamically linked (uses shared libs), for GNU/Linux 2.6.18, st ripped] at systime Fri Mar 13 21:15:17 CET 2015".) (Started at systime Mon J ul 13 11:08:37 2015.)
pipe[CPUPROC#26]: My PID=18780, tID=18806. SHMEM_POOL attached. TermCap init done. FastLisp independent micro-kernel init done. Address cross-reference table is changed. All inits done. (VERSION_BMDFM_SYS : "Sancho M. BMDFMSys V 5.9.9.") (Compiled on: "Linux RedHatEL6S62VM 2.6.32-220.13.1.el6.x86_64 #1 S MP Thu Mar 29 11:46:40 EDT 2012 x86_64") (Compiled by: "gcc version 4.4.6 2 0110731 (Red Hat 4.4.6-3) (GCC) as [ELF 64-bit LSB executable, x86-64, versi on 1 (SYSV), dynamically linked (uses shared libs), for GNU/Linux 2.6.18, st
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= Page 28 of 79 =

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9 of 79 – <http://bmdfm.com>



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[DFMSrv]:
[DFMSrv]:
[DFMSrv]: DB Listener queue stir-up
[DFMSrv]: Asynchronous heap blocking
[DFMSrv]: Queue of busy I/O: EntNumb
[DFMSrv]: Queue of busy I/O: ReadBlk
[DFMSrv]: 492 Free IORBP entities
[DFMSrv]: DB Loader queue blocking
[DFMSrv]: DB Loader queue stir-up
[DFMSrv]: DB Listener queue blocking
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[DFMSrv]: DB Listener queue stir-up
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[DFMSrv]: 494 Free IORBP entities
[DFMSrv]: DB Loader queue blocking
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[DFMSrv]: DB Listener queue blocking
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[DFMSrv]: DB Listener queue stir-up
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[DFMSrv]: Queue of busy I/O: EntNumb
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[DFMSrv]: 495 Free IORBP entities
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[DFMSrv]: Asynchronous heap blocking
[DFMSrv]: Queue of busy I/O: EntNumb
[DFMSrv]: Queue of busy I/O: ReadBlk
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#          (@ @)          #
#          oOo--( )--oOo-- #
#          BMDFM PROJECT  #
#          Oleksandr Pochayevets (aka Sancho M.) #
#          | | | |         #
#          oOo Ooo         #
#
#####
##                               ##
## End User License Agreement (EULA) ##
##                               ##
#####

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```

[Msg]: Title_banner
#####
[Ver]: Binary Modular Data-Flow Machine (BM DFM) Release History
[Ver]: and Codenames:
[Ver]:   Years      Versions      BM_DFM Codename      Release
[Ver]:   -----
[Ver]:   1996-1997   0.0.1-1.9.9   "Bare Metal" DFM      Official
[Ver]:   1998-1999   2.0.0-2.9.9   "Big Monster" DFM      Unofficial
[Ver]:   2000-2001   3.0.0-3.9.9   "Beast Master" DFM      Unofficial
[Ver]:   2002-2003   4.0.0-4.9.9   "Behemoth Mighty" DFM  Official
[Ver]:   2004-2015   5.0.0-5.9.9   "Broken Mind" DFM      Official
[Ver]: VERSION BMDFM SYS = "Sancho M. BMDFMSys V5.9.9." # The BM DFM Server.
[Ver]: VERSION TERMCAP = "Sancho M. TermCap v.1.2.0." # Term capabilities.
[Ver]: VERSION FSTLISP = "Sancho M. FstLisp v.2.9.6." # FastLisp RTEngine.
[Ver]: VERSION CFLPUDEF = "Sancho M. CFLPUDEF v.1.0.0." # FastLSP UDFs in C.
[Ver]: VERSION STRGLIB = "Sancho M. StrgLib v.2.2.5." # FstString library.
[Ver]: VERSION MEMPOOL = "Sancho M. MemPool v.2.8.8." # ShMem Pool driver.
[TermCap]: ----- Server is running on TERM=xterm (80x24).
[SysMsg]: Overall machinery init for the virtual out-of-order general purpose processing was completed at systime Mon Jul 13 11:08:39 2015.
[SysMsg]: Going simultaneous jobs running all the threads in parallel...
[DFMSrv]: All resources were unhooked and invoked successfully!
[SysMsg]: The complete "Broken Mind" Data-Flow Machine Server has been fully started.
[SysMsg]: The entire DFM SMP MIMD architecture is ready for dynamic scheduling now.
[Legacy_MainFrame_Initial_Greeting_Message]: GOOD MORNING.

Console input: stat on
[SysMsg]: ===== System time is Mon Jul 13 11:08:42 2015. =====
[DFMSrv]: Statistic and status line are switched on.
[DFMSrv]: Info will be displayed on the event registering console bar.

Console input: reset time
[SysMsg]: ===== System time is Mon Jul 13 11:08:50 2015. =====
[SysMsg]: Sending SIG RESET TIME to the CPU PROCs...
[SysMsg]: Sending SIG RESET TIME to the OQ PROCs...
[SysMsg]: Sending SIG RESET TIME to the IORBP PROCs...
[DFMSrv]: Okay. Good. Every child PROC has been told.
[DFMSrv]: The child time counters have been reset.
[SysMsg]: A message routed out of the NPIPE at systime Mon Jul 13 11:08:51 2015.
npipe[ExtTaskLd#0]: Task Loader PID=19173. Connection acknowledged. (VERSION BMDFM SYS: "Sancho M. BMDFMSys V5.9.9.") ( COMPILED ON: "Linux RedHatELSL6 2VM 2.6.32-220.13.1.el6.x86_64 #1 SMP Thu Mar 29 11:46:40 EDT 2012 x86_64.") ( COMPILED BY: "gcc version 4.4.6 20110731 (Red Hat 4.4.6-3) (GCC) as [ELF 64-bit LSB executable, x86-64, version 1 (SYSV), dynamically linked (uses shared libs), for GNU/Linux 2.6.18, stripped] at systime Fri Mar 13 21:14:59 C ET 2015".) Commenced (logged in) at systime Mon Jul 13 11:08:51 2015. USR_TA SKJOB_NAME="Fibonacci.flp". [MSG#0]
[SysMsg]: A message routed out of the NPIPE at systime Mon Jul 13 11:08:51 2015.
npipe[ExtTaskLs#0]: Task Listener PID=19173, tID=19174. Connection acknowledged. (VERSION BMDFM SYS: "Sancho M. BMDFMSys V5.9.9.") ( COMPILED ON: "Linux RedHatELSL62VM 2.6.32-220.13.1.el6.x86_64 #1 SMP Thu Mar 29 11:46:40 EDT 2012 x86_64.") ( COMPILED BY: "gcc version 4.4.6 20110731 (Red Hat 4.4.6-3) (GCC) as [ELF 64-bit LSB executable, x86-64, version 1 (SYSV), dynamically linked (uses shared libs), for GNU/Linux 2.6.18, stripped] at systime Fri Mar 13 21:14:59 CBT 2015".) Commenced (logged in) at systime Mon Jul 13 11:08:51 2015. USR_TASKJOB_NAME="Fibonacci.flp". [MSG#1]
[SysMsg]: A message received from the pipe at systime Mon Jul 13 11:08:51 2015.
pipe[IORBPPROC#30]: Initialization has been done on SocketN# 0. [msg#0]
[SysMsg]: A message received from the pipe at systime Mon Jul 13 11:08:59 2015.
pipe[IORBPPROC#16]: RESOURCE RELEASE CTRL SEQUENCE has been started on SocketN# 0. IORBPPROC#16(PID=18845,tID=18861) will take care of it. [msg#1]
[SysMsg]: A message routed out of the NPIPE at systime Mon Jul 13 11:08:59 2015.
npipe[ExtTaskLd#0]: External Loader/Listener pair (PID=19173/PID=19173,tID=19174) is detached (logged out) at systime Mon Jul 13 11:08:59 2015. USR_TASKJOB_NAME="Fibonacci.flp". [MSG#2]
[SysMsg]: A message received from the pipe at systime Mon Jul 13 11:09:01 2015.
pipe[PROCstat]: LOAD AVERAGE within the systime period [Mon Jul 13 11:08:52 2015; Mon Jul 13 11:09:01 2015] {i01/50000|db37/150000|oq102/20000|cpu25/32<1} 63% utilization. RECENT PEAK LOAD: [PROCstat]: Jul 13 11:08:58 {i00/50000|db37/150000|oq138/20000|cpu32<3} 80% utilization [msg#2]

Console input: get count
[SysMsg]: ===== System time is Mon Jul 13 11:09:01 2015. =====
[SysMsg]: Sending SIG GET TIME to the CPU PROCs...
pipe[CPUPROC#0]: USRs=236, USRs=130000, SYSs=0, SYSus=230000.
[SysMsg]: Sending SIG GET TIME to the OQ PROCs...
pipe[OQPROC#0]: USRs=0, USRs=60000, SYSs=0, SYSus=50000.
[SysMsg]: Sending SIG GET TIME to the IORBP PROCs...
pipe[IORBPPROC#0]: USRs=0, USRs=80000, SYSs=0, SYSus=30000.
[SysMsg]: ***** GENERAL PERFORMANCE BENCHMARKS *****
[SysMsg]: WARNING: CPU PROC is multithreaded, thread times are not measured separately.
[SysMsg]: WARNING: OQ PROC is multithreaded, thread times are not measured separately.

```



```
[CPUPROC#13]: BEGIN at (sec=1436778539, usec=754121)
TaskCntx_ID = #500
FstLispCode = `(SETQ@I MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000000@I (+@J
MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000001@I
MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000002@I)')
AddressRefs = <15:1:1>[0] (#3) [S$0$], <16:1:1>[0] (#3) [S$0$],
<17:1:1>[0] (#3) [S$0$] /* MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000000@I,
MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000001@I,
MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000002@I */
END OF CPUPROC ENTRY at (sec=1436778539, usec=754163)
[CPUPROC#13]: BEGIN at (sec=1436778539, usec=754292)
```

```
*You may recompile the `BMDPMLdr' with commented `#define _NOISY_MODEL_'
to disable print of the linked Global function bytecode.
Connection with the BM DPM Server has been established but not yet registered.
Checking whether the `Fibonacci master.flp' file is already precompiled...
Reading the `Fibonacci master.flp' source FastLisp file...
```

```

*** Resetting time counters (first null assignment)... ***
Modifying the FastLisp code (PATTERN No# 1)...
(PROGN <Global FastLisp function set> <FastLisp_prog>)
Checking the syntax of the source FastLisp file...
Modifying the FastLisp code (PATTERN No# 3)...
(PROGN {(SETQ <termcap_var> <termcap_val>)} <FastLisp_prog>})
Looking for uninitialized variables/arrays in the FastLisp code...
Checking the CODE STYLE RESTRICTIONS for the BM DFM parallel processing...
*****
* Summary of the BM DFM CODE STYLE RESTRICTIONS:
* -----
*
* o Variable names within the inclusive range of
*   ['TMP_000000000'; 'TMP_999999999'] are reserved.
* o 'SHADOW' is the reserved name for a UDF.
* o Array names should differ from ordinary variable names.
* o Every variable should be initialized before use.
* The following is an example of how to copy an array:
*
*   ...
*   (arsetq a 0 1)
*   (arsetq a 1 5)
*   (alsetq b (alindex a 2)) # instead of `(setq b a)'
*   ...
* o The <step> and <limit> values of a <for> loop should be
*   the integer numeric constants, function arguments or
*   initialized variables which are not changed inside this
*   <for> loop.
* o Second argument of the booleans <or> and <and> should
*   not include any assignments, I/O, conditional/
*   iteration processing and UDF calls.
*
* NOTE: Any conventional program can be converted by a
*       formal procedure to the program that is compliant
*       with the above mentioned code style restrictions.
*
*****
*You may recompile BMDFMldr module with commented `#define EXPLAIN_RULE'
*to disable print of the code style restriction rule summary.
Modifying the FastLisp code (PATTERN No# 4)...
(PROGN {(SETQ $<arg numb> <arg val>)} <FastLisp_prog>})
Squeezing the nested source PROGN statements...
Redundant nested source PROGN statements removed: 2.
Modifying the FastLisp code (PATTERN No# 5)...
(PROGN (OUTF (PRN_STRING_FMT) (CAT "" <FastLisp_prog>))) "")
Reorganizing the FastLisp code...
Resolving data types in the FastLisp code...
Registering in the BM DFM Server Task Connection Zone...
Porking up the message queue listener...
Listener engine has been commenced.
The Loader/Listener pair is fully attached by the BM DFM Server:
  Loader PID=24446, Listener PID=24446, SocketN# is 0.
-----
(PROGN
  (SETQ@I MAIN:$1 50)
  (SETQ@S MAIN:TERM_TYPE@S "xterm")
  (SETQ@I MAIN:LINES_TERM@I 24)
  (SETQ@I MAIN:COLUMNS_TERM@I 80)
  (SETQ@S MAIN:CLRSKR_TERM@S "\e[H\e[2J")
  (SETQ@S MAIN:REVERSE_TERM@S "\e[7m")
  (SETQ@S MAIN:BLINK_TERM@S "\e[5m")
  (SETQ@S MAIN:BOLD_TERM@S "\e[1m")
  (SETQ@S MAIN:NORMAL_TERM@S "\e[0m")
  (SETQ@S MAIN:HIDECURSOR_TERM@S "\e[?251")
  (SETQ@S MAIN:SHOWCURSOR_TERM@S "\e[?121\e[?25h")
  (SETQ@S MAIN:GOTOCURSOR_TERM@S "\e[%i%d;%dH")
  (DEFUN
    MAIN:FIBONACCICOORDINATOR
    (PROGN
      (DEFUN
        MAIN:FIBONACCICOORDINATOR:SHADOW
        (PROGN
          (SETQ@I
            MAIN:FIBONACCICOORDINATOR:SHADOW:N@I
            (+ 0 MAIN:FIBONACCICOORDINATOR:SHADOW:$1)
          )
          (SETQ@I
            MAIN:FIBONACCICOORDINATOR:SHADOW:SPAWN@I
            (+ 0 MAIN:FIBONACCICOORDINATOR:SHADOW:$2)
          )
          (SETQ@I
            MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000004@I
            (<@I MAIN:FIBONACCICOORDINATOR:SHADOW:N@I 2)
          )
          (IF@J
            MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000004@I
            (SETQ@I
              MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000000@I
              MAIN:FIBONACCICOORDINATOR:SHADOW:N@I
            )
            (PROGN
              (SETQ@I
                MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000003@I
                (>@I MAIN:FIBONACCICOORDINATOR:SHADOW:SPAWN@I 0)
              )
              (IF@J
                MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000003@I
                (PROGN
                  (SETQ@I
                    MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000001
                    (MAIN:FIBONACCICOORDINATOR
                     (-@J MAIN:FIBONACCICOORDINATOR:SHADOW:N@I)
                     (>@J MAIN:FIBONACCICOORDINATOR:SHADOW:SPAWN@I 1)
                    )
                  )
                  (SETQ@I
                    MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000002
                    (MAIN:FIBONACCICOORDINATOR
                     (-@J MAIN:FIBONACCICOORDINATOR:SHADOW:N@I 2)
                     (>@J MAIN:FIBONACCICOORDINATOR:SHADOW:SPAWN@I 1)
                    )
                  )
                  (SETQ@I
                    MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000000@I
                    (+@J
                     MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000001
                     MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000002
                    )
                  )
                )
              )
            )
          )
        )
      )
    )
  )
  (PROGN
    (SETQ@S
      MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000001
      (BMDFM2BMDFM_OFFLOAD@J
        "Fibonacci"
        (STR@I (--@J MAIN:FIBONACCICOORDINATOR:SHADOW:N@I) 1)
      )
    )
    (SETQ@S
      MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000002
      (BMDFM2BMDFM_OFFLOAD@J
        "Fibonacci"
        (STR@I (-@J MAIN:FIBONACCICOORDINATOR:SHADOW:N@I 2))
      )
    )
    (SETQ@I
      MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000000@I
      (+@J
        MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000001
        MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000002
      )
    )
    (PROGN
      (SETQ@S
        MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000001
        (BMDFM2BMDFM_OFFLOAD@J
          "Fibonacci"
          (STR@I (--@J MAIN:FIBONACCICOORDINATOR:SHADOW:N@I) 1)
        )
      )
      (SETQ@S
        MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000002
        (BMDFM2BMDFM_OFFLOAD@J
          "Fibonacci"
          (STR@I (-@J MAIN:FIBONACCICOORDINATOR:SHADOW:N@I 2))
        )
      )
      (SETQ@I
        MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000000@I
        (+@J
          MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000001
          MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000002
        )
      )
    )
    (DEFUN
      MAIN:FIBONACCI
      (PROGN
        (SETQ@I MAIN:FIBONACCI:N@I (+ 0 MAIN:FIBONACCI:$1))
        (SETQ@I MAIN:FIBONACCI:SPAWN@I (N_CPU@PROC))
        (SETQ@I
          MAIN:FIBONACCI:TMP_000000000@I
          (MAIN:FIBONACCICOORDINATOR MAIN:FIBONACCI:N@I MAIN:FIBONACCI:SPAWN@I)
        )
        (SETQ@I MAIN:N@I (+@J 0 MAIN:$1))
        (SETQ@I MAIN:TMP_000000001 (MAIN:FIBONACCI MAIN:N@I))
        (SETQ@S
          MAIN:TMP_000000001
          (OUTF (PRN_STRING_FMT) (CAT "" MAIN:TMP_000000001))
        )
        (SETQ@S MAIN:TMP_000000000@S "")
      )
    )
  )
  (PROGN (SETQ@I MAIN:$1 50) (SETQ@S MAIN:TERM_TYPE@S "xterm") (SETQ@I MAIN:LINES_TERM@I 24) (SETQ@I MAIN:COLUMNS_TERM@I 80) (SETQ@S MAIN:CLRSKR_TERM@S "\e[H\e[2J") (SETQ@S MAIN:REVERSE_TERM@S "\e[7m") (SETQ@S MAIN:BLINK_TERM@S "\e[5m") (S

```

```

MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000001
(BMDFM2BMDFM_OFFLOAD@J
  "Fibonacci"
  (STR@I (--@J MAIN:FIBONACCICOORDINATOR:SHADOW:N@I) 1)
)
)
(SETQ@S
  MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000002
  (BMDFM2BMDFM_OFFLOAD@J
    "Fibonacci"
    (STR@I (-@J MAIN:FIBONACCICOORDINATOR:SHADOW:N@I 2)
    )
  )
  (SETQ@I
    MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000000@I
    (+@J
      (IVAL MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000001)
      (IVAL MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000002)
    )
  )
)
)
)
)
)
)
)
)
)
)
(SETQ@I MAIN:FIBONACCICOORDINATOR:N@I (+ 0 MAIN:FIBONACCICOORDINATOR:$1))
(SETQ@I
  MAIN:FIBONACCICOORDINATOR:SPAWN@I
  (+ 0 MAIN:FIBONACCICOORDINATOR:$2)
)
(SETQ@I
  MAIN:FIBONACCICOORDINATOR:TMP_000000004@I
  (<@I MAIN:FIBONACCICOORDINATOR:N@I 2)
)
(IF@J
  MAIN:FIBONACCICOORDINATOR:TMP_000000004@I
  (SETQ@I
    MAIN:FIBONACCICOORDINATOR:TMP_000000000@I
    MAIN:FIBONACCICOORDINATOR:N@I
  )
  (PROGN
    (SETQ@I
      MAIN:FIBONACCICOORDINATOR:TMP_000000003@I
      (>@I MAIN:FIBONACCICOORDINATOR:SPAWN@I 0)
    )
    (IF@J
      MAIN:FIBONACCICOORDINATOR:TMP_000000003@I
      (PROGN
        (SETQ@I
          MAIN:FIBONACCICOORDINATOR:TMP_000000001
          (MAIN:FIBONACCICOORDINATOR:SHADOW
           (--@J MAIN:FIBONACCICOORDINATOR:N@I)
           (>@J MAIN:FIBONACCICOORDINATOR:SPAWN@I 1)
          )
        )
        (SETQ@I
          MAIN:FIBONACCICOORDINATOR:TMP_000000002
          (MAIN:FIBONACCICOORDINATOR:SHADOW
           (-@J MAIN:FIBONACCICOORDINATOR:N@I 2)
           (>@J MAIN:FIBONACCICOORDINATOR:SPAWN@I 1)
          )
        )
        (SETQ@I
          MAIN:FIBONACCICOORDINATOR:TMP_000000000@I
          (+@J
            MAIN:FIBONACCICOORDINATOR:TMP_000000001
            MAIN:FIBONACCICOORDINATOR:TMP_000000002
          )
        )
      )
    )
    (PROGN
      (SETQ@S
        MAIN:FIBONACCICOORDINATOR:TMP_000000001
        (BMDFM2BMDFM_OFFLOAD@J
          "Fibonacci"
          (STR@I (--@J MAIN:FIBONACCICOORDINATOR:N@I) 1)
        )
      )
      (SETQ@S
        MAIN:FIBONACCICOORDINATOR:TMP_000000002
        (BMDFM2BMDFM_OFFLOAD@J
          "Fibonacci"
          (STR@I (-@J MAIN:FIBONACCICOORDINATOR:N@I 2))
        )
      )
      (SETQ@I
        MAIN:FIBONACCICOORDINATOR:TMP_000000000@I
        (+@J
          MAIN:FIBONACCICOORDINATOR:TMP_000000001
          MAIN:FIBONACCICOORDINATOR:TMP_000000002
        )
      )
    )
    (PROGN
      (SETQ@I MAIN:$1 50) (SETQ@S MAIN:TERM_TYPE@S "xterm") (SETQ@I MAIN:LINES_TERM@I 24) (SETQ@I MAIN:COLUMNS_TERM@I 80) (SETQ@S MAIN:CLRSKR_TERM@S "\e[H\e[2J") (SETQ@S MAIN:REVERSE_TERM@S "\e[7m") (SETQ@S MAIN:BLINK_TERM@S "\e[5m") (S

```

```

EQQ@S MAIN:BOLD_TERM@S "\e[1m") (SETQ@S MAIN:NORMAL_TERM@S "\e[0m") (SETQ@S MAI
N:HIDECURSOR_TERM@S "\e[?251]") (SETQ@S MAIN:SHOWCURSOR_TERM@S "\e[?121\e[?25h")
(SETQ@S MAIN:GOTOCURSOR_TERM@S "\e[%i%d;%dH") (DEFUN MAIN:FIBONACCICOORDINATOR
(PROGN (DEFUN MAIN:FIBONACCICOORDINATOR:SHADOW (PROGN (SETQ@I MAIN:FIBONACCICO
ORDINATOR:SHADOW:N@I (+ 0 MAIN:FIBONACCICOORDINATOR:SHADOW:$1)) (SETQ@I MAIN:FI
BONACCICOORDINATOR:SHADOW:SPAWN@I (+ 0 MAIN:FIBONACCICOORDINATOR:SHADOW:$2)) (S
ETQ@I MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000004@I (<@I MAIN:FIBONACCICOOR
DINATOR:SHADOW:N@I 2)) (IF@J MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000004@I
(SETQ@I MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000000@I MAIN:FIBONACCICOORDIN
ATOR:SHADOW:N@I) (PROGN (SETQ@I MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000003
@I (>@I MAIN:FIBONACCICOORDINATOR:SHADOW:SPAWN@I 0)) (IF@J MAIN:FIBONACCICOORDI
NATOR:SHADOW:TMP_000000003@I (PROGN (SETQ@I MAIN:FIBONACCICOORDINATOR:SHADOW:T
MP_000000001 (MAIN:FIBONACCICOORDINATOR (--@J MAIN:FIBONACCICOORDINATOR:SHADOW
:N@I) (>@J MAIN:FIBONACCICOORDINATOR:SHADOW:SPAWN@I 1))) (SETQ@I MAIN:FIBONACC
ICOORDINATOR:SHADOW:TMP_000000002 (MAIN:FIBONACCICOORDINATOR (--@J MAIN:FIBONAC
CICOORDINATOR:SHADOW:N@I 2) (>@J MAIN:FIBONACCICOORDINATOR:SHADOW:SPAWN@I 1)))
(SETQ@I MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000000@I (+@J MAIN:FIBONACCIC
OORDINATOR:SHADOW:TMP_000000001 MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_00000000
2))) (PROGN (SETQ@S MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000001 (BMDFM2BMDFM
M OFFLOAD@J "Fibonacci" (STR@I (--@J MAIN:FIBONACCICOORDINATOR:SHADOW:N@I))) (
SETQ@S MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000002 (BMDFM2BMDFM OFFLOAD@J "
Fibonacci" (STR@I (--@J MAIN:FIBONACCICOORDINATOR:SHADOW:N@I 2))) (SETQ@I MAIN:
FIBONACCICOORDINATOR:SHADOW:TMP_000000000@I (+@J (IVAL MAIN:FIBONACCICOORDINAT
OR:SHADOW:TMP_000000001) (IVAL MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000002
)))))) (SETQ@I MAIN:FIBONACCICOORDINATOR:N@I (+ 0 MAIN:FIBONACCICOORDINATOR:
$1)) (SETQ@I MAIN:FIBONACCICOORDINATOR:SPAWN@I (+ 0 MAIN:FIBONACCICOORDINATOR:$
2)) (SETQ@I MAIN:FIBONACCICOORDINATOR:TMP_000000004@I (<@I MAIN:FIBONACCICOORD
INATOR:N@I 2)) (IF@J MAIN:FIBONACCICOORDINATOR:TMP_000000004@I (SETQ@I MAIN:FI
BONACCICOORDINATOR:TMP_000000000@I MAIN:FIBONACCICOORDINATOR:N@I) (PROGN (SETQ
@I MAIN:FIBONACCICOORDINATOR:TMP_000000003@I (>@I MAIN:FIBONACCICOORDINATOR:SP
AWN@I 0)) (IF@J MAIN:FIBONACCICOORDINATOR:TMP_000000003@I (PROGN (SETQ@I MAIN:
FIBONACCICOORDINATOR:TMP_000000001 (MAIN:FIBONACCICOORDINATOR:SHADOW (--@J MAI
N:FIBONACCICOORDINATOR:N@I) (>@J MAIN:FIBONACCICOORDINATOR:SPAWN@I 1))) (SETQ@
I MAIN:FIBONACCICOORDINATOR:TMP_000000002 (MAIN:FIBONACCICOORDINATOR:SHADOW (-
@J MAIN:FIBONACCICOORDINATOR:N@I 2) (>@J MAIN:FIBONACCICOORDINATOR:SPAWN@I 1)
) (SETQ@I MAIN:FIBONACCICOORDINATOR:TMP_000000000@I (+@J MAIN:FIBONACCICOORDIN
ATOR:TMP_000000001 MAIN:FIBONACCICOORDINATOR:TMP_000000002))) (PROGN (SETQ@S
MAIN:FIBONACCICOORDINATOR:TMP_000000001 (BMDFM2BMDFM OFFLOAD@J "Fibonacci" (ST
R@I (--@J MAIN:FIBONACCICOORDINATOR:N@I))) (SETQ@S MAIN:FIBONACCICOORDINATOR:T
MP_000000002 (BMDFM2BMDFM OFFLOAD@J "Fibonacci" (STR@I (--@J MAIN:FIBONACCICOOR
DINATOR:N@I 2))) (SETQ@I MAIN:FIBONACCICOORDINATOR:TMP_000000000@I (+@J (IVAL
MAIN:FIBONACCICOORDINATOR:TMP_000000001) (IVAL MAIN:FIBONACCICOORDINATOR:N@I (-
000000002)))))) (DEFUN MAIN:FIBONACCI (PROGN (SETQ@I MAIN:FIBONACCI:N@I (+
0 MAIN:FIBONACCI:$1)) (SETQ@I MAIN:FIBONACCI:SPAWN@I (N CPUPROC)) (SETQ@I MAIN:
FIBONACCI:TMP_000000000@I (MAIN:FIBONACCICOORDINATOR MAIN:FIBONACCI:N@I MAIN:F
IBONACCI:SPAWN@I))) (SETQ@I MAIN:N@I (+@J 0 MAIN:$1)) (SETQ@I MAIN:TMP_000000
001 (MAIN:FIBONACCI MAIN:N@I)) (SETQ@S MAIN:TMP_000000001 (OUTF (PRN_STRING_FM
T) (CAT "" MAIN:TMP_000000001))) (SETQ@S MAIN:TMP_000000000@S ""))
-----
*You may recompile BMDFMldr module with commented `#define _NOISY_MODE`
to disable print of the FastLisp code.
Performing preliminary STATIC SCHEDULING (HARD_ARRAY_SYNCRO=NO,
EXT_IN_OUT_SYNCRO=YES),...
Progress: *S*U*U*i*i*i*i*U
The translator module has finished the static scheduling.
The translator has returned the following exit code: 0(Success).
The following generated control sequence (so-called "BM_DFM UNICODE')
will be transferred to the BM_DFM kernel:
-----
(CTRL
(N# 0)
(OpGroup 1)
(COP 50)
(dfmput_marshaled_cluster
(Vars N# Ref_Name [Array]
(0 0 "MAIN:$1")
(1 34 "MAIN:TERM_TYPE@S")
(2 29 "MAIN:LINES_TERM@I")
(3 4 "MAIN:COLUMNS_TERM@I")
(4 3 "MAIN:CLRSKR_TERM@S")
(5 32 "MAIN:REVERSE_TERM@S")
(6 1 "MAIN:BLINK_TERM@S")
(7 2 "MAIN:BOLD_TERM@S")
(8 31 "MAIN:NORMAL_TERM@S")
(9 28 "MAIN:HIDECURSOR_TERM@S")
(10 33 "MAIN:SHOWCURSOR_TERM@S")
(11 27 "MAIN:GOTOCURSOR_TERM@S")
)
)
(Fnc
(N# 0)
(FLP (SETQ@I MAIN:$1 50))
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" I 00 00 00 00 00 00 00 00" " 2 00 00 00 00 00 00 00 00"
)
)
(Var_Ptrs 0)
)
(Fnc
(N# 1)
(FLP (SETQ@S MAIN:TERM_TYPE@S "xterm"))
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 05 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" S 00 00 00 00 00 00 00 00" "05 00 00 00 00 00 00 00 00"
" x t e r m 00 00 00 00"
)
)
(Var_Ptrs 1)
)
(Fnc
(N# 2)
(FLP (SETQ@I MAIN:LINES_TERM@I 24))
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" I 00 00 00 00 00 00 00 00" "18 00 00 00 00 00 00 00 00"
)
)
(Var_Ptrs 2)
)
(Fnc
(N# 3)
(FLP (SETQ@I MAIN:COLUMNS_TERM@I 80))
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" I 00 00 00 00 00 00 00 00" " P 00 00 00 00 00 00 00 00"
)
)
(Var_Ptrs 3)
)
)
)
(Fnc
(N# 4)
(FLP (SETQ@S MAIN:CLRSKR_TERM@S "\e[H\e[2J"))
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 05 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" S 00 00 00 00 00 00 00 00" "07 00 00 00 00 00 00 00 00"
"1B [ H 1B [ 2 J 00"
)
)
(Var_Ptrs 4)
)
(Fnc
(N# 5)
(FLP (SETQ@S MAIN:REVERSE_TERM@S "\e[7m"))
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 05 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" S 00 00 00 00 00 00 00 00" "04 00 00 00 00 00 00 00 00"
"1B [ 7 m 00 00 00 00"
)
)
(Var_Ptrs 5)
)
(Fnc
(N# 6)
(FLP (SETQ@S MAIN:BLINK_TERM@S "\e[5m"))
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 05 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" S 00 00 00 00 00 00 00 00" "04 00 00 00 00 00 00 00 00"
"1B [ 5 m 00 00 00 00"
)
)
(Var_Ptrs 6)
)
(Fnc
(N# 7)
(FLP (SETQ@S MAIN:BOLD_TERM@S "\e[1m"))
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 05 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" S 00 00 00 00 00 00 00 00" "04 00 00 00 00 00 00 00 00"
"1B [ 1 m 00 00 00 00"
)
)
(Var_Ptrs 7)
)
(Fnc
(N# 8)
(FLP (SETQ@S MAIN:NORMAL_TERM@S "\e[0m"))
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 05 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" S 00 00 00 00 00 00 00 00" "04 00 00 00 00 00 00 00 00"
"1B [ 0 m 00 00 00 00"
)
)
(Var_Ptrs 8)
)
(Fnc
(N# 9)
(FLP (SETQ@S MAIN:HIDECURSOR_TERM@S "\e[?251]")
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 05 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" S 00 00 00 00 00 00 00 00" "06 00 00 00 00 00 00 00 00"
"1B [ ? 2 5 1 00 00"
)
)
(Var_Ptrs 9)
)
(Fnc
(N# 10)
(FLP (SETQ@S MAIN:SHOWCURSOR_TERM@S "\e[?121\e[?25h"))
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 05 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" S 00 00 00 00 00 00 00 00" "0C 00 00 00 00 00 00 00 00"
"1B [ ? 1 2 1 1B [ " ? ? 2 5 h 00 00 00 00"
)
)
(Var_Ptrs 10)
)
(Fnc
(N# 11)
(FLP (SETQ@S MAIN:GOTOCURSOR_TERM@S "\e[%i%d;%dH"))
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 05 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" S 00 00 00 00 00 00 00 00" "0A 00 00 00 00 00 00 00 00"
"1B [ % i % d ; % " d H 00 00 00 00 00 00"
)
)
(Var_Ptrs 11)
)
)
)
(CTRL
(N# 1)
(OpGroup 2)
(COP 14)
(GOTO 51)
(REM "Pass over UDF `MAIN:FIBONACCICOORDINATOR' body")
)
(CTRL
(N# 2)
(OpGroup 2)
(COP 14)
(GOTO 27)
(REM "Pass over UDF `MAIN:FIBONACCICOORDINATOR:SHADOW' body")
)
)
(CTRL
(N# 3)
(OpGroup 1)
(COP 50)
(dfmput_marshaled_cluster
(Vars N# Ref_Name [Array]
(0 12 "MAIN:FIBONACCICOORDINATOR:SHADOW:$1")
(1 14 "MAIN:FIBONACCICOORDINATOR:SHADOW:N@I")
(2 13 "MAIN:FIBONACCICOORDINATOR:SHADOW:$2")
)
)
)

```

```

(3 15 "MAIN:FIBONACCICOORDINATOR:SHADOW:SPAWN@I")
(4 20 "MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__00000004@I")
)
(Fnc
(N# 0)
(FLP
(SETQ@I
MAIN:FIBONACCICOORDINATOR:SHADOW:N@I
(+ 0 MAIN:FIBONACCICOORDINATOR:SHADOW:$1)
)
)
(FLP COMPILED
"D5 01 00 00 00 00 00 00" "02 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
" T BC 00 00 00 00 00 00" "02 00 00 00 00 00 00 00"
"03 00 00 00 00 00 00 00" " I 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00" " V 00 00 00 00 00 00 00 00"
"01 00 00 00 00 00 00 00"
)
(Var_Ptrs 1 0)
)
(Fnc
(N# 1)
(FLP
(SETQ@I
MAIN:FIBONACCICOORDINATOR:SHADOW:SPAWN@I
(+ 0 MAIN:FIBONACCICOORDINATOR:SHADOW:$2)
)
)
(FLP COMPILED
"D5 01 00 00 00 00 00 00" "02 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
" T BC 00 00 00 00 00 00" "02 00 00 00 00 00 00 00"
"03 00 00 00 00 00 00 00" " I 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00" " V 00 00 00 00 00 00 00 00"
"01 00 00 00 00 00 00 00"
)
(Var_Ptrs 3 2)
)
(Fnc
(N# 2)
(FLP
(SETQ@I
MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__00000004@I
(<@I MAIN:FIBONACCICOORDINATOR:SHADOW:N@I 2)
)
)
(FLP COMPILED
"D5 01 00 00 00 00 00 00" "02 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
"D4 x 00 00 00 00 00 00" "02 00 00 00 00 00 00 00"
"03 00 00 00 00 00 00 00" " i 00 00 00 00 00 00 00 00"
"01 00 00 00 00 00 00 00" " I 00 00 00 00 00 00 00 00"
"02 00 00 00 00 00 00 00"
)
(Var_Ptrs 4 1)
)
)
(CTRL
(N# 4)
(OpGroup 1)
(COP 70)
(dfmpmut_zdata
(VarRef 20)
(VarName "MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__00000004@I")
(Inq_Dest Ld)
)
)
(CTRL (N# 5) (OpGroup 1) (COP 81) (<accum_slo> (dfmget_idata)))
(CTRL
(N# 6)
(OpGroup 2)
(COP 17)
(IF NOT <accum_slo> (GOTO 9))
(REM
"Pass over 'MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__00000004@I' <if>
conditional branch"
)
)
(CTRL
(N# 7)
(OpGroup 1)
(COP 50)
(dfmpmut_marshaled_cluster
(Vars_N# Ref_Name [Array]
(0 14 "MAIN:FIBONACCICOORDINATOR:SHADOW:N@I")
(1 16 "MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__00000000@I")
)
)
(Fnc
(N# 0)
(FLP
(SETQ@I
MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__00000000@I
MAIN:FIBONACCICOORDINATOR:SHADOW:N@I
)
)
(FLP COMPILED
"D5 01 00 00 00 00 00 00" "02 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
" i 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
)
(Var_Ptrs 1 0)
)
)
)
(CTRL
(N# 8)
(OpGroup 2)
(COP 14)
(GOTO 26)
(REM
"Pass over 'MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__00000004@I' <else>
conditional branch"
)
)
(CTRL
(N# 9)

```

```

(OpGroup 1)
(COP 50)
(dfmpmut_marshaled_cluster
(Vars_N# Ref_Name [Array]
(0 15 "MAIN:FIBONACCICOORDINATOR:SHADOW:SPAWN@I")
(1 19 "MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__00000003@I")
)
)
(Fnc
(N# 0)
(FLP
(SETQ@I
MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__00000003@I
(>@I MAIN:FIBONACCICOORDINATOR:SHADOW:SPAWN@I 0)
)
)
(FLP COMPILED
"D5 01 00 00 00 00 00 00" "02 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
"D4 80 00 00 00 00 00 00" "02 00 00 00 00 00 00 00"
"03 00 00 00 00 00 00 00" " i 00 00 00 00 00 00 00 00"
"01 00 00 00 00 00 00 00" " I 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00"
)
(Var_Ptrs 1 0)
)
)
)
(CTRL
(N# 10)
(OpGroup 1)
(COP 70)
(dfmpmut_zdata
(VarRef 19)
(VarName "MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__00000003@I")
(Inq_Dest Ld)
)
)
(CTRL (N# 11) (OpGroup 1) (COP 81) (<accum_slo> (dfmget_idata)))
(CTRL
(N# 12)
(OpGroup 2)
(COP 17)
(IF NOT <accum_slo> (GOTO 25))
(REM
"Pass over 'MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__00000003@I' <if>
conditional branch"
)
)
(CTRL
(N# 13)
(OpGroup 2)
(COP 12)
(ENTER_RECURSION)
(Vars_N# Ref_Name [Array]
(0 11 "MAIN:FIBONACCICOORDINATOR:N@I")
(1 9 "MAIN:FIBONACCICOORDINATOR:$1")
(2 21 "MAIN:FIBONACCICOORDINATOR:SHADOW:SPAWN@I")
(3 10 "MAIN:FIBONACCICOORDINATOR:$2")
(4 26 "MAIN:FIBONACCICOORDINATOR:TMP__00000004@I")
(5 22 "MAIN:FIBONACCICOORDINATOR:TMP__00000000@I")
(6 25 "MAIN:FIBONACCICOORDINATOR:TMP__00000003@I")
(7 23 "MAIN:FIBONACCICOORDINATOR:TMP__00000001")
(8 24 "MAIN:FIBONACCICOORDINATOR:TMP__00000002")
)
)
)
(CTRL
(N# 14)
(OpGroup 1)
(COP 50)
(dfmpmut_marshaled_cluster
(Vars_N# Ref_Name [Array]
(0 9 "MAIN:FIBONACCICOORDINATOR:$1")
(1 14 "MAIN:FIBONACCICOORDINATOR:SHADOW:N@I")
(2 10 "MAIN:FIBONACCICOORDINATOR:$2")
(3 15 "MAIN:FIBONACCICOORDINATOR:SHADOW:SPAWN@I")
)
)
(Fnc
(N# 0)
(FLP
(ALSETQ
MAIN:FIBONACCICOORDINATOR:$1
(--@J MAIN:FIBONACCICOORDINATOR:SHADOW:N@I)
)
)
(FLP COMPILED
"D5 01 00 00 00 00 00 00" "02 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00" " T 08 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
"D4 F4 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
" i 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
)
(Var_Ptrs 0 1)
)
)
(Fnc
(N# 1)
(FLP
(ALSETQ
MAIN:FIBONACCICOORDINATOR:$2
(>@J MAIN:FIBONACCICOORDINATOR:SHADOW:SPAWN@I 1)
)
)
(FLP COMPILED
"D5 01 00 00 00 00 00 00" "02 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00" " T 08 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00"
"D4 \ ( 01 00 00 00 00 00" "02 00 00 00 00 00 00 00"
"03 00 00 00 00 00 00 00" " i 00 00 00 00 00 00 00 00"
"01 00 00 00 00 00 00 00" " I 00 00 00 00 00 00 00 00"
"01 00 00 00 00 00 00 00"
)
(Var_Ptrs 2 3)
)
)
)
(REM
"UDF 'MAIN:FIBONACCICOORDINATOR' invoke initialization (passing the
arguments)"
)
)
(CTRL
(N# 15)
(OpGroup 2)

```

```

(COP 15)
(GOSUB 2)
(REM "UDF `MAIN:FIBONACCICOORDINATOR' call")
)
(CTRL
(N# 16)
(OpGroup 1)
(COP 50)
(dfmput_marshaled_cluster
(Var_N# Ref_Name [Array]
(0 17 "MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000001")
(1 22 "MAIN:FIBONACCICOORDINATOR:TMP__000000000@I")
)
)
(Fnc
(N# 0)
(FLP
(ALSETQ
MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000001
MAIN:FIBONACCICOORDINATOR:TMP__000000000@I
)
)
(FLP_COMPILED
"D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" " T 08 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" i 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
)
)
(Var_Ptrs 0 1)
)
)
(REM "UDF `MAIN:FIBONACCICOORDINATOR' returned value")
)
(CTRL (N# 17) (OpGroup 2) (COP 13) (LEAVE_RECURSION))
(CTRL
(N# 18)
(OpGroup 2)
(COP 12)
(ENTER_RECURSION)
(Var_N# Ref_Name [Array]
(0 11 "MAIN:FIBONACCICOORDINATOR:N@I")
(1 9 "MAIN:FIBONACCICOORDINATOR:$1")
(2 21 "MAIN:FIBONACCICOORDINATOR:SPAWN@I")
(3 10 "MAIN:FIBONACCICOORDINATOR:$2")
(4 26 "MAIN:FIBONACCICOORDINATOR:TMP__000000004@I")
(5 22 "MAIN:FIBONACCICOORDINATOR:TMP__000000000@I")
(6 25 "MAIN:FIBONACCICOORDINATOR:TMP__000000003@I")
(7 23 "MAIN:FIBONACCICOORDINATOR:TMP__000000001")
(8 24 "MAIN:FIBONACCICOORDINATOR:TMP__000000002")
)
)
)
(CTRL
(N# 19)
(OpGroup 1)
(COP 50)
(dfmput_marshaled_cluster
(Var_N# Ref_Name [Array]
(0 9 "MAIN:FIBONACCICOORDINATOR:$1")
(1 14 "MAIN:FIBONACCICOORDINATOR:SHADOW:N@I")
(2 10 "MAIN:FIBONACCICOORDINATOR:$2")
(3 15 "MAIN:FIBONACCICOORDINATOR:SHADOW:SPAWN@I")
)
)
(Fnc
(N# 0)
(FLP
(ALSETQ
MAIN:FIBONACCICOORDINATOR:$1
(-@J MAIN:FIBONACCICOORDINATOR:SHADOW:N@I 2)
)
)
(FLP_COMPILED
"D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" " T 08 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"D4 C4 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"03 00 00 00 00 00 00 00 00" " i 00 00 00 00 00 00 00 00 00"
"01 00 00 00 00 00 00 00 00" " I 00 00 00 00 00 00 00 00 00"
"02 00 00 00 00 00 00 00 00"
)
)
(Var_Ptrs 0 1)
)
)
(Fnc
(N# 1)
(FLP
(ALSETQ
MAIN:FIBONACCICOORDINATOR:$2
(>@J MAIN:FIBONACCICOORDINATOR:SHADOW:SPAWN@I 1)
)
)
(FLP_COMPILED
"D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" " T 08 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"D4 \ ( 01 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"03 00 00 00 00 00 00 00 00" " i 00 00 00 00 00 00 00 00 00"
"01 00 00 00 00 00 00 00 00" " I 00 00 00 00 00 00 00 00 00"
"01 00 00 00 00 00 00 00 00"
)
)
(Var_Ptrs 2 3)
)
)
)
(REM
"UDF `MAIN:FIBONACCICOORDINATOR' invoke initialization (passing the
arguments)"
)
)
(CTRL
(N# 20)
(OpGroup 2)
(COP 15)
(GOSUB 2)
(REM "UDF `MAIN:FIBONACCICOORDINATOR' call")
)
)
(CTRL
(N# 21)
(OpGroup 1)
(COP 50)
(dfmput_marshaled_cluster
(Var_N# Ref_Name [Array]
(0 18 "MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000002")
(1 22 "MAIN:FIBONACCICOORDINATOR:TMP__000000000@I")
)
)
)
(Fnc

```

```

(N# 0)
(FLP
(ALSETQ
MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000002
MAIN:FIBONACCICOORDINATOR:TMP__000000000@I
)
)
)
(FLP_COMPILED
"D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" " T 08 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" i 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
)
)
(Var_Ptrs 0 1)
)
)
)
(REM "UDF `MAIN:FIBONACCICOORDINATOR' returned value")
)
(CTRL (N# 22) (OpGroup 2) (COP 13) (LEAVE_RECURSION))
(CTRL
(N# 23)
(OpGroup 1)
(COP 50)
(dfmput_marshaled_cluster
(Var_N# Ref_Name [Array]
(0 17 "MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000001")
(1 18 "MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000002")
(2 16 "MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000000@I")
)
)
)
(Fnc
(N# 0)
(FLP
(SETQ@I
MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000000@I
(+@J
MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000001
MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000002
)
)
)
(FLP_COMPILED
"D5 01 00 00 00 00 00 00 00" "03 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"D4 BC 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"03 00 00 00 00 00 00 00 00" " V 00 00 00 00 00 00 00 00 00"
"01 00 00 00 00 00 00 00 00" " V 00 00 00 00 00 00 00 00 00"
"02 00 00 00 00 00 00 00 00"
)
)
(Var_Ptrs 2 0 1)
)
)
)
)
(CTRL
(N# 24)
(OpGroup 2)
(COP 14)
(GOTO 26)
(REM
"Pass over `MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000003@I' <else>
conditional branch"
)
)
)
(CTRL
(N# 25)
(OpGroup 1)
(COP 50)
(dfmput_marshaled_cluster
(Var_N# Ref_Name [Array]
(0 14 "MAIN:FIBONACCICOORDINATOR:SHADOW:N@I")
(1 17 "MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000001")
(2 18 "MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000002")
(3 16 "MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000000@I")
)
)
)
(Fnc
(N# 0)
(FLP
(SETQ@S
MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000001
(BMDFM2BMDFM_OFFLOAD@J
"Fibonacci"
(STR@I (-@J MAIN:FIBONACCICOORDINATOR:SHADOW:N@I))
)
)
)
)
(FLP_COMPILED
"D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 05 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" t 04 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"05 00 00 00 00 00 00 00 00" " S 00 00 00 00 00 00 00 00 00"
"09 00 00 00 00 00 00 00 00" " F i b o n a c c i"
" i 00 00 00 00 00 00 00 00" "D4 C4 01 00 00 00 00 00 00"
"01 00 00 00 00 00 00 00 00" "D4 F4 00 00 00 00 00 00 00"
"01 00 00 00 00 00 00 00 00" " i 00 00 00 00 00 00 00 00 00"
"01 00 00 00 00 00 00 00 00"
)
)
(Var_Ptrs 1 0)
)
)
)
(Fnc
(N# 1)
(FLP
(SETQ@S
MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000002
(BMDFM2BMDFM_OFFLOAD@J
"Fibonacci"
(STR@I (-@J MAIN:FIBONACCICOORDINATOR:SHADOW:N@I 2))
)
)
)
)
(FLP_COMPILED
"D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 05 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" t 04 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"05 00 00 00 00 00 00 00 00" " S 00 00 00 00 00 00 00 00 00"
"09 00 00 00 00 00 00 00 00" " F i b o n a c c i"
" i 00 00 00 00 00 00 00 00" "D4 C4 01 00 00 00 00 00 00"
"01 00 00 00 00 00 00 00 00" "D4 C4 00 00 00 00 00 00 00"
"02 00 00 00 00 00 00 00 00" "03 00 00 00 00 00 00 00 00"
" i 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" I 00 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
)

```



```

)
(Var_Ptrs 2 0)
)
(Fnc
(N# 2)
(FLP
(SETQ@I
MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000000@I
(+@J
(IVAL MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000001)
(IVAL MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000002)
)
)
)
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "03 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"D4 BC 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"05 00 00 00 00 00 00 00 00" "T AC 00 00 00 00 00 00 00"
"01 00 00 00 00 00 00 00 00" "V 00 00 00 00 00 00 00 00"
"01 00 00 00 00 00 00 00 00" "T AC 00 00 00 00 00 00 00"
"01 00 00 00 00 00 00 00 00" "V 00 00 00 00 00 00 00 00"
"02 00 00 00 00 00 00 00 00"
)
)
(Var_Ptrs 3 1 2)
)
)
)
(CTRL
(N# 26)
(OpGroup 2)
(COP 16)
(RETURN)
(REM "End of UDF `MAIN:FIBONACCICOORDINATOR:SHADOW' body")
)
(CTRL
(N# 27)
(OpGroup 1)
(COP 50)
(dfmput_marshaled_cluster
(VarS_N# Ref Name [Array]
(0 9 "MAIN:FIBONACCICOORDINATOR:$1")
(1 11 "MAIN:FIBONACCICOORDINATOR:N@I")
(2 10 "MAIN:FIBONACCICOORDINATOR:$2")
(3 21 "MAIN:FIBONACCICOORDINATOR:SPAWN@I")
(4 26 "MAIN:FIBONACCICOORDINATOR:TMP__000000004@I")
)
)
(Fnc
(N# 0)
(FLP
(SETQ@I
MAIN:FIBONACCICOORDINATOR:N@I
(+ 0 MAIN:FIBONACCICOORDINATOR:$1)
)
)
)
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" T BC 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"03 00 00 00 00 00 00 00 00" "I 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "V 00 00 00 00 00 00 00 00"
"01 00 00 00 00 00 00 00 00"
)
)
(Var_Ptrs 1 0)
)
)
(Fnc
(N# 1)
(FLP
(SETQ@I
MAIN:FIBONACCICOORDINATOR:SPAWN@I
(+ 0 MAIN:FIBONACCICOORDINATOR:$2)
)
)
)
)
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" T BC 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"03 00 00 00 00 00 00 00 00" "I 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "V 00 00 00 00 00 00 00 00"
"01 00 00 00 00 00 00 00 00"
)
)
(Var_Ptrs 3 2)
)
)
)
(Fnc
(N# 2)
(FLP
(SETQ@I
MAIN:FIBONACCICOORDINATOR:TMP__000000004@I
(<@I MAIN:FIBONACCICOORDINATOR:N@I 2)
)
)
)
)
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"D4 x 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"03 00 00 00 00 00 00 00 00" "I 00 00 00 00 00 00 00 00"
"01 00 00 00 00 00 00 00 00" "I 00 00 00 00 00 00 00 00"
"02 00 00 00 00 00 00 00 00"
)
)
(Var_Ptrs 4 1)
)
)
)
)
(CTRL
(N# 28)
(OpGroup 1)
(COP 70)
(dfmput_xdata
(VarRef 26)
(VarName "MAIN:FIBONACCICOORDINATOR:TMP__000000004@I")
(Inq_Dest Ld)
)
)
)
(CTRL (N# 29) (OpGroup 1) (COP 81) (<accum_slo> (dfmget_idata)))
(CTRL
(N# 30)
(OpGroup 2)
(COP 17)
)
)

```

```

(IF_NOT <accum_slo> (GOTO 33))
(REM
"Pass over `MAIN:FIBONACCICOORDINATOR:TMP__000000004@I' <if> conditional
branch"
)
)
)
(CTRL
(N# 31)
(OpGroup 1)
(COP 50)
(dfmput_marshaled_cluster
(VarS_N# Ref Name [Array]
(0 11 "MAIN:FIBONACCICOORDINATOR:N@I")
(1 22 "MAIN:FIBONACCICOORDINATOR:TMP__000000000@I")
)
)
)
(Fnc
(N# 0)
(FLP
(SETQ@I
MAIN:FIBONACCICOORDINATOR:TMP__000000000@I
MAIN:FIBONACCICOORDINATOR:N@I
)
)
)
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" i 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
)
)
(Var_Ptrs 1 0)
)
)
)
)
(CTRL
(N# 32)
(OpGroup 2)
(COP 14)
(GOTO 50)
(REM
"Pass over `MAIN:FIBONACCICOORDINATOR:TMP__000000004@I' <else> conditional
branch"
)
)
)
(CTRL
(N# 33)
(OpGroup 1)
(COP 50)
(dfmput_marshaled_cluster
(VarS_N# Ref Name [Array]
(0 21 "MAIN:FIBONACCICOORDINATOR:SPAWN@I")
(1 25 "MAIN:FIBONACCICOORDINATOR:TMP__000000003@I")
)
)
)
(Fnc
(N# 0)
(FLP
(SETQ@I
MAIN:FIBONACCICOORDINATOR:TMP__000000003@I
(>@I MAIN:FIBONACCICOORDINATOR:SPAWN@I 0)
)
)
)
(FLP COMPILED
"D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"D4 80 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"03 00 00 00 00 00 00 00 00" " i 00 00 00 00 00 00 00 00"
"01 00 00 00 00 00 00 00 00" "I 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00"
)
)
(Var_Ptrs 1 0)
)
)
)
)
)
(CTRL
(N# 34)
(OpGroup 1)
(COP 70)
(dfmput_xdata
(VarRef 25)
(VarName "MAIN:FIBONACCICOORDINATOR:TMP__000000003@I")
(Inq_Dest Ld)
)
)
)
)
(CTRL (N# 35) (OpGroup 1) (COP 81) (<accum_slo> (dfmget_idata)))
(CTRL
(N# 36)
(OpGroup 2)
(COP 17)
(IF_NOT <accum_slo> (GOTO 49))
(REM
"Pass over `MAIN:FIBONACCICOORDINATOR:TMP__000000003@I' <if> conditional
branch"
)
)
)
)
(CTRL
(N# 37)
(OpGroup 2)
(COP 12)
(ENTER_RECURSION)
(VarS_N# Ref Name [Array]
(0 14 "MAIN:FIBONACCICOORDINATOR:SHADOW:N@I")
(1 12 "MAIN:FIBONACCICOORDINATOR:SHADOW:$1")
(2 15 "MAIN:FIBONACCICOORDINATOR:SHADOW:SPAWN@I")
(3 13 "MAIN:FIBONACCICOORDINATOR:SHADOW:$2")
(4 20 "MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000004@I")
(5 16 "MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000000@I")
(6 19 "MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000003@I")
(7 17 "MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000001")
(8 18 "MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000002")
)
)
)
)
(CTRL
(N# 38)
(OpGroup 1)
(COP 50)
(dfmput_marshaled_cluster
(VarS_N# Ref Name [Array]
(0 12 "MAIN:FIBONACCICOORDINATOR:SHADOW:$1")
(1 11 "MAIN:FIBONACCICOORDINATOR:N@I")
(2 13 "MAIN:FIBONACCICOORDINATOR:SHADOW:$2")
(3 21 "MAIN:FIBONACCICOORDINATOR:SPAWN@I")
)
)
)
)

```

```

(Fnc
(N# 0)
(FLP
(ALSETQ
MAIN:FIBONACCICOORDINATOR:SHADOW:$1
(--@J MAIN:FIBONACCICOORDINATOR:N@I)
)
)
(FLP_COMPILED
"D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" " T 08 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"D4 F4 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" i 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
)
)
(Var_Ptrs 0 1)
)
(Fnc
(N# 1)
(FLP
(ALSETQ
MAIN:FIBONACCICOORDINATOR:SHADOW:$2
(>@J MAIN:FIBONACCICOORDINATOR:SPAWN@I 1)
)
)
(FLP_COMPILED
"D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" " T 08 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"D4 \ ( 01 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"03 00 00 00 00 00 00 00 00" " i 00 00 00 00 00 00 00 00"
"01 00 00 00 00 00 00 00 00" " I 00 00 00 00 00 00 00 00"
"01 00 00 00 00 00 00 00 00"
)
)
(Var_Ptrs 2 3)
)
)
(REM
"UDF `MAIN:FIBONACCICOORDINATOR:SHADOW' invoke initialization (passing the
arguments)"
)
)
(CTRL
(N# 39)
(OpGroup 2)
(COP 15)
(GOSUB 3)
(REM "UDF `MAIN:FIBONACCICOORDINATOR:SHADOW' call")
)
)
(CTRL
(N# 40)
(OpGroup 1)
(COP 50)
(dfmpmt_marshaled_cluster
(Var_N# Ref_Name [Array]
(0 23 "MAIN:FIBONACCICOORDINATOR:TMP_000000001")
(1 16 "MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000000@I")
)
)
(Fnc
(N# 0)
(FLP
(ALSETQ
MAIN:FIBONACCICOORDINATOR:TMP_000000001
MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000000@I
)
)
(FLP_COMPILED
"D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" " T 08 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" i 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
)
)
(Var_Ptrs 0 1)
)
)
(REM "UDF `MAIN:FIBONACCICOORDINATOR:SHADOW' returned value")
)
)
(CTRL (N# 41) (OpGroup 2) (COP 13) (LEAVE_RECURSION))
(CTRL
(N# 42)
(OpGroup 2)
(COP 12)
(ENTER_RECURSION)
(Var_N# Ref_Name [Array]
(0 14 "MAIN:FIBONACCICOORDINATOR:SHADOW:N@I")
(1 12 "MAIN:FIBONACCICOORDINATOR:SHADOW:$1")
(2 15 "MAIN:FIBONACCICOORDINATOR:SHADOW:SPAWN@I")
(3 13 "MAIN:FIBONACCICOORDINATOR:SHADOW:$2")
(4 20 "MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000004@I")
(5 16 "MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000000@I")
(6 19 "MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000003@I")
(7 17 "MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000001")
(8 18 "MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000002")
)
)
)
(CTRL
(N# 43)
(OpGroup 1)
(COP 50)
(dfmpmt_marshaled_cluster
(Var_N# Ref_Name [Array]
(0 12 "MAIN:FIBONACCICOORDINATOR:SHADOW:$1")
(1 11 "MAIN:FIBONACCICOORDINATOR:SHADOW:N@I")
(2 13 "MAIN:FIBONACCICOORDINATOR:SHADOW:$2")
(3 21 "MAIN:FIBONACCICOORDINATOR:SPAWN@I")
)
)
)
(Fnc
(N# 0)
(FLP
(ALSETQ
MAIN:FIBONACCICOORDINATOR:SHADOW:$1
(-@J MAIN:FIBONACCICOORDINATOR:N@I 2)
)
)
(FLP_COMPILED
"D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" " T 08 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"D4 C4 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"03 00 00 00 00 00 00 00 00" " i 00 00 00 00 00 00 00 00"
"01 00 00 00 00 00 00 00 00" " I 00 00 00 00 00 00 00 00"
"02 00 00 00 00 00 00 00 00"
)
)
)
)

```

```

)
(Var_Ptrs 0 1)
)
)
(Fnc
(N# 1)
(FLP
(ALSETQ
MAIN:FIBONACCICOORDINATOR:SHADOW:$2
(>@J MAIN:FIBONACCICOORDINATOR:SPAWN@I 1)
)
)
(FLP_COMPILED
"D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" " T 08 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"D4 \ ( 01 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"03 00 00 00 00 00 00 00 00" " i 00 00 00 00 00 00 00 00"
"01 00 00 00 00 00 00 00 00" " I 00 00 00 00 00 00 00 00"
"01 00 00 00 00 00 00 00 00"
)
)
(Var_Ptrs 2 3)
)
)
(REM
"UDF `MAIN:FIBONACCICOORDINATOR:SHADOW' invoke initialization (passing the
arguments)"
)
)
(CTRL
(N# 44)
(OpGroup 2)
(COP 15)
(GOSUB 3)
(REM "UDF `MAIN:FIBONACCICOORDINATOR:SHADOW' call")
)
)
(CTRL
(N# 45)
(OpGroup 1)
(COP 50)
(dfmpmt_marshaled_cluster
(Var_N# Ref_Name [Array]
(0 24 "MAIN:FIBONACCICOORDINATOR:TMP_000000002")
(1 16 "MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000000@I")
)
)
)
(Fnc
(N# 0)
(FLP
(ALSETQ
MAIN:FIBONACCICOORDINATOR:TMP_000000002
MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000000@I
)
)
(FLP_COMPILED
"D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" " T 08 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
" i 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
)
)
(Var_Ptrs 0 1)
)
)
(REM "UDF `MAIN:FIBONACCICOORDINATOR:SHADOW' returned value")
)
)
(CTRL (N# 46) (OpGroup 2) (COP 13) (LEAVE_RECURSION))
(CTRL
(N# 47)
(OpGroup 1)
(COP 50)
(dfmpmt_marshaled_cluster
(Var_N# Ref_Name [Array]
(0 23 "MAIN:FIBONACCICOORDINATOR:TMP_000000001")
(1 24 "MAIN:FIBONACCICOORDINATOR:TMP_000000002")
(2 22 "MAIN:FIBONACCICOORDINATOR:TMP_000000000@I")
)
)
)
(Fnc
(N# 0)
(FLP
(SETQ@I
MAIN:FIBONACCICOORDINATOR:TMP_000000000@I
(+@J
MAIN:FIBONACCICOORDINATOR:TMP_000000001
MAIN:FIBONACCICOORDINATOR:TMP_000000002
)
)
)
)
(FLP_COMPILED
"D5 01 00 00 00 00 00 00 00" "03 00 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00 00"
"00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
"D4 BC 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
"03 00 00 00 00 00 00 00 00" " V 00 00 00 00 00 00 00 00"
"01 00 00 00 00 00 00 00 00" " V 00 00 00 00 00 00 00 00"
"02 00 00 00 00 00 00 00 00"
)
)
(Var_Ptrs 2 0 1)
)
)
)
(CTRL
(N# 48)
(OpGroup 2)
(COP 14)
(GOTO 50)
(REM
"Pass over `MAIN:FIBONACCICOORDINATOR:TMP_000000003@I' <else> conditional
branch"
)
)
)
(CTRL
(N# 49)
(OpGroup 1)
(COP 50)
(dfmpmt_marshaled_cluster
(Var_N# Ref_Name [Array]
(0 11 "MAIN:FIBONACCICOORDINATOR:N@I")
(1 23 "MAIN:FIBONACCICOORDINATOR:TMP_000000001")
(2 24 "MAIN:FIBONACCICOORDINATOR:TMP_000000002")
(3 22 "MAIN:FIBONACCICOORDINATOR:TMP_000000000@I")
)
)
)
(Fnc
(N# 0)
(FLP
)
)
)

```



```

(SETQ@S
  MAIN:FIBONACCICOORDINATOR:TMP__00000001
  (BMDFM2BMDFM_OFFLOAD@J
    "Fibonacci"
    (STR@I (--@J MAIN:FIBONACCICOORDINATOR:N@I))
  )
)
)
(FLP_COMPILED
  "D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
  "00 00 00 00 00 00 00 00 00" "D4 05 00 00 00 00 00 00 00"
  "00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
  " t 04 00 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
  "05 00 00 00 00 00 00 00 00" " S 00 00 00 00 00 00 00 00 00"
  "09 00 00 00 00 00 00 00 00" " F i b o n a c c i"
  " i 00 00 00 00 00 00 00 00 00" "D4 C4 01 00 00 00 00 00 00"
  "01 00 00 00 00 00 00 00 00" "D4 F4 00 00 00 00 00 00 00"
  "01 00 00 00 00 00 00 00 00" " i 00 00 00 00 00 00 00 00 00"
  "01 00 00 00 00 00 00 00 00"
)
(Var_Ptrs 1 0)
)
(Fnc
  (N# 1)
  (FLP
    (SETQ@S
      MAIN:FIBONACCICOORDINATOR:TMP__00000002
      (BMDFM2BMDFM_OFFLOAD@J
        "Fibonacci"
        (STR@I (--@J MAIN:FIBONACCICOORDINATOR:N@I 2))
      )
    )
  )
  (FLP_COMPILED
    "D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00 00" "D4 05 00 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
    " t 04 00 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
    "05 00 00 00 00 00 00 00 00" " S 00 00 00 00 00 00 00 00 00"
    "09 00 00 00 00 00 00 00 00" " F i b o n a c c i"
    " i 00 00 00 00 00 00 00 00 00" "D4 C4 01 00 00 00 00 00 00"
    "01 00 00 00 00 00 00 00 00" "D4 C4 01 00 00 00 00 00 00"
    "02 00 00 00 00 00 00 00 00" "03 00 00 00 00 00 00 00 00"
    " i 00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
    " I 00 00 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
  )
  (Var_Ptrs 2 0)
)
)
(Fnc
  (N# 2)
  (FLP
    (SETQ@I
      MAIN:FIBONACCICOORDINATOR:TMP__00000000@I
      (+@J
        (IVAL MAIN:FIBONACCICOORDINATOR:TMP__00000000@I)
        (IVAL MAIN:FIBONACCICOORDINATOR:TMP__00000000@I)
      )
    )
  )
  (FLP_COMPILED
    "D5 01 00 00 00 00 00 00 00" "03 00 00 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
    "D4 BC 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
    "05 00 00 00 00 00 00 00 00" " T AC 00 00 00 00 00 00 00 00"
    "01 00 00 00 00 00 00 00 00" " V 00 00 00 00 00 00 00 00 00"
    "01 00 00 00 00 00 00 00 00" " T AC 00 00 00 00 00 00 00 00"
    "01 00 00 00 00 00 00 00 00" " T AC 00 00 00 00 00 00 00 00"
    "02 00 00 00 00 00 00 00 00" " V 00 00 00 00 00 00 00 00 00"
  )
  (Var_Ptrs 3 1 2)
)
)
)
(CTRL
  (N# 50)
  (OpGroup 2)
  (COP 16)
  (RETURN)
  (REM "End of UDF `MAIN:FIBONACCICOORDINATOR' body")
)
)
(CTRL
  (N# 51)
  (OpGroup 2)
  (COP 14)
  (GOTO 59)
  (REM "Pass over UDF `MAIN:FIBONACCI' body")
)
)
(CTRL
  (N# 52)
  (OpGroup 1)
  (COP 50)
  (dfmput_marshaled_cluster
    (Vars_N#_Ref_Name [Array]
      (0 5 "MAIN:FIBONACCI:$1")
      (1 6 "MAIN:FIBONACCI:N@I")
      (2 7 "MAIN:FIBONACCI:SPAWN@I")
    )
  )
  (Fnc
    (N# 0)
    (FLP (SETQ@I MAIN:FIBONACCI:N@I (+ 0 MAIN:FIBONACCI:$1)))
    (FLP_COMPILED
      "D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
      "00 00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00 00"
      "00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
      " T BC 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
      "03 00 00 00 00 00 00 00 00" " I 00 00 00 00 00 00 00 00 00"
      "00 00 00 00 00 00 00 00 00" " V 00 00 00 00 00 00 00 00 00"
      "01 00 00 00 00 00 00 00 00"
    )
  )
  (Var_Ptrs 1 0)
)
)
(Fnc
  (N# 1)
  (FLP (SETQ@I MAIN:FIBONACCI:SPAWN@I (N_CPUPROC)))
  (FLP_COMPILED
    "D5 01 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
    " T D0 02 00 00 00 00 00 00 00"
  )
  (Var_Ptrs 2)
)

```

```

)
)
)
(CTRL
  (N# 53)
  (OpGroup 2)
  (COP 12)
  (ENTER RECURSION)
  (Vars_N#_Ref_Name [Array]
    (0 11 "MAIN:FIBONACCICOORDINATOR:N@I")
    (1 9 "MAIN:FIBONACCICOORDINATOR:$1")
    (2 21 "MAIN:FIBONACCICOORDINATOR:SPAWN@I")
    (3 10 "MAIN:FIBONACCICOORDINATOR:$2")
    (4 26 "MAIN:FIBONACCICOORDINATOR:TMP__000000004@I")
    (5 22 "MAIN:FIBONACCICOORDINATOR:TMP__000000000@I")
    (6 25 "MAIN:FIBONACCICOORDINATOR:TMP__000000003@I")
    (7 23 "MAIN:FIBONACCICOORDINATOR:TMP__000000001")
    (8 24 "MAIN:FIBONACCICOORDINATOR:TMP__000000002")
  )
)
)
(CTRL
  (N# 54)
  (OpGroup 1)
  (COP 50)
  (dfmput_marshaled_cluster
    (Vars_N#_Ref_Name [Array]
      (0 9 "MAIN:FIBONACCICOORDINATOR:$1")
      (1 6 "MAIN:FIBONACCI:N@I")
      (2 10 "MAIN:FIBONACCICOORDINATOR:$2")
      (3 7 "MAIN:FIBONACCI:SPAWN@I")
    )
  )
  (Fnc
    (N# 0)
    (FLP (ALSETQ MAIN:FIBONACCICOORDINATOR:$1 MAIN:FIBONACCI:N@I))
    (FLP_COMPILED
      "D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
      "00 00 00 00 00 00 00 00 00" " T 08 00 00 00 00 00 00 00 00"
      "00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
      " i 00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
    )
  )
  (Var_Ptrs 0 1)
)
)
(Fnc
  (N# 1)
  (FLP (ALSETQ MAIN:FIBONACCICOORDINATOR:$2 MAIN:FIBONACCI:SPAWN@I))
  (FLP_COMPILED
    "D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00 00" " T 08 00 00 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
    " i 00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
  )
  (Var_Ptrs 2 3)
)
)
)
(REM
  "UDF `MAIN:FIBONACCICOORDINATOR' invoke initialization (passing the
  arguments)"
)
)
(CTRL
  (N# 55)
  (OpGroup 2)
  (COP 15)
  (GOSUB 2)
  (REM "UDF `MAIN:FIBONACCICOORDINATOR' call")
)
)
(CTRL
  (N# 56)
  (OpGroup 1)
  (COP 50)
  (dfmput_marshaled_cluster
    (Vars_N#_Ref_Name [Array]
      (0 8 "MAIN:FIBONACCI:TMP__000000000@I")
      (1 22 "MAIN:FIBONACCICOORDINATOR:TMP__000000000@I")
    )
  )
  (Fnc
    (N# 0)
    (FLP
      (ALSETQ
        MAIN:FIBONACCI:TMP__000000000@I
        MAIN:FIBONACCICOORDINATOR:TMP__000000000@I
      )
    )
  )
  (FLP_COMPILED
    "D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00 00" " T 08 00 00 00 00 00 00 00 00"
    "00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
    " i 00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
  )
  (Var_Ptrs 0 1)
)
)
(REM "UDF `MAIN:FIBONACCICOORDINATOR' returned value")
)
)
(CTRL (N# 57) (OpGroup 2) (COP 13) (LEAVE_RECURSION))
(CTRL
  (N# 58)
  (OpGroup 2)
  (COP 16)
  (RETURN)
  (REM "End of UDF `MAIN:FIBONACCI' body")
)
)
(CTRL
  (N# 59)
  (OpGroup 1)
  (COP 50)
  (dfmput_marshaled_cluster
    (Vars_N#_Ref_Name [Array] (0 0 "MAIN:$1") (1 30 "MAIN:N@I"))
  )
  (Fnc
    (N# 0)
    (FLP (SETQ@I MAIN:N@I (+@J 0 MAIN:$1)))
    (FLP_COMPILED
      "D5 01 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
      "00 00 00 00 00 00 00 00 00" "D4 04 00 00 00 00 00 00 00"
      "00 00 00 00 00 00 00 00 00" "01 00 00 00 00 00 00 00 00"
      "D4 BC 00 00 00 00 00 00 00" "02 00 00 00 00 00 00 00 00"
      "03 00 00 00 00 00 00 00 00" " I 00 00 00 00 00 00 00 00 00"
      "00 00 00 00 00 00 00 00 00" " V 00 00 00 00 00 00 00 00 00"
      "01 00 00 00 00 00 00 00 00"
    )
  )
  (Var_Ptrs 1 0)
)
)

```































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2 of 79 - <http://bmdfm.com>















```
tN# 20. IORBPPROC#27(PID=24019,tID=24048) will take care of it. [msg#248]
[SysMsg]: A message received from the pipe at systime Mon Jul 13 11:12:17 20
15.
pipe[IORBPPROC#5]: RESOURCE RELEASE CTRL SEQUENCE has been started on Socket
N# 21. IORBPPROC#5(PID=24019,tID=24026) will take care of it. [msg#249]
[SysMsg]: A message routed out of the NPIPE at systime Mon Jul 13 11:12:17 2
015.
npipe[ExtTaskLd#20]: External Loader/Listener pair (PID=25141/PID=25141,tID=
25143) is detached (logged out) at systime Mon Jul 13 11:12:17 2015. USR_TAS
KJOB_NAME="Fibonacci.flz". [MSG#375]
[SysMsg]: A message routed out of the NPIPE at systime Mon Jul 13 11:12:17 2
015.
npipe[ExtTaskLd#21]: External Loader/Listener pair (PID=25145/PID=25145,tID=
25146) is detached (logged out) at systime Mon Jul 13 11:12:17 2015. USR_TAS
KJOB_NAME="Fibonacci.flz". [MSG#376]
[SysMsg]: A message routed out of the NPIPE at systime Mon Jul 13 11:12:17 2
015.
npipe[ExtTaskLd#0]: Task Loader PID=25126. Connection acknowledged. (VERSION
_BMDPM SYS : "Sancho M. BMDPMsys V5.9.9.") ( COMPILED ON: "Linux RedHatEL56
2VM 2.6.32-220.13.1.el6.x86_64 #1 SMP Thu Mar 29 11:46:40 EDT 2012 x86_64")
( COMPILED BY: "gcc version 4.4.6 20110731 (Red Hat 4.4.6-3) (GCC) as [ELF
64-bit LSB executable, x86-64, version 1 (SYSV), dynamically linked (uses sh
ared libs), for GNU/Linux 2.6.18, stripped] at systime Fri Mar 13 21:14:59 C
ET 2015".) Commenced (logged in) at systime Mon Jul 13 11:12:17 2015. USR_TA
SKJOB_NAME="Fibonacci.flz". [MSG#377]
[SysMsg]: A message routed out of the NPIPE at systime Mon Jul 13 11:12:17 2
015.
npipe[ExtTaskLs#0]: Task Listener PID=25126, tID=25168. Connection acknowledged
ged. (VERSION_BMDPM SYS : "Sancho M. BMDPMsys V5.9.9.") ( COMPILED ON: "Lin
ux RedHatEL562VM 2.6.32-220.13.1.el6.x86_64 #1 SMP Thu Mar 29 11:46:40 EDT 2
012 x86_64") ( COMPILED BY: "gcc version 4.4.6 20110731 (Red Hat 4.4.6-3) (
GCC) as [ELF 64-bit LSB executable, x86-64, version 1 (SYSV), dynamically li
nked (uses shared libs), for GNU/Linux 2.6.18, stripped] at systime Fri Mar 1
3 21:14:59 CET 2015".) Commenced (logged in) at systime Mon Jul 13 11:12:17
2015. USR_TASKJOB_NAME="Fibonacci.flz". [MSG#378]
[SysMsg]: A message received from the pipe at systime Mon Jul 13 11:12:17 20
15.
pipe[IORBPPROC#8]: Initialization has been done on SocketN# 0. [msg#250]
[SysMsg]: A message received from the pipe at systime Mon Jul 13 11:12:17 20
15.
pipe[IORBPPROC#6]: RESOURCE RELEASE CTRL SEQUENCE has been started on Socket
N# 22. IORBPPROC#6(PID=24019,tID=24027) will take care of it. [msg#251]
[SysMsg]: A message routed out of the NPIPE at systime Mon Jul 13 11:12:17 2
015.
npipe[ExtTaskLd#22]: External Loader/Listener pair (PID=25064/PID=25064,tID=
25151) is detached (logged out) at systime Mon Jul 13 11:12:17 2015. USR_TAS
KJOB_NAME="Fibonacci.flz". [MSG#379]
[SysMsg]: A message received from the pipe at systime Mon Jul 13 11:12:17 20
15.
pipe[IORBPPROC#8]: RESOURCE RELEASE CTRL SEQUENCE has been started on Socket
N# 23. IORBPPROC#8(PID=24019,tID=24029) will take care of it. [msg#252]
[SysMsg]: A message routed out of the NPIPE at systime Mon Jul 13 11:12:17 2
015.
npipe[ExtTaskLd#23]: External Loader/Listener pair (PID=25085/PID=25085,tID=
25157) is detached (logged out) at systime Mon Jul 13 11:12:17 2015. USR_TAS
KJOB_NAME="Fibonacci.flz". [MSG#380]
[SysMsg]: A message received from the pipe at systime Mon Jul 13 11:12:17 20
15.
pipe[IORBPPROC#0]: RESOURCE RELEASE CTRL SEQUENCE has been started on Socket
N# 24. IORBPPROC#0(PID=24019,tID=24039) will take care of it. [msg#253]
[SysMsg]: A message routed out of the NPIPE at systime Mon Jul 13 11:12:17 2
015.
npipe[ExtTaskLd#24]: External Loader/Listener pair (PID=25090/PID=25090,tID=
25159) is detached (logged out) at systime Mon Jul 13 11:12:17 2015. USR_TAS
KJOB_NAME="Fibonacci.flz". [MSG#381]
[SysMsg]: A message received from the pipe at systime Mon Jul 13 11:12:17 20
15.
pipe[IORBPPROC#9]: RESOURCE RELEASE CTRL SEQUENCE has been started on Socket
N# 25. IORBPPROC#9(PID=24019,tID=24030) will take care of it. [msg#254]
[SysMsg]: A message routed out of the NPIPE at systime Mon Jul 13 11:12:17 2
015.
npipe[ExtTaskLd#25]: External Loader/Listener pair (PID=25101/PID=25101,tID=
25163) is detached (logged out) at systime Mon Jul 13 11:12:17 2015. USR_TAS
KJOB_NAME="Fibonacci.flz". [MSG#382]
[SysMsg]: A message received from the pipe at systime Mon Jul 13 11:12:17 20
15.
pipe[PROCstat]: LOAD AVERAGE within the systime period [Mon Jul 13 11:12:08
2015; Mon Jul 13 11:12:19 2015] {io1309/50000|db1324/150000|oql90/20000|cu
9/32<49} 73% utilization. RECENT PEAK LOAD: [PROCstat]: Jul 13 11:12:17 {io2
900/50000|db1649/150000|oql37/20000|cpu32<23} 81% utilization [msg#256]
```

```
Console input: get count
[SysMsg]: ===== System time is Mon Jul 13 11:12:19 2015. =====
[SysMsg]: Sending SIG GET TIME to the CPU PROCs...
pipe[CPUPROC#0]: USRs=234, USRs=910000, SYSs=10, SYSs=940000.
[SysMsg]: Sending SIG GET TIME to the QO PROCs...
pipe[QOPROC#0]: USRs=4, USRs=580000, SYSs=7, SYSs=820000.
[SysMsg]: Sending SIG GET TIME to the IORBP PROCs...
pipe[IORBPPROC#0]: USRs=6, USRs=210000, SYSs=4, SYSs=730000.
[SysMsg]: **** GENERAL PERFORMANCE BENCHMARKS ****
[SysMsg]: WARNING: CPU PROC is multithreaded, thread times are not measured
separately.
[SysMsg]: WARNING: QO PROC is multithreaded, thread times are not measured s
eparately.
[SysMsg]: WARNING: IORBP PROC is multithreaded, thread times are not measure
d separately.
[Msg]: All times are given in seconds below.
[DFMSrv]: "CPU PROCs" concurrency factor:
[DFMSrv]: 2.458500000000E+02/2.458500000000E+02=1.000000000000E+00.
[DFMSrv]: Only for real SMP (cNUMA) system with enough of CPUs:
[DFMSrv]: 2.458500000000E+02/1.000000000000E+01=2.458500000000E+01.
[DFMSrv]: Parallelizing index:
[DFMSrv]: 2.334000000000E+01/1.240000000000E+01=1.882258064516E+00.
[DFMSrv]: Overall reached concurrency for the BM DFM Server:
[DFMSrv]: 2.691900000000E+02/2.458500000000E+02=1.09435593647E+00.
[DFMSrv]: Estimation of the "CPU PROCs" runtime workload:
[DFMSrv]: Abs. time range: min=2.458500000000E+02, max=2.458500000000E+02.
[DFMSrv]: Square root of dispersion = 0.000000000000E+00.
[DFMSrv]: Normalized standard deviation = 0.000000000000E+00 (0.00%).
[DFMSrv]: Estimation of the "QO PROCs" runtime workload:
[DFMSrv]: Abs. time range: min=1.240000000000E+01, max=1.240000000000E+01.
[DFMSrv]: Square root of dispersion = 0.000000000000E+00.
[DFMSrv]: Normalized standard deviation = 0.000000000000E+00 (0.00%).
```

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```
[DFMSrv]: Estimation of the "IORBP PROCs" runtime workload:
[DFMSrv]: Abs. time range: min=1.094000000000E+01, max=1.094000000000E+01.
[DFMSrv]: Square root of dispersion = 0.000000000000E+00.
[DFMSrv]: Normalized standard deviation = 0.000000000000E+00 (0.00%).
```

```
Console input: down down
[SysMsg]: ===== System time is Mon Jul 13 11:12:36 2015. =====
[SysMsg]: Now, the BM DFM Server is urgently going down...
[SysMsg]: Destroying the external connection file...
[SysMsg]: Destroying the ExtTask(Trace) nFIFO pipe...
[SysMsg]: Sending SIGINT to ExtTasks in TCZ...
[SysMsg]: Sending SIGTERM to ExtTasks in TCZ...
[SysMsg]: Sending SIGKILL to ExtTasks in TCZ...
[SysMsg]: Sending SIGKILL to ExtTraces in TPA...
[SysMsg]: Sending SIGKILL to the PROCstat...
[SysMsg]: Sending SIGKILL to the CPU PROCs...
[SysMsg]: Sending SIGKILL to the QO PROCs...
[SysMsg]: Sending SIGKILL to the IORBP PROCs...
[SysMsg]: Invoking taskjob_end callback(...)
[SysMsg]: Deinitializing BM DFM...
[DFMSrv]: Release semaphores done.
[DFMSrv]: Close msg PROC pipe done.
[SysMsg]: Closing the PROC logs "PROCs2.log"...
[DFMSrv]: *** PROC logfile is closed at systime Mon Jul 13 11:12:36 2015 ***
[MemPool]: The shared memory pool deinit done.
[SysMsg]: Destroying the freeIPC EMERGENCY CASE file...
[SysMsg]: SHUTDOWN completed at systime Mon Jul 13 11:12:36 2015.
[Legacy_MainFrame_Final_Message]: GOOD BYE.
[SysMsg]: Closing the logs "BMDMsrv2.log"...
*** Logfile is closed at systime Mon Jul 13 11:12:36 2015 ***
```

```
tty3$ # list PROCs2.log
tty3$ cat PROCs2.log
```

```
*** PROC logfile is opened at systime Mon Jul 13 11:11:52 2015 ***
[CPUPROC#31]: BEGIN at (sec=1436778727, usec=980743)
TaskCntx_ID = #500
FstLispCode = `(SETQ@S MAIN:TMP_000000001 "")'
AddressRefs = <433:0:0>[0] (#0){CHRO} /* MAIN:TMP_000000001 */
OutputOrder = {(Ld), 0(Ls)}
END_OF_CPUPROC_ENTRY at (sec=1436778727, usec=981074)
[CPUPROC#30]: BEGIN at (sec=1436778727, usec=981272)
TaskCntx_ID = #500
FstLispCode = `(SETQ@I MAIN:LINEs TERM@I 24)'
AddressRefs = <169:0:0>[0] (#0){SLO8} /* MAIN:LINEs TERM@I */
END_OF_CPUPROC_ENTRY at (sec=1436778727, usec=981371)
[CPUPROC#27]: BEGIN at (sec=1436778727, usec=981463)
TaskCntx_ID = #500
FstLispCode = `(SETQ@I MAIN:COLUMNs TERM@I 80)'
AddressRefs = <9:0:0>[0] (#0){SLO8} /* MAIN:COLUMNs TERM@I */
END_OF_CPUPROC_ENTRY at (sec=1436778727, usec=981523)
[CPUPROC#29]: BEGIN at (sec=1436778727, usec=980900)
TaskCntx_ID = #500
FstLispCode = `(SETQ@S MAIN:TERM TYPE@S "xterm")'
AddressRefs = <374:0:0>[0] (#0){CHRS} /* MAIN:TERM TYPE@S */
END_OF_CPUPROC_ENTRY at (sec=1436778727, usec=981376)
[CPUPROC#29]: BEGIN at (sec=1436778727, usec=981927)
TaskCntx_ID = #500
FstLispCode = `(SETQ@S MAIN:CLRSR TERM@S "\e[H\e[2J")'
AddressRefs = <6:0:0>[0] (#0){CHR7} /* MAIN:CLRSR TERM@S */
END_OF_CPUPROC_ENTRY at (sec=1436778727, usec=982019)
[IORBPPROC#25]: BEGIN at (sec=1436778727, usec=982227)
TaskCntx_ID = #500
SrcAddrRefs = <478:0:0>[0] (#0) = "42n" /* MAIN:TMP_000000002@S */
END_OF_IORBPPROC_ENTRY at (sec=1436778727, usec=982332)
[CPUPROC#30]: BEGIN at (sec=1436778727, usec=981976)
TaskCntx_ID = #500
FstLispCode = `(SETQ@S MAIN:REVERSE TERM@S "\e(7m")'
AddressRefs = <291:0:0>[0] (#0){CHR4} /* MAIN:REVERSE TERM@S */
END_OF_CPUPROC_ENTRY at (sec=1436778727, usec=982518)
[CPUPROC#26]: BEGIN at (sec=1436778727, usec=982676)
TaskCntx_ID = #500
FstLispCode = `(SETQ@I MAIN:N@I (IVAL@S MAIN:TMP_000000002@S))'
AddressRefs = <213:0:0>[0] (#0){SLO8}, <478:0:0>[0] (#0){CHR3} /* MAIN:N@I,
MAIN:TMP_000000002@S */
END_OF_CPUPROC_ENTRY at (sec=1436778727, usec=982746)
[CPUPROC#24]: BEGIN at (sec=1436778727, usec=982828)
TaskCntx_ID = #500
FstLispCode = `(SETQ@S MAIN:BLINK TERM@S "\e(5m")'
AddressRefs = <1:0:0>[0] (#0){CHR4} /* MAIN:BLINK TERM@S */
END_OF_CPUPROC_ENTRY at (sec=1436778727, usec=982918)
[CPUPROC#22]: BEGIN at (sec=1436778727, usec=982912)
TaskCntx_ID = #500
FstLispCode = `(SETQ@S MAIN:BOLD TERM@S "\e(1m")'
AddressRefs = <4:0:0>[0] (#0){CHR4} /* MAIN:BOLD TERM@S */
END_OF_CPUPROC_ENTRY at (sec=1436778727, usec=982983)
```

```
[CPUPROC#7]: BEGIN at (sec=1436778737, usec=878607)
TaskCntx_ID = #627
FstLispCode = `(SETQ@I MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000000@I (+@J
MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000001@I
MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000002@I))'
AddressRefs = <15:6:1>[0] (#38){SLO8}, <16:6:1>[0] (#38){SLO8},
<17:6:1>[0] (#38){SLO8} /* MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000000@I,
MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000001@I,
MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000002@I */
END_OF_CPUPROC_ENTRY at (sec=1436778737, usec=878651)
[CPUPROC#25]: BEGIN at (sec=1436778737, usec=878782)
TaskCntx_ID = #627
FstLispCode = `(ALSETQ MAIN:FIBONACCICOORDINATOR:TMP_000000002@I
MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000000@I)'
AddressRefs = <23:1:2>[0] (#65){SLO8}, <15:6:1>[0] (#38){SLO8} /*
MAIN:FIBONACCICOORDINATOR:TMP_000000002@I,
MAIN:FIBONACCICOORDINATOR:SHADOW:TMP_000000000@I */
END_OF_CPUPROC_ENTRY at (sec=1436778737, usec=878806)
[CPUPROC#18]: BEGIN at (sec=1436778737, usec=878885)
TaskCntx_ID = #627
FstLispCode = `(SETQ@I MAIN:FIBONACCICOORDINATOR:TMP_000000000@I (+@J
MAIN:FIBONACCICOORDINATOR:TMP_000000001@I
MAIN:FIBONACCICOORDINATOR:TMP_000000002@I))'
AddressRefs = <21:1:2>[0] (#65){SLO8}, <22:1:2>[0] (#65){SLO8},
<23:1:2>[0] (#65){SLO8} /* MAIN:FIBONACCICOORDINATOR:TMP_000000000@I,
MAIN:FIBONACCICOORDINATOR:TMP_000000001@I,
MAIN:FIBONACCICOORDINATOR:TMP_000000002@I */
END_OF_CPUPROC_ENTRY at (sec=1436778737, usec=878910)
[CPUPROC#1]: BEGIN at (sec=1436778737, usec=878982)
TaskCntx_ID = #627
```

```

FstLispCode = `(ALSETQ MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000002@I
MAIN:FIBONACCICOORDINATOR:TMP__000000000@I)'
  AddressRefs = <17:22:0>[0] (#22){SLO8}, <21:1:2>[0] (#65){SLO8} /*
MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000002@I,
MAIN:FIBONACCICOORDINATOR:TMP__000000000@I */
END_OF_CPUPROC_ENTRY at (sec=1436778737, usec=879011)
[CPUPROC#17]: BEGIN at (sec=1436778737, usec=879117)
  TaskCntx_ID = #627
  FstLispCode = `(SETQ@I MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000000@I (+@J
MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000001@I
MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000002@I))'
  AddressRefs = <15:22:0>[0] (#22){SLO8}, <16:22:0>[0] (#22){SLO8},
<17:22:0>[0] (#22){SLO8} /* MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000000@I,
MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000001@I,
MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000002@I */
END_OF_CPUPROC_ENTRY at (sec=1436778737, usec=879140)
[CPUPROC#8]: BEGIN at (sec=1436778737, usec=879271)
  TaskCntx_ID = #627
  FstLispCode = `(ALSETQ MAIN:FIBONACCICOORDINATOR:TMP__000000002@I
MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000000@I)'
  AddressRefs = <23:1:0>[0] (#1){SLO8}, <15:22:0>[0] (#22){SLO8} /*
MAIN:FIBONACCICOORDINATOR:TMP__000000002@I,
MAIN:FIBONACCICOORDINATOR:SHADOW:TMP__000000000@I */
END_OF_CPUPROC_ENTRY at (sec=1436778737, usec=879304)
[CPUPROC#6]: BEGIN at (sec=1436778737, usec=879685)
  TaskCntx_ID = #627
  FstLispCode = `(SETQ@I MAIN:FIBONACCICOORDINATOR:TMP__000000000@I (+@J
MAIN:FIBONACCICOORDINATOR:TMP__000000001@I
MAIN:FIBONACCICOORDINATOR:TMP__000000002@I))'
  AddressRefs = <21:1:0>[0] (#1){SLO8}, <22:1:0>[0] (#1){SLO8},
<23:1:0>[0] (#1){SLO8} /* MAIN:FIBONACCICOORDINATOR:TMP__000000000@I,
MAIN:FIBONACCICOORDINATOR:TMP__000000001@I,
MAIN:FIBONACCICOORDINATOR:TMP__000000002@I */
END_OF_CPUPROC_ENTRY at (sec=1436778737, usec=879723)
[CPUPROC#21]: BEGIN at (sec=1436778737, usec=879942)
  TaskCntx_ID = #627
  FstLispCode = `(ALSETQ MAIN:FIBONACCI:TMP__000000000@I
MAIN:FIBONACCICOORDINATOR:TMP__000000000@I)'
  AddressRefs = <7:1:0>[0] (#1){SLO8}, <21:1:0>[0] (#1){SLO8} /*
MAIN:FIBONACCI:TMP__000000000@I, MAIN:FIBONACCICOORDINATOR:TMP__000000000@I */
END_OF_CPUPROC_ENTRY at (sec=1436778737, usec=879972)
[CPUPROC#30]: BEGIN at (sec=1436778737, usec=880218)
  TaskCntx_ID = #627
  FstLispCode = `(ALSETQ MAIN:TMP__000000001 MAIN:FIBONACCI:TMP__000000000@I)'
  AddressRefs = <39:1:0>[0] (#1){SLO8}, <7:1:0>[0] (#1){SLO8} /*
MAIN:TMP__000000001, MAIN:FIBONACCI:TMP__000000000@I */
END_OF_CPUPROC_ENTRY at (sec=1436778737, usec=880268)
[CPUPROC#12]: BEGIN at (sec=1436778737, usec=880358)
  TaskCntx_ID = #627
  FstLispCode = `(SETQ@S MAIN:TMP__000000001 (OUTF (PRN_STRING_FMT) (CAT ""
MAIN:TMP__000000001)))'
  AddressRefs = <39:2:0>[0] (#2){CHR8}, <39:1:0>[0] (#1){SLO8} /*
MAIN:TMP__000000001, MAIN:TMP__000000001 */
  OutputOrder = {1(Ls)}
END_OF_CPUPROC_ENTRY at (sec=1436778737, usec=880426)
*** PROC logfile is closed at systime Mon Jul 13 11:12:36 2015 ***

```

```

tty1$ # stop multiple instances of CommChannel
tty1$ killall CommChannel; rm BMDFMpipe*

```



# <EOF>